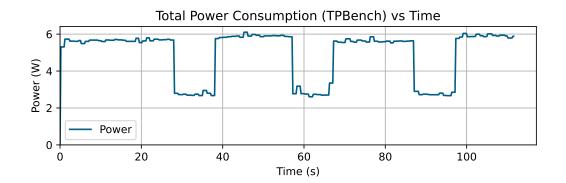
ECE 361E: Homework 2

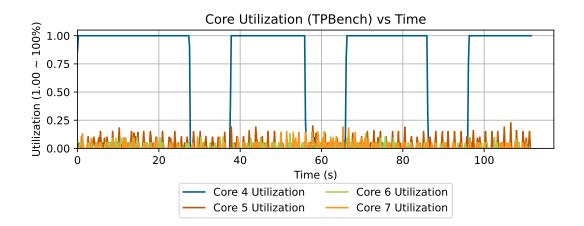
Sidharth Babu, SNB2593 Tianda Huang, TH32678

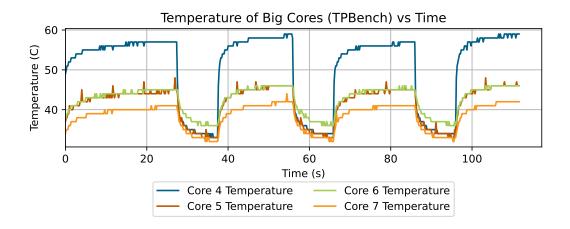
February 12, 2023

1 Problem 1

1.1 Question 1



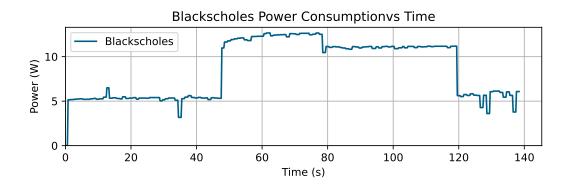


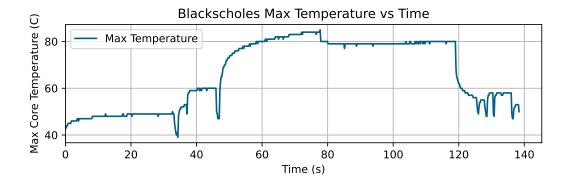


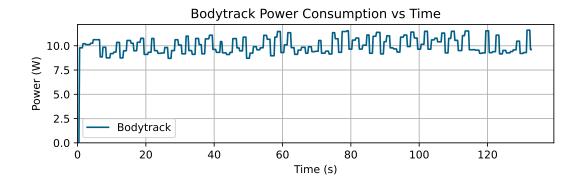
1.2 Question 2

We can identify four major phases of the benchmark's execution, marked by sharp rises in temperature (and also power and utilization).

1.3 Question 3







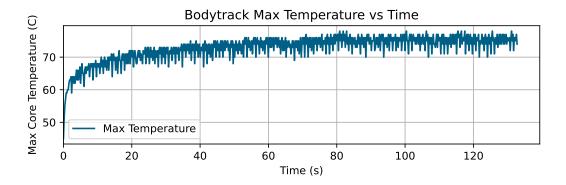


Figure 1: blackscholes and bodytrack Benchmark Results Table

Benchmark	Run time [s]	Avg. power [W]	Avg. max temp [°C]	Max temp [°C]	Energy [J]
black scholes	138.41	8.55	66.21	85.00	42.60
bodytrack	132.78	9.97	72.77	78.0	49.62

2 Problem 2

2.1 Question 1

Based on the near-perfect performance metrics and confusion matrix (i.e. only one missed prediction per testfile), the performance of the classifier could be considered 'excellent'. This is likely due to the 'total_watts' parameter that represents a near-perfect metric of big-core wattage since we roughly have

total W \approx big-core W + little-core W + gpu W + memory W

Figure 2: blackscholes and bodytrack Confusion Matrix

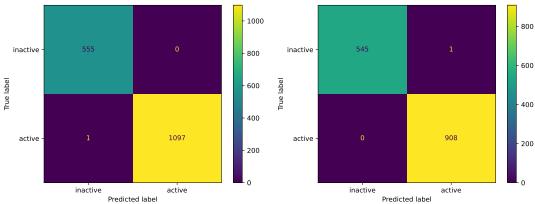
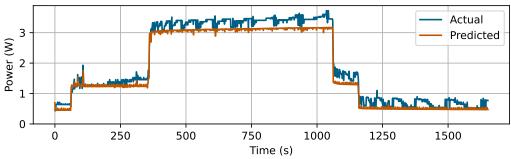


Figure 3: blackscholes and bodytrack SVM Model Performance Metrics

Benchmark	Accuracy	Precision	Recall	F1-Score
black scholes	0.999	1.000	0.999	1.000
bodytrack	0.999	0.999	1.000	0.999

2.2 Question 2

True vs. Predicted Power Values for BlackScholes Test



True vs. Predicted Power Values for BodyTrack Test

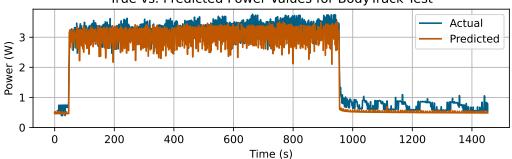
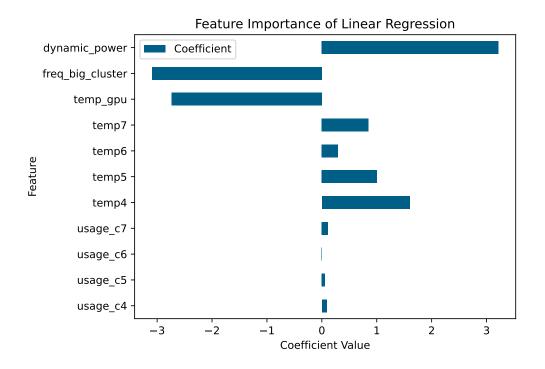


Figure 4: Wattage Linear Regression Model Performance

Dataset	training	black scholes	body track
\mathbb{R}^2	0.987	0.957	0.922
MSE	0.010	0.059	0.122

2.3 Question 3

Dynamic power, big cluster frequency, and GPU temperature are the features with the highest magnitude (and thus the largest contribution to the linear regressor). We can see that in general, frequency and its derived parameter dynamic power are the best features, followed by temperature, then followed by usage. This is likely due to dynamic power being a major component in total power draw. Temperature is an effect of increased power draw and thus lags behind when observing the data; this means that temperature is a relatively good feature.



3 Problem 3

3.1 Question 1

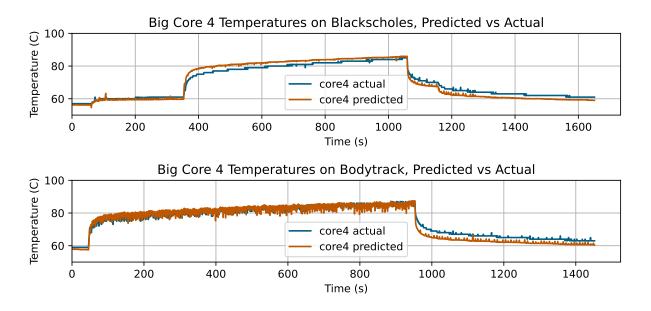


Figure 5: Temperature MLPRegressor MSE (C^2)

Dataset	Test MSE (Core 4)	Test MSE (Core 5)	Test MSE (Core 6)	Test MSE (Core 7)
black scholes	5.18	6.11	6.22	3.78
bodytrack	5.76	7.24	7.42	6.84

3.2 Question 2

The first major technique for improving performance is by including more past temperature data when estimating the next temperature step. As the processor elements contain some thermal mass, temperature will continue to rise even as power drops, and fast spikes in usage and power may not be reflected in a temperature increase in the very next time step. Thus, by including more past temperatures in calculating the next, we can account for this delay in temperature response.

Second, from a model perspective, increasing the number of nodes in the MLP can marginally increase the performance.

3.3 BONUS Question 3

An on-demand governor such as the PI-controller listed can save more energy at the cost of an increase in runtime. A higher temperature decreases efficiency in the MCU due to higher static power and increased resistances, resulting in worse performance-per-watt metrics. However, a lower temperature ceiling results in lower total performance, which increases runtime.

The tradeoff between better performance/runtime and better performance-per-watt cannot be simply solved by reducing temperature as much as possible: past a certain point, the increased time from a lower temperature/power draw results in higher total energy compared to the inefficient power/high performance case.

4 Contributions and Valuable Things Learned

Both group members, Sidharth Babu and Tianda Huang, contributed an equal amount of work due to working on the entire project together.

We learned about the power and temperature implications of running high workloads on an single-package SOC, including the temperature-bleed effects of running cores/functional units on physically adjacent cores/functional units.

On the data science and machine learning side, we learned about the importance of scaling input features (linearly and non-linearly) regarding achieving maximum performance from a model.