UMASH: a fast almost universal 64-bit string hash

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UMASH is a string hash function with throughput—22 GB/s on a 2.5 GHz Xeon 8175M—and latency—9-20 ns for input sizes up to 64 bytes on the same machine—comparable to that of performance-optimised hashes like MurmurHash3, XXH3, or farmhash. Its 64-bit output is almost universal (see below for collision probability), and it, as well as both its 32-bit halves, passes both Reini Urban's fork of SMHasher and Yves Orton's extended version (after expanding each seed to a full 320-byte key for the latter).

The universal collision bounds for UMASH hold under the assumption that independent keys are fully re-generated with random data. While UMASH accepts a regular 64-bit "seed" value, it is merely a hint that we would like to see a different set of hash values, without any guarantee. Nevertheless, this suffices to pass SMHasher's seed-based collision tests. For real guarantees, expand short keys into full UMASH keys with a stream cipher like Salsa20.

UMASH should not be used for cryptographic purposes, especially against adversaries that can exploit side-channels or adapt to the hashed outputs, but does guarantee worst-case pair-wise collision bounds. For any two strings of s bytes or fewer, the probability of them hashing to the same value satisfies $\varepsilon < \lceil s/4096 \rceil \cdot 2^{-55}$, as long as the key is generated uniformly at random. However, once a few collisions have been identified (e.g., through a timing side-channel in a hash table), the linearity of the hash function makes it trivial to combine them into exponentially more colliding values.

Overview of the UMASH hash function

The structure of UMASH on strings of 9 or more bytes closely follows that of Dai and Krovetz's VHASH, with parameters weakened to improve speed at the expense of collision rate, and the NH block compressor replaced with a (unfortunately) bespoke hybrid compressor OH (OH because O is the letter between N and P).

UMASH splits an input string into a sequence of blocks M; the first-level OH hash

further decomposes each block \mathbf{M}_i of 256 bytes into m, a sequence of 16-byte chunks. The final block may be short, in which case the blocking logic may include redundant data to ensure that the final block's size is a multiple of 16 bytes.

Each block is compressed to 16 bytes with OH. The resulting outputs are then fed to a Carter-Wegman polynomial hash function, and the accumulator reversibly finalised to obtain a UMASH value.

Note that, while the analysis below assumes a modulus of $M_{61} = 2^{61} - 1$ for the polynomial hash, the implementation actually works in $2^{64} - 8 = 8 \cdot (2^{61} - 1)$. This does not worsen the collision probability, but obviously affects the exact hash values computed by the function.

The first-level compression function is sampled from the OH family with word size w=64 bits and a block size of 256 bytes.

That compression function relies on PH, the equivalent of NH in 128-bit carry-less arithmetic for all but the last iteration, which instead mixes the remaining 16-byte chunk with NH.

The core PH construction has been re-derived and baptised multiple times, recently as CLNH by Lemire and Kaser; Bernstein dates it back to 1968. Compared to NH, PH offers higher throughput on higher-end contemporary cores, but worse latency; that's why OH switches to NH for the last iteration.

For a given 16-byte chunk m_i , we parameterised PH on a 16-byte key k_i as

$$\mathtt{PH}_{k_i}(m_i) = (m_i^{hi} \oplus k_i^{hi}) \odot (m_i^{lo} \oplus k_i^{lo}),$$

where \cdot^{hi} is the high 8-byte (64-bit) half of a 16-byte value, \cdot^{lo} the low 8-byte half, and \odot is a multiplication in 128-bit carry-less arithmetic. This family of hash functions mixes pairs of w=64 bit values into 2w-bit hashes that are $(2^{-w}=2^{-64})$ -almost-XOR-universal.

This last property means that, not only are two different 16-byte chunks unlikely to collide (i.e., $P[\mathtt{PH}_{k_i}(x) = \mathtt{PH}_{k_i}(y)] \leq 2^{-64}$ for $x \neq y$), but in fact any specific difference $\Delta_{\mathtt{XOR}}$ is equally unlikely:

$$P[\mathsf{PH}_{k_i}(x) \oplus \mathsf{PH}_{k_i}(y) = \Delta_{\mathsf{XOR}}] \leq 2^{-64}.$$

Almost-XOR-universality lets us combine multiple PH-mixed values with bitwise xor (\oplus) while preserving 2^{-64} -almost-XOR-universality for the final result.

The final NH step is similar, except in mixed-width modular arithmetic:

$$NH_{k_i}(m_j) = (m_i^{hi} +_{64} k_i^{hi}) \cdot_{128} (m_i^{lo} +_{64} k_i^{lo}),$$

where $+_{64}$ denotes 64-bit modular addition, and \cdot_{128} a full $64 \times 64 \rightarrow 128$ -bit multiplication.

The NH family of hash function mixes 128-bit (16-byte) values into 128-bit hashes that are 2^{-64} -almost- Δ -unversal: For any $x \neq y$, every specific difference Δ between NH_k(x) and NH_k(y) satisfies

$$P[PH_{k_i}(x) -_{128} PH_{k_i}(y) = \Delta] \le 2^{-64},$$

where $-_{128}$ denotes modular 128-bit subtraction.

Let m be a block of $n \ge 1$ 16-byte chunks, and t an arbitrary 128-bit tag; the OH hash of m and t for a given parameter vector k is

$$\mathtt{OH}_k(m) = \left(igoplus_{i=1}^{n-1} \mathtt{PH}_{k_i}(m_i)
ight) \oplus \left(\mathtt{NH}_{k_n}(m_n) +_{128} t
ight).$$

We will use the tag t to encode the initial block size, before expansion to an integral number of chunks, and thus prevent length extension attacks.

Having defined OH, we must now show that it is a universal hash for non-empty vectors of up to 16 chunks of 16 bytes each, and that a Toeplitz extension with a 2-chunk (4-word) shift independently satisfies the same collision bound. When doing so, we must also remember to take into account the public nature of the procedure we use to expand the final block to full 16-byte chunks.

Since we only expand the final block to a round number of chunks, but not necessarily to a full block of 16 chunks, we must split our analysis of the universality bound in two main cases:

- input x comprises more chunks than y (which symmetrically covers the opposite case)
- the two differing inputs x and y expand to the same number of chunks

In the first case, with different sizes, we assume without loss of generality that x comprises more 16-byte chunks that y, i.e., |x| > |y|.

Let $h_y = \mathrm{OH}_k(y)$ be the hash value for y, given a fixed vector of parameters k. We wish to bound the probability that $h_x = \mathrm{OH}_k(x) = h_y$; we can easily do so by noting that h_y only considered the first |y| < |x| 16-byte parameters in k. This means the last NH step in h_x is independent of h_y . Let n = |x| and t_x be the tag for the initial byte size of x, before expansion to complete chunks; for h_x to equal h_y , we must find

$$\left(\bigoplus_{i=1}^{n-1} \mathrm{PH}_{k_i}(x_i)\right) \oplus \left(\mathrm{NH}_{k_n}(x_n) +_{128} t_x\right) = h_y$$

$$\Leftrightarrow \quad \mathtt{NH}_{k_n}(m_n) = (h_y -_{128} t_x) \oplus \bigoplus_{i=1}^{n-1} \mathtt{PH}_{k_i}(x_i)$$

The parameters for the left-hand side NH step are independent of everything on the right-hand side. The probability of a collision is thus equal to the probability that a randomly parameterised NH yields an arbitrary 128-bit value. The most likely value for NH is 0, with probability 2^{w-1} , i.e., 2^{-63} in our case (w = 64).

The probability of a collision between blocks of different lengths is thus at most 2^{-63} .

For the Toeplitz extension case, we can similarly note that the parameters for the final NH step in x are independent of the extension hash for y and of everything in the collision bound above. We can thus apply the same reasoning, $mutatis\ mutandis$, for the case where we reuse all but the first (or any other strictly positive number) chunks in the parameter vector k and backfill more independently generated parameters at the end.

When the two blocks x and y are expanded to a different number of chunks, we find that OH collides with probability at most 2^{-63} , and that computing a second OH that reuses all but the first parameter chunk gives us another independent collision probability of at most 2^{-63} .

We now have to handle the case when the two blocks differ, but expand the same number of chunks |x| = |y|. The expansion function is public, so it's trivial to construct messages that differ, but are expand to the same block. That's why we encode the original byte size of each block in the tags t_x and t_y .

Assume x = y after expansion. Their sizes must differ, and thus $t_x \neq t_y$. In that case,

$$\mathtt{OH}_k(x) = \left(\bigoplus_{i=1}^{n-1} \mathtt{PH}_{k_i}(x_i)\right) \oplus \left(\mathtt{NH}_{k_n}(x_n) +_{128} t_x\right),$$

and, since x = y,

$$\mathtt{OH}_k(y) = \left(\bigoplus_{i=1}^{n-1} \mathtt{PH}_{k_i}(x_i)\right) \oplus (\mathtt{NH}_{k_n}(x_n) +_{128} t_y),$$

The two expressions are identical, except for $t_x \neq t_y$, and the two hashes thus always differ, regardless of the parameters k.

Finally, we're left with the classic case, $x \neq y$ and n = |x| = |y|, and no constraint on the tags t_x and t_y .

Due to the heterogeneous nature of OH, we must further examine two subcases:

- the only difference between x and y is in the last chunk $x_n \neq y_n$
- there is a difference in the first n-1 chunks (and the last chunks may still differ)

When only the last chunks differ, the PH iterations in

$$\mathtt{OH}_k(x) = \left(igoplus_{i=1}^{n-1} \mathtt{PH}_{k_i}(x_i)
ight) \oplus \left(\mathtt{NH}_{k_n}(x_n) +_{128} t_x
ight),$$

and

$$\mathtt{OH}_k(y) = \left(\bigoplus_{i=1}^{n-1} \mathtt{PH}_{k_i}(x_i)\right) \oplus \left(\mathtt{NH}_{k_n}(y_n) +_{128} t_y\right),$$

are identical, since $\forall i < n, x_i = y_i$. The two hashes differ iff the ENH iterations NH() $+_{128} t$ differ. The ENH family is 2^{-w} -almost-universal (this directly follows from the 2^{-w} -almost- Δ -universality of NH).

We only considered the last parameter chunk in k, so the same reasoning applies independently for any Toeplitz extension shifted by at least one chunk.

In the last remaining subcase, we have a difference in the first n-1 chunks that are hashed with PH. The ENH iterations are independent of the first n-1 parameters, so we can directly rely on the 2^{-w} -almost-XOR-universality of the PH iterations to find a collision probability of $2^{-w}=2^{-64}$ or less.

We can also extend the Toeplitz extension proof of Krovetz for this case. Assume without loss of generality (apparently...) that the last PH chunk x_{n-1} and y_{n-1} differ. As long as the extension is shifted by 2 or more chunks, the parameters for that PH iteration are independent of the unshifted OH hash values; we can thus derive a collision bound of at most $2^{-w}=2^{-64}$ for the 2^{-w} -almost-XOR-universality of PH.

We had to enumerate a few cases, but we do find a collision probability of at most 2^{-63} for OH, and the same independent probability for a Toeplitz extended version, as long as the extension skips two or more 16-byte parameter chunks.

The second level is a Carter-Wegman polynomial hash in $\mathbb{F} = \mathbb{Z}/(2^{61} - 1)\mathbb{Z}$ that consumes one 64-bit half of the OH output at a time. In other words, given the *i*th OH-compressed value $OH_k(\mathbf{M}_i)$, we consume its 128-bit integer value by separately feeding $OH_k(\mathbf{M}_i)$ mod 2^{64} and $|OH_k(\mathbf{M}_i)/2^{64}|$ to the polynomial hash.

Let $n=|\mathbf{M}|$ be the number of OH blocks in the input, and y_j , for j< d=2n, be the stream of alternating low $(\mathrm{OH}_k(\mathbf{M}_{j/2}) \bmod 2^{64})$ and high

 $(\lfloor \mathtt{OH}_k(\mathbf{M}_{(j-1)/2})/2^{64} \rfloor)$ 64-bit halves from OH's outputs. The polynomial hash is parameterised on a single multiplier $f \in \mathbb{F}$ and evaluates to

$$CW_f(y) = \left(\sum_{j=0}^{d-1} y_j \cdot f^{d-j}\right) \mod 2^{61} - 1,$$

a polynomial of degree d=2n, twice the number of OH blocks.

Two streams of at most d half-values y and y' that differ in at least one place will evaluate to the same hash $CW_f(y) = CW_f(y')$ with probability at most $d/(2^{61}-1)$, as long as the multiplier f is uniformly chosen at random. In practice, we will forbid the weak multiplier 0, bringing the collision probability to $\varepsilon_{\mathbb{F}} < d/(2^{61}-2)...$ pretty much the same thing.

We will also simplify the software implementation by actually evaluating that polynomial in the ring $\mathbb{Z}/(2^{64}-8)\mathbb{Z}$, which contains \mathbb{F} as a subfield: $2^{64}-8=8\cdot(2^{61}-1)$. This cannot worsen the collision probability, since we could always reduce the result mod $2^{61}-1$ after the fact.

The last step is a finalizer that reversibly mixes the mod $2^{64} - 8$ polynomial hash value to improve its distribution and pass SMHasher.

Where do collisions come from?

Strings of 8 or fewer bytes are converted to 64-bit integers and passed to a mixing routine based on SplitMix64, with the addition of a random parameter that differs for each input size $s \in [0,8]$. The result is universal, never collides values of the same size, and otherwise collides values with probability $\varepsilon_{\rm short} \approx 2^{-64}$.

There's nothing special about SplitMix64, except that it's well known, satisfies SMHasher's bias and avalanche tests, and is invertible. Similarly, we use an invertible xor-rot finaliser in the general case to satisfy SMHasher without impacting the collision bounds.

Longer strings of 9 or more bytes feed the result of OH to the polynomial hash in $\mathbb{F}=\mathbb{Z}/(2^{61}-1)\mathbb{Z}$. When the input is of medium size $s\in[9,16)$, it is expanded to 16 bytes by redundantly reading the first and last 8 bytes. In any case, the small modulus means polynomial hashing disregards slightly more than 6 bits off each OH output. This conservatively yields a collision probability less than 2^{-56} from OH; we prevent extension attacks by making OH consume the block's original byte length.

That's quickly dominated by the collision probability for the polynomial hash, $\varepsilon_{\mathbb{F}} < d/(2^{-61}-2)$, where $d=2\lceil s/256\rceil$. The worst-case collision probability is thus safely less than $\lceil s/4096\rceil \cdot 2^{-55}$. That's still universal with collision probability $\varepsilon_{\mathrm{long}} < 2^{-40}$ for strings of 128 MB or less.

Applications that need stronger guarantees should compute two independent UMASH values (for a total of 128 bits), while recycling most of the OH key array with a Toeplitz extension. A C implementation that simply fuses the OH inner loops achieves a throughput of 11 GB/s and short-input latency of 9-21 ns on a 2.5 GHz Xeon 8175M, more than twice as fast as classic options like hardware-accelerated SHA-256, SipHash-2-4, or even SipHash-1-3.

Reference UMASH implementation in Python

```
from collections import namedtuple
from functools import reduce
import random
import struct

# We work with 64-bit words
W = 2 ** 64

# We chunk our input 16 bytes at a time
CHUNK_SIZE = 16

# We work on blocks of 16 chunks
BLOCK_SIZE = 16
```

Carry-less multiplication

Addition in the ring of polynomials over GF(2) is simply bitwise xor. Multiplication is slightly more complex to describe in sofware. The code below shows a pure software implementation; in practice, we expect to use hardware carry-less multiplication instructions like CLMUL on x86-64 or VMULL on ARM.

While non-cryptographic hash functions have historically avoided this operation, it is now a key component in widely used encryption schemes like AES-GCM. We can expect a hardware implementation to be available on servers and other higher end devices.

```
def gfmul(x, y):
    """Returns the 128-bit carry-less product of 64-bit x and y."""
    ret = 0
    for i in range(64):
        if (x & (1 << i)) != 0:
            ret ^= y << i
        return ret</pre>
```

Key generation

A UMASH key consists of one multiplier for polynomial hashing in $\mathbb{F} = \mathbb{Z}/(2^{61} - 1)\mathbb{Z}$, and of 32 OH words of 64 bit each (16 chunks of 16 bytes each).

The generation code uses rejection sampling to avoid weak keys: the polynomial key avoids 0 (for which the hash function constantly returns 0), while the OH keys avoid repeated values in order to protects against known weaknesses in the extremely similar NH.

```
# We encode a OH key and a secondary twisting constant in one UmashKey.
TWISTING_COUNT = 2
# For short keys, the secondary set of constants lives four u64s
# ahead: that's the value we used in the earlier more symmetric
# incarnation of UMASH, and it seemed to work well.
SHORT_KEY_SHIFT = 4
UmashKey = namedtuple("UmashKey", ["poly", "oh"])
def is_acceptable_multiplier(m):
    """A 61-bit integer is acceptable if it isn't 0 mod 2**61 - 1.
    return 1 < m < (2 ** 61 - 1)
def generate_key(random=random.SystemRandom()):
    """Generates a UMASH key with `random`"""
    poly = 0
    while not is_acceptable_multiplier(poly):
        poly = random.getrandbits(61)
    oh = []
    for _ in range(2 * BLOCK_SIZE + TWISTING_COUNT):
        u64 = None
        while u64 is None or u64 in oh:
            u64 = random.getrandbits(64)
        oh.append(u64)
    return UmashKey(poly, oh)
```

Short input hash

Input strings of 8 bytes or fewer are expanded to 64 bits and shuffled with a modified SplitMix64 update function. The modification inserts a 64-bit data-independent "noise" value in the SplitMix64 code; that noise differs unpredictably for each input length in order to prevent extension attacks.

This conversion to 8 bytes is optimised for architectures where misaligned loads are fast, and attempts to minimise unpredictable branches.

```
def vec_to_u64(buf):
    """Converts a <= 8 byte buffer to a 64-bit integer. The conversion is
    unique for each input size, but may collide across sizes."""
    n = len(buf)
    assert n <= 8
    if n \ge 4:
        lo = struct.unpack("<I", buf[0:4])[0]</pre>
        hi = struct.unpack("<I", buf[-4:])[0]</pre>
        # len(buf) < 4. Decode the length in binary.
        lo = 0
        hi = 0
        # If the buffer size is odd, read its first byte in `lo`
        if (n & 1) != 0:
            lo = buf[0]
        # If the buffer size is 2 or 3, read its last 2 bytes in `hi`.
        if (n & 2) != 0:
            hi = struct.unpack("<H", buf[-2:])[0]</pre>
    # Minimally mix `hi` in the `lo` bits: SplitMix64 seems to have
    # trouble with the top ~4 bits.
    return (hi << 32) | ((hi + lo) % (2 ** 32))
```

In addition to a full-blown key, UMASH accepts a seed parameter. The seed is used to modify the output of UMASH without providing any bound on collision probabilities: independent keys should be generated to bound the probability of two inputs colliding under multiple keys.

The short vector hash modifies the SplitMix64 update function by reversibly xoring the state with a data-independent value in the middle of the function. This additional xor lets us take the input seed into account, works around SplitMix64(0) = 0, and adds unpredictable variability to inputs of different lengths that vec_to_u64 might convert to the same 64-bit integer. It comes in after one round of xor-shift-multiply has mixed the data around: moving it earlier fails SMHasher's Avalanche and Differential Distribution tests.

Every step after vec_to_u64 is reversible, so inputs of the same byte length ≤ 8 will never collide. Moreover, a random value from the OH key is injected in the process; while each step is trivial to invert, the additional xor is only invertible if we know the random OH key.

Two values of the same size never collide. Two values x and y of different lengths collide iff $k_{len(x)} \oplus k_{len(y)}$ has exactly the correct value to cancel out the difference between x and y after partial mixing. This happens with probability 2^{-64} .

When we use a Toeplitz extension to generate a second OH key, collisions for the first and second keys are independent: $k_{1\text{en}(x)} \oplus k_{1\text{en}(y)}$ and $k_{1\text{en}(x)+S} \oplus k_{1\text{en}(y)+S}$ are independent for chunk shift constant $S \geq 2$ (we use S = 2, i.e., four 64-bit words).

```
def umash_short(key, seed, buf):
    """Hashes a buf of 8 or fewer bytes with a pseudo-random permutation."""
    assert len(buf) <= 8
    # Add an unpredictable value to the seed. vec_to_u64` will
    # return the same integer for some inputs of different length;
    # avoid predictable collisions by letting the input size drive the
    # selection of one value from the random `OH` key.
   noise = (seed + key[len(buf)]) % W
    h = vec_to_u64(buf)
   h = h >> 30
   h = (h * 0xBF58476D1CE4E5B9) % W
    h = h >> 27
    # Our only modification to SplitMix64 is this `xor` of the
    # randomised `seed` into the shuffled input. A real
    # implementation can expose marginally more instruction-level
    # parallelism with h = (h \cap noise) (h >> 27).
   h ^= noise
   h = (h * 0x94D049BB133111EB) % W
   h = h >> 31
   return h
```

Long input hash

Inputs of 9 bytes or longers are first compressed with a 64-bit OH function (128-bit output), and then accumulated in a polynomial string hash.

When the input size is smaller than 16 bytes, we expand it to a 16-byte chunk by concatenating its first and last 8 bytes, while generating the "tag" by xoring the original byte size with the seed (this logic would also work on 8-byte inputs, but results in slower hashes for that common size). We pass this single input chunk to the OH compression function, and feed the result to the polynomial hash. The tag protects against extension attacks, and the OH function reduces to the low latency NH compression function for single-chunk inputs.

We use NH for the last chunk in each OH block because, while throughput is challenging with NH compared to PH, the latency of NH's integer multiplications tends to be lower than that of PH's carry-less multiplications.

Longer inputs are turned into a stream of 16-byte chunks by letting the last chunk contain redundant data if it would be short, and passing blocks of up to 16 chunks to 0H. The last block can still be shorter than 16 chunks of 16 bytes (256 bytes), but will always consist of complete 16-byte chunks. Again, we protect against extension collisions by propagating the original byte size with each block, for the outer polynomial hash to ingest.

This approach simplifies the end-of-string code path when misaligned loads are efficient, but does mean we must encode the original string length s somewhere. We use the same trick as VHASH and xor (s mod 256) with the seed to generate the last block's tag value. Since only the last block may span fewer than 256 bytes, this is equivalent to xoring $|\mathbf{M}_i|$ mod 256, a block's size modulo the maximum block size, with the seed to generate each block's tag.

Tags are 128-bit values, and xoring a 64-bit seed with s mod 256 yields a 64-bit value. We avoid a carry in the NH step by letting the tag be equal to the xored value shifted left by 64 bits (i.e., we only populate the high 64-bit half).

```
def chunk bytes(buf):
    """Segments bytes in 16-byte chunks; generates a stream of (chunk,
    original_byte_size). The original_byte_size is always CHUNK_SIZE,
    except for the last chunk, which may be short.
    When the input is shorter than 16 bytes, we convert it to a
    16-byte chunk by reading the first 8 and last 8 bytes in the
    buffer. We know there are at least that many bytes because
    `umash_short` handles inputs of 8 bytes or less.
    If the last chunk is short (not aligned on a 16 byte boundary),
    yields the partially redundant last 16 bytes in the buffer. We
    know there are at least 16 such bytes because we already
    special-cased shorter inputs.
    assert len(buf) >= CHUNK_SIZE / 2
    n = len(buf)
    if n < CHUNK SIZE:
        yield buf[: CHUNK_SIZE // 2] + buf[-CHUNK_SIZE // 2 :], n
        return
    for i in range(0, len(buf), CHUNK_SIZE):
        if i + CHUNK SIZE <= n:</pre>
            yield buf[i : i + CHUNK_SIZE], CHUNK_SIZE
        else:
            yield buf[n - CHUNK_SIZE :], n - i
def blockify chunks(chunks):
    """Joins chunks in up to 256-byte blocks, and generates
    a stream of (block, original_byte_size)."""
```

```
acc = []
size = 0
for chunk, chunk_size in chunks:
    assert len(chunk) == CHUNK_SIZE
    assert len(acc) <= BLOCK_SIZE
    if len(acc) == BLOCK_SIZE:
        # Only the last chunk may be short.
        assert size == CHUNK_SIZE * BLOCK_SIZE
        yield acc, size
        acc = []
        size = 0
    acc.append(chunk)
    size += chunk_size
assert acc
yield acc, size</pre>
```

Given a stream of blocks, the last of which may be partial, we compress each block of up to 256 bytes (but always a multiple of 16 bytes) with the OH function defined by the key. This yields a stream of 128-bit OH outputs.

We propagate a seed argument all the way to the individual OH calls. We use that seed to elicit different hash values, with no guarantee of collision avoidance. Callers that need collision probability bounds should generate fresh keys.

In order to prevent extension attacks, we generate each block's tag by xoring the seed with that block's byte size modulo 256 (the maximum byte size), and shifting the resulting 64-bit value left by 64 bits, much like VHASH does.

We already proved that two different blocks will collide with probability at most 2^{-63} , as long as different pre-expanded block size in bytes are assigned distinct tags. The proof relied on mere almost-universality for the ENH iteration; applying a reversible finaliser (we **xor** the low half into the high half) does not affect the collision bound, but improves distribution by inserting a different form of linearity between the modular arithmetic of NH and the Carter-Wegman polynomial.

```
def oh_mix_one_block(key, block, tag, secondary=False):
    """Mixes each chunk in block."""
    mixed = list()
    lrc = (0, 0)  # we generate an additional chunk by xoring everything together
    for i, chunk in enumerate(block):
        ka = key[2 * i]
        kb = key[2 * i + 1]
        xa, xb = struct.unpack("<QQ", chunk)
        lrc = (lrc[0] ^ (ka ^ xa), lrc[1] ^ (kb ^ xb))
        if i < len(block) - 1:
             mixed.append(gfmul(xa ^ ka, xb ^ kb))
        else:</pre>
```

```
# compute ENH(chunk, tag)
            xa = (xa + ka) % W
            xb = (xb + kb) \% W
            enh = ((xa * xb) + tag) % (W * W)
            enh ^= (enh % W) * W
            mixed.append(enh)
    if secondary:
        mixed.append(gfmul(lrc[0] ^ key[-2], lrc[1] ^ key[-1]))
    mixed.reverse()
    return mixed
# TODO: compare against the reference directly with `secondary=True`.
def oh compress one block(key, block, tag, secondary=False):
    """Applies the `OH` hash to compress a block of up to 256 bytes."""
    mixed = oh_mix_one_block(key, block, tag, secondary)
    if secondary is False:
        # Easy case (fast hash): xor everything
        return reduce(lambda x, y: x ^ y, mixed, 0)
    def xs(x, i):
        """Computes our almost-xor-shift of x, on parallel 64-bit halves.
        If i == 0, this function is the identity
        If i == 1, this is (x << 1)
        Otherwise, this is (x << i) ^ (x << 1)."""
        def parallel_shift(x, s):
            lo, hi = x \% W, x // W
            lo = (lo << s) % W
            hi = (hi << s) % W
            return lo + W * hi
        if i == 0:
            return x
        if i == 1:
            return parallel_shift(x, 1)
        return parallel_shift(x, i) ^ parallel_shift(x, 1)
    acc = mixed[0]
    for i, mixed_chunk in enumerate(mixed[1:]):
        acc ^= xs(mixed_chunk, i)
    return acc
def oh_compress(key, seed, blocks, secondary):
```

```
"""Applies the `OH` compression function to each block; generates
a stream of compressed values"""
for block, block_size in blocks:
    size_tag = block_size % (CHUNK_SIZE * BLOCK_SIZE)
    tag = (seed ^ size_tag) * W
    yield oh_compress_one_block(key, block, tag, secondary)
```

OH is a fast compression function. However, it doesn't scale to arbitrarily large inputs. We split each of its 128-bit outputs in two 64-bit halves, and accumulate them in a single polynomial hash modulo $2^{64} - 8 = 8 \cdot (2^{61} - 1)$.

Modular multiplication in that ring is more efficient when one of the multipliers is known to be less than 2^{61} . A random multiplier $f \in \mathbb{F} = \mathbb{Z}/(2^{61}-1)\mathbb{Z}$ naturally satisfies that constraint; so does its square f^2 , once fully reduced to \mathbb{F} . We will use that to pre-compute $f^2 \in \mathbb{F}$ and overlap most of the work in a double-pumped Horner update. The result will differ from a direct evaluation modulo $2^{64}-8$, but not in \mathbb{F} , which is what matters for analyses.

Every time we feed one 64-bit half of a PH value to the polynomial in $\mathbb{F}=\mathbb{Z}/(2^{61}-1)\mathbb{Z}$, we lose slightly more than 3 bits of data. The resulting collision probability for truncated PH is less than $\lceil 2^{64}/|\mathbb{F}| \rceil^2 \cdot 2^{-63} < 2^{-56}$. As s, the input size in bytes, grows, that's quickly dominated by the collision probability for the polynomial hash in \mathbb{F} , $\varepsilon_{\mathbb{F}} \leq d/|\mathbb{F}| = 2\lceil s/256\rceil/(2^{61}-2)$, where the last decrement accounts for our disqualifying 0 from the set of multipliers.

Note that the Horner update increments before multiplying: this does not impact the degree of the hash polynomial, nor its collisions probability since its inputs are randomised by OH, but having the last step be a multiplication improves distribution.

```
def poly_reduce(multiplier, input_size, compressed_values):
    """Updates a polynomial hash with the `OH` compressed outputs."""
    # Square the multiplier and fully reduce it. This does not affect
    # the result modulo 2**61 - 1, but does differ from a
    # direct evaluation modulo 2**64 - 8.
   mulsq = (multiplier ** 2) % (2 ** 61 - 1)
    acc = [0]
    def update(y0, y1):
        """Double-pumped Horner update (mostly) modulo 8 * (2**61 - 1)."""
        # Perform a pair of Horner updates in (mod 2**61 - 1).
        reference = multiplier * (acc[0] + y0)
        reference = multiplier * (reference + y1)
        reference %= 2 ** 61 - 1
        # The real update is in (mod 2**64 - 8), with a multiplier<sup>2</sup>
        # reduced to (mod 2**61 - 1).
        acc[0] = (mulsq * (acc[0] + y0) + multiplier * y1) % (W - 8)
```

```
# Both values should be the same (mod 2**61 - 1).
assert acc[0] % (2 ** 61 - 1) == reference

for value in compressed_values:
    lo = value % W
    hi = value // W
    update(lo, hi)
return acc[0]
```

Reversible finalisation

Mere universality does not guarantee good distribution; in particular, bit avalanche tests tend to fail. In the case of UMASH, the modulus, $2^{64} - 8$ also creates obvious patterns: the low 3 bits end up being a mod 8 version of the mod $2^{61} - 1$ polynomial hash.

This predictability does not impact our collision bounds (they already assume the hash outputs are modulo $2^{61} - 1$), but could create clumping in data structures. We address that with an invertible xor-rotate transformation. Rotating before xor mixes both the low and high bits around, and xoring a pair of bit-rotated values guarantees invertibility (xoring a single rotate would irreversibly map both 0 and -1 to 0).

The pair of rotation constants in the finalizer, 8 and 33, was found with an exhaustive search: they're good enough for SMHasher. In theory, this is a bad finalizer, for all constants. The rotation counts are likely tied to the (mod 2**64 - 8) polynomial hash.

```
def rotl(x, count):
    """Rotates the 64-bit value `x` to the left by `count` bits."""
    ret = 0
    for i in range(64):
        bit = (x >> i) & 1
        ret |= bit << ((i + count) % 64)
    return ret

def finalize(x):
    """Invertibly mixes the bits in x."""
    return x ^ rotl(x, 8) ^ rotl(x, 33)</pre>
```

Putting it all together

UMASH is the combination of umash_short for inputs shorter than 9 bytes, and umash_long for everything else. The latter hash function chops up its input in 16-byte chunks (where the last chunk is never partial, but potentially redundant),

groups these chunks in blocks of up to 256 bytes, and compresses each block down to 128 bits with OH. A polynomial hash function in $\mathbb{F} = \mathbb{Z}/(2^{61}-1)\mathbb{Z}$ reduces that stream of compressed outputs to a single machine word.

Two short inputs never collide if they have the same length; in general the probability that they collide satisfies $\varepsilon_{\rm short} \leq 2^{-64}$.

The probability of collision for longer inputs is the sum of the collision probability for one PH block after truncation to \mathbb{F}^2 , $\varepsilon_{\rm PH} < 9^2 \cdot 2^{-63} \approx 0.64 \cdot 2^{-56}$, and that of the polynomial hash, $\varepsilon_{\mathbb{F}} \approx d \cdot 2^{-61}$, where $d = 2\lceil s/256 \rceil$ is twice the number of PH blocks for an input length s > 8. Together, this yields $\varepsilon_{\rm long} \approx 2\lceil s/256 \rceil \cdot 2^{-61} + 0.64 \cdot 2^{-56} < \lceil s/4096 \rceil \cdot 2^{-55}$.

The short input hash is independent of the polynomial hash multiplier, and can thus be seen as a degree-0 polynomial. Any collision between short and long inputs is subject to the same $\varepsilon_{\rm long}$ collision bound. The total collision probability for strings of s bytes or fewer (with expectation taken over the generated key) thus adds up to $\varepsilon < \lceil s/4096 \rceil \cdot 2^{-55}$.

Implementation tricks

UMASH is structured to minimise the amount of work for mid-sized inputs composed of a handful of chunks. The last 16-byte chunk stops the OH loop early to divert to a low-latency NH in (64-bit) general purpose registers, and the chunking logic avoids complex variable-length memcpy.

The multipliers and modulus for the polynomial hashes were chosen to simplify implementation on 64-bit machines: assuming the current hash accumulator is in $[0, 2^{64} - 8)$, we can increment it by a 64-bit value in three x86-64 instructions, multiply the sum by a 61-bit multiplier in five instructions, and re-normalize the accumulator to the modulus range with two instructions followed by a predictable branch.

It's also convenient that the range for fast multipliers, $[0, 2^{61})$, is as wide as the theoretical modulus: we can precompute the square of the polynomial multipliers and perform a double Horner update with two independent multiplications, which exposes more instruction-level parallelism than individual Horner updates.

For longer inputs, we can micro-optimise the PH bulk of the OH inner loop by pre-loading the key in SIMD registers (e.g., in 8 AVX-256 registers), when vectorising the $m_i \oplus k_i$ step. That step isn't particularly slow, but PH is so simple that a decent implementation tends to be backend-bound, so minimising the number of hardware micro-ops helps.

We only consider the size of the input at the very end of the umash_long function; this makes it possible to implement an incremental hashing interface. The outer polynomial hash also enables parallel out-of-order computation of the hash, as long as the I/O is delivered at 256 byte boundaries (in practice, the I/O granularity is at least 512 bytes, often 4KB or more), a useful property when hashing larger data files.

What if we need stronger guarantees?

We can reuse most of the OH key with a Toeplitz extension. We skip two OH parameter chunks (four 64-bit words), and obtain a second independent UMASH with five additional parameters (one multiplier, and four OH values) in the key; it's also easy to merge the two OH compression loops, especially for incremental hashing.

When the input is 8 bytes or shorter, we **xor** in a single length-dependent value from the OH key. Inputs of the same length never collide, so a Toeplitz extension offers independent collision probabilities. Two inputs of different length collide when $k_s \oplus k_{s'}$ equals one unlucky value; the same expression in the Toeplitz-extended key is $k_{s+K} \oplus k_{s'+K}$ for any Toeplitz shift K > 0, with distribution independent of $k_s \oplus k_{s'}$.

Combining two UMASHes squares the collision probability. The resulting probability, $\varepsilon^2 < \lceil s/4096 \rceil^2 \cdot 2^{-110}$ is easily comparable to the uncorrectable DRAM error rates reported by Facebook: 0.03% per month $\approx 2^{-66}$ per CPU cycle.

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Phil Vachon helped me gain more confidence in the implementation tricks borrowed from VHASH after replacing the NH compression function with PH.

Change log

2020-08-27: use s for the input byte length parameter, which renders less confusingly than l (ell) in the face of font issues.

2020-09-13: use an invertible xor-rot finalizer, instead of multiply xorshift.

2020-09-20: mix 9-16 byte inputs with NH instead of PH, before passing to the same polynomial hash and finalisation.

2020-10-13: replace PH with OH, a variant that uses NH for the last 16-byte chunk. Analysis shows we can make every block benefit from the improved latency of NH, not just short 9-16 byte blocks.