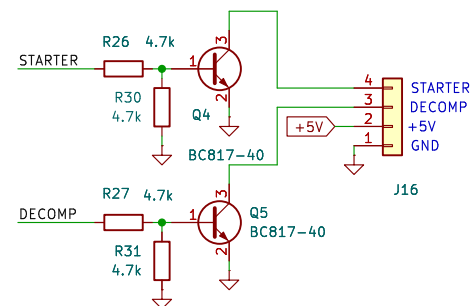
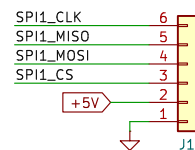


The diagram shows the motor driver section of the ESDA6V1-5SC6. It includes four motor channels (J11, J12, J13, J14) with +5V, GND, and PWM signals. The PWM signals are connected to the motor driver IC pins. The IC is also connected to TIM3_CH1, TIM3_CH2, TIM3_CH3, TIM3_CH4, and TIM14_CH1. The IC is labeled ESDA6V1-5SC6.



The image contains two circuit diagrams. The top diagram shows a header J9 with four pins. Pin 4 is connected to UART4.RX via a green wire and a red box labeled 'NM'. Pin 3 is connected to UART4.TX via a green wire and a red box labeled '100'. Pin 2 is connected to a +5V power source via a green wire and a red box labeled '100'. Pin 1 is connected to GND via a green wire and a red box labeled '100'. The bottom diagram shows a header J10 with four pins. Pin 3 is connected to TIM16_CH1N via a green wire and a red box labeled '100'. Pin 2 is connected to a +5V power source via a green wire and a red box labeled '100'. Pin 1 is connected to GND via a green wire and a red box labeled '100'. Pin 4 is connected to a +5V power source via a green wire and a red box labeled '100'.

GPS

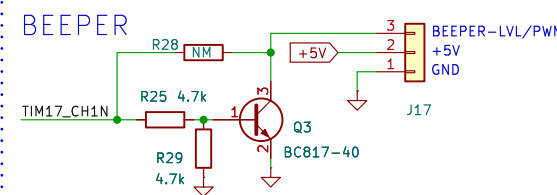
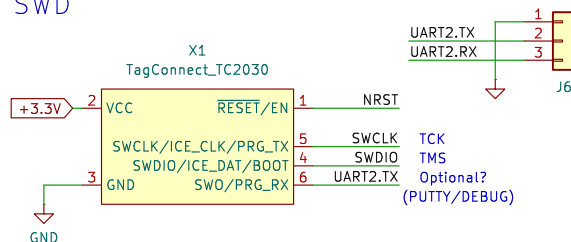
6 I2C2.SCL
5 I2C2.SDA
4 UART3.TX
3 UART3.RX
2
1

J5

+5V

R2 4.7k
R6 4.7k
R7 100
R8 100

+3.3V



- FID1
Fiducial
- FID2
Fiducial

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