


BRAKING IO

POD 5

REV 1

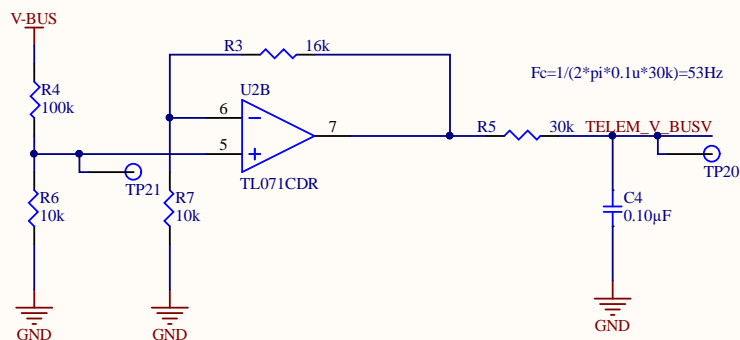
Title <i><b>Braking IO PCB</b></i>			Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706		
Engineer:		Revision:			
Date: 9/12/2019	Time: 12:51:47 PM	Sheet	of		
File: braking_io.SchDoc					

Size L4 for max input current (9A)

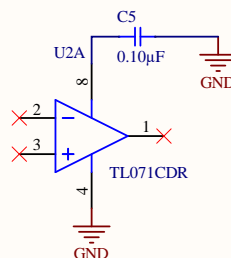
Max current: 10A

Filter design reference: <http://www.ti.com/lit/an/snva538/snva538.pdf>  
<http://ecee.colorado.edu/~rwe/papers/APEC99.pdf>

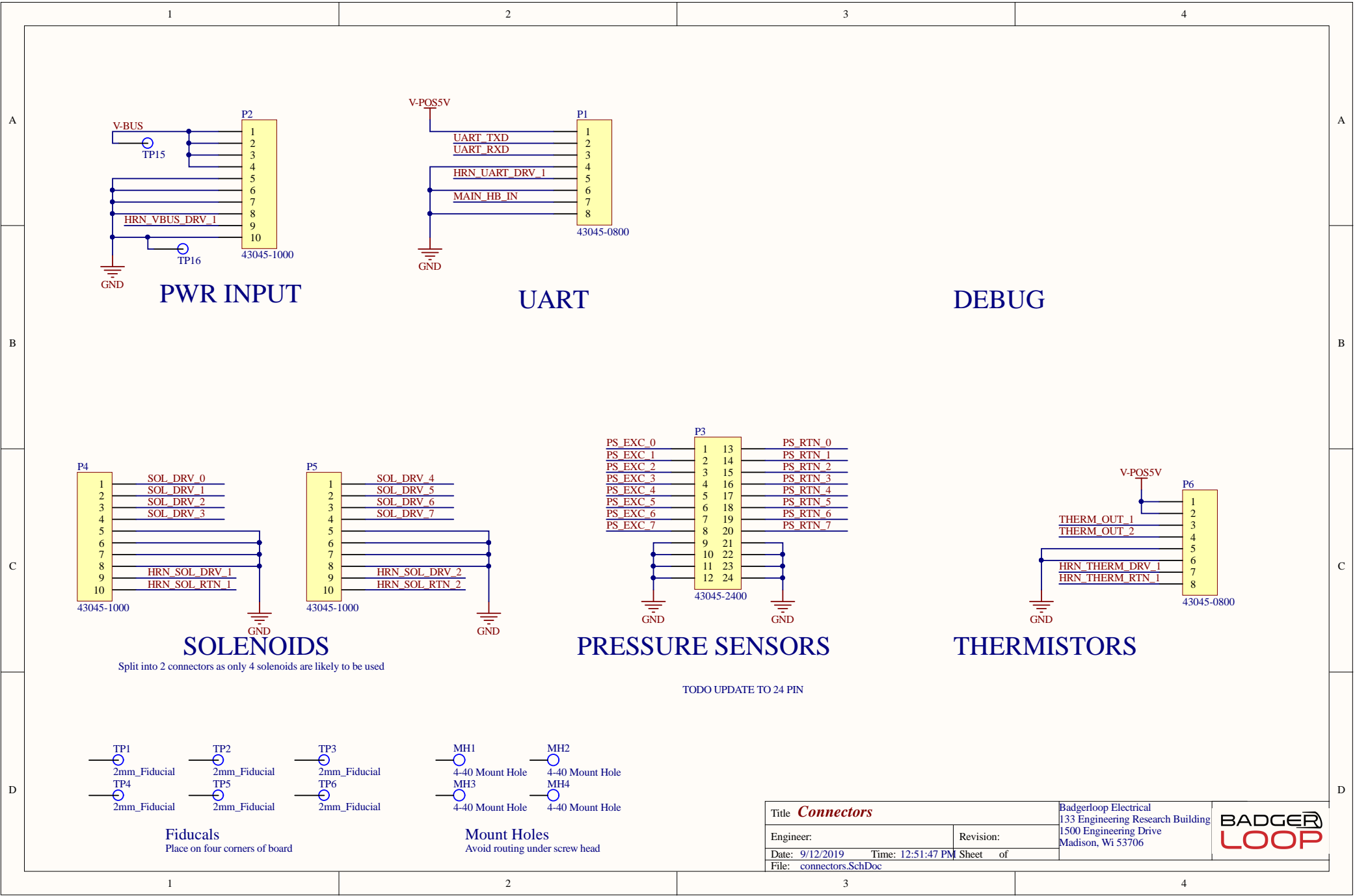
Max current draw:  $9A \rightarrow 9A \cdot 0.002\Omega = 0.018V$   
 INA Gain:  $200V/V \rightarrow 3.6V$  at Max current  
 Power:  $I^2 \cdot R = 4A^2 \cdot 0.01 = 0.04W$

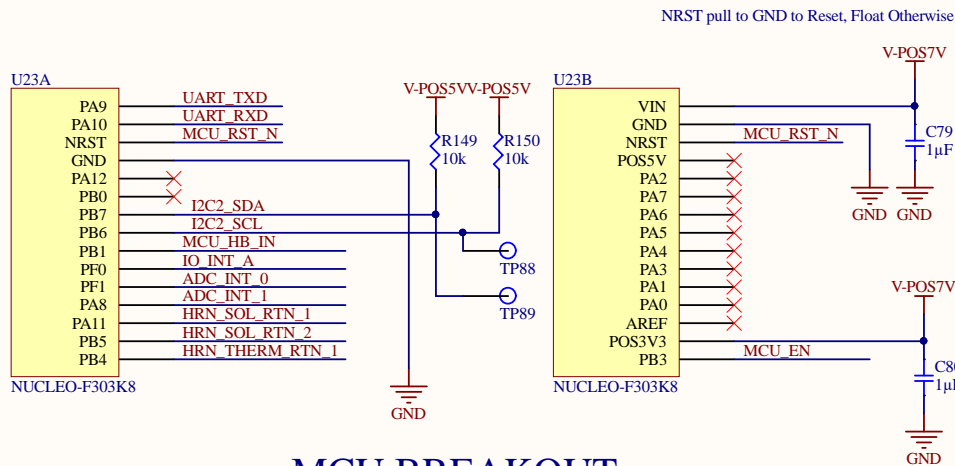


## VOLTAGE TELEMETRY

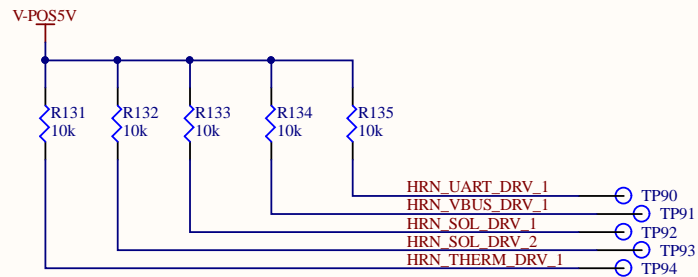


**BADGER**  
**LOOP**

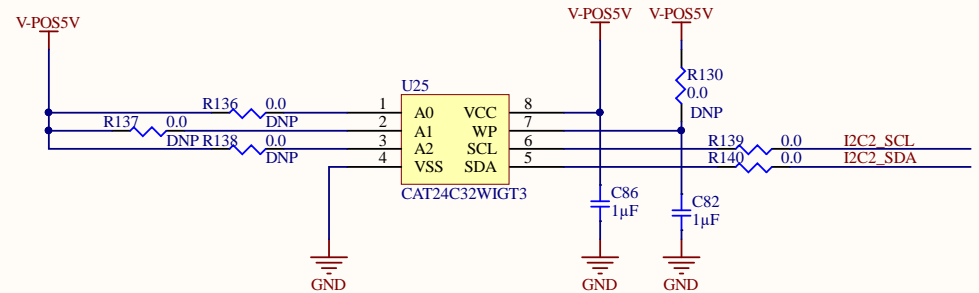




## MCU BREAKOUT



## HARNESS ID



I2C Address: 0x50h

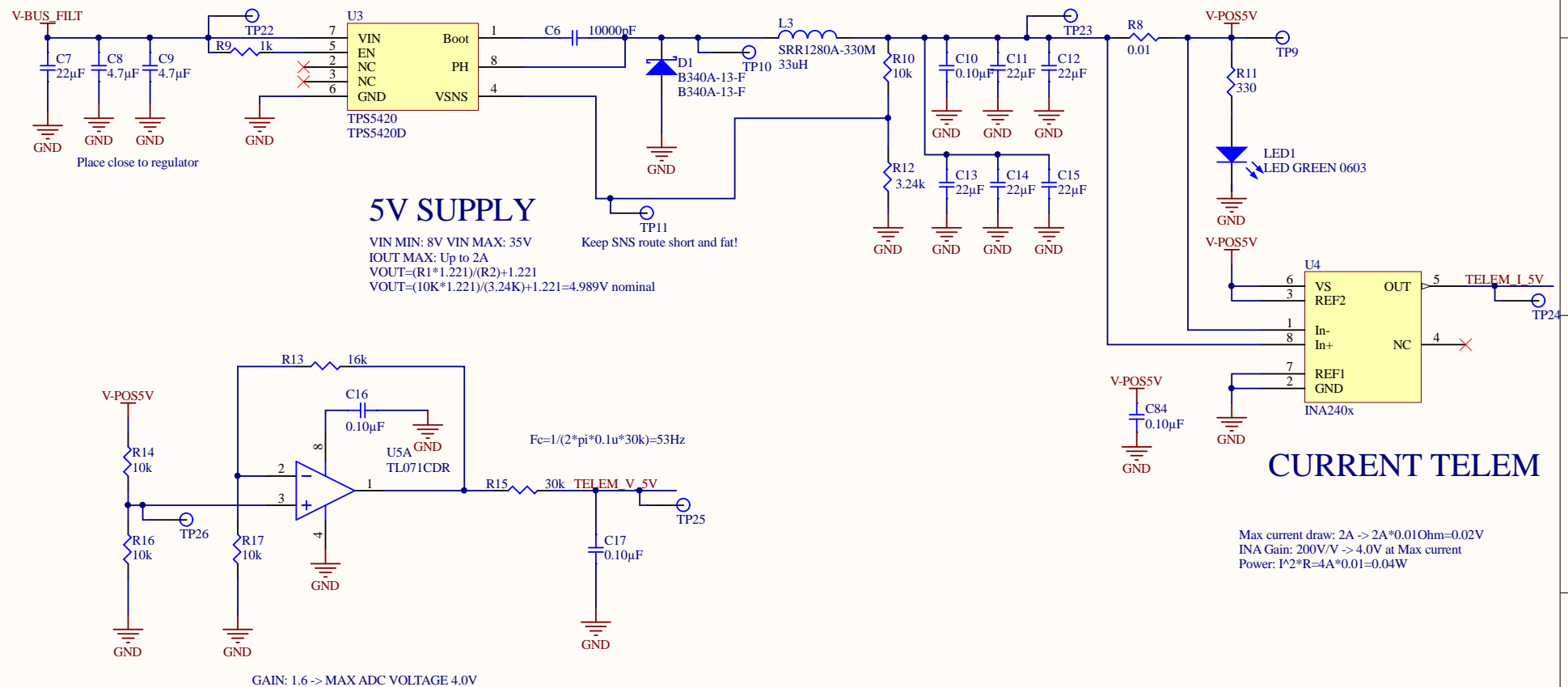
## I2C EEPROM

Title <b>Microcontroller</b>		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:		Revision:	
Date: 9/12/2019	Time: 12:51:48 PM	Sheet	of
File: mcu.SchDoc			


**BADGER**  
**LOOP**

Notes:  
 Follow layout reference design  
 Place bypass caps close to regulator  
 Keep hot loops as short as possible  
 Possible to replace ceramic bulk cap with a tantalum.

Replace with Tantalum?  
 Place close to regulator  
[See https://github.com/badgerloop-software/hardware/tree/master/braking\\_io/design](https://github.com/badgerloop-software/hardware/tree/master/braking_io/design)

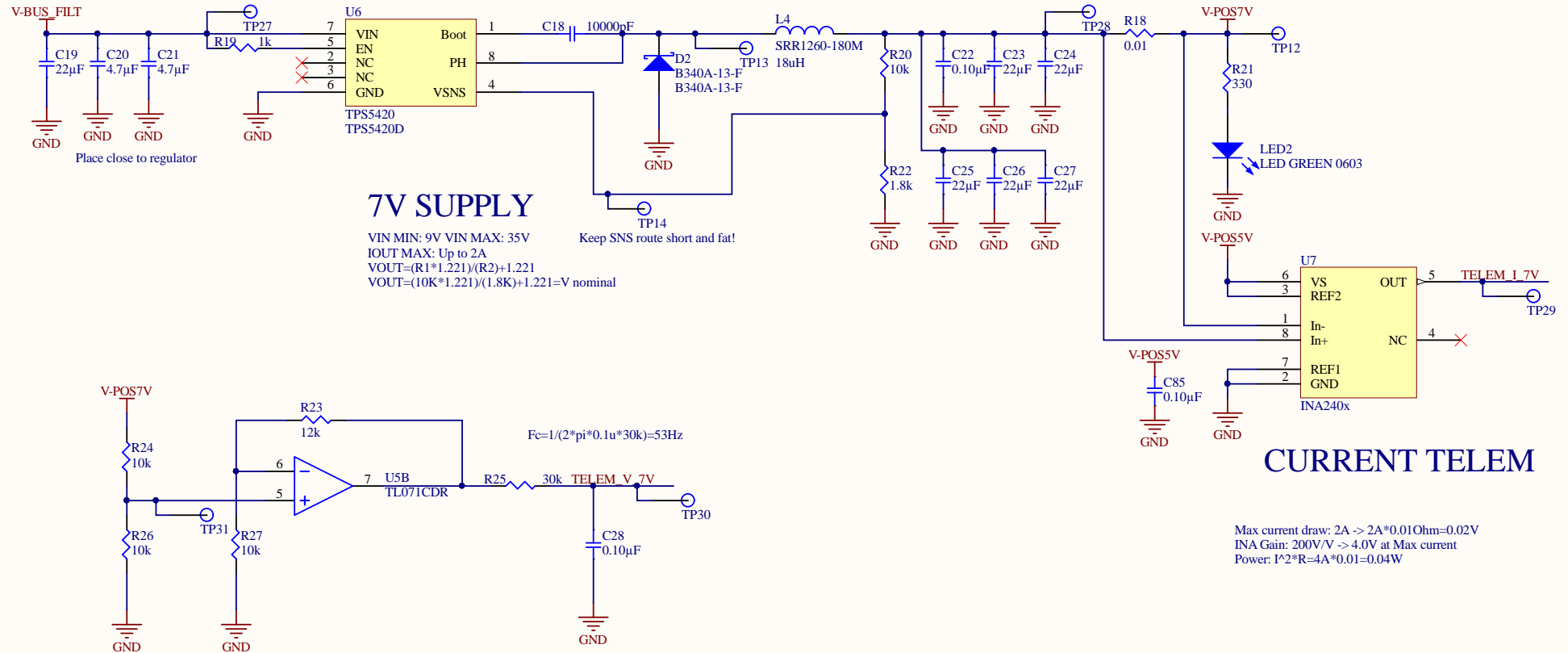


# VOLTAGE TELEMETRY

Title		Badgerloop Electrical		
Engineer:		133 Engineering Research Building		
Date: 9/12/2019		1500 Engineering Drive		
Time: 12:51:48 PM		Madison, WI 53706		
Revision:		Sheet of		
File: power_5V.SchDoc				

Notes:  
Follow layout reference design  
Place bypass caps close to regulator  
Keep hot loops as short as possible  
Possible to replace ceramic bulk cap with a tantalum.

Replace with Tantalum?  
Place close to regulator  
See [https://github.com/badgerloop-software/hardware/tree/master/braking\\_io/design](https://github.com/badgerloop-software/hardware/tree/master/braking_io/design)

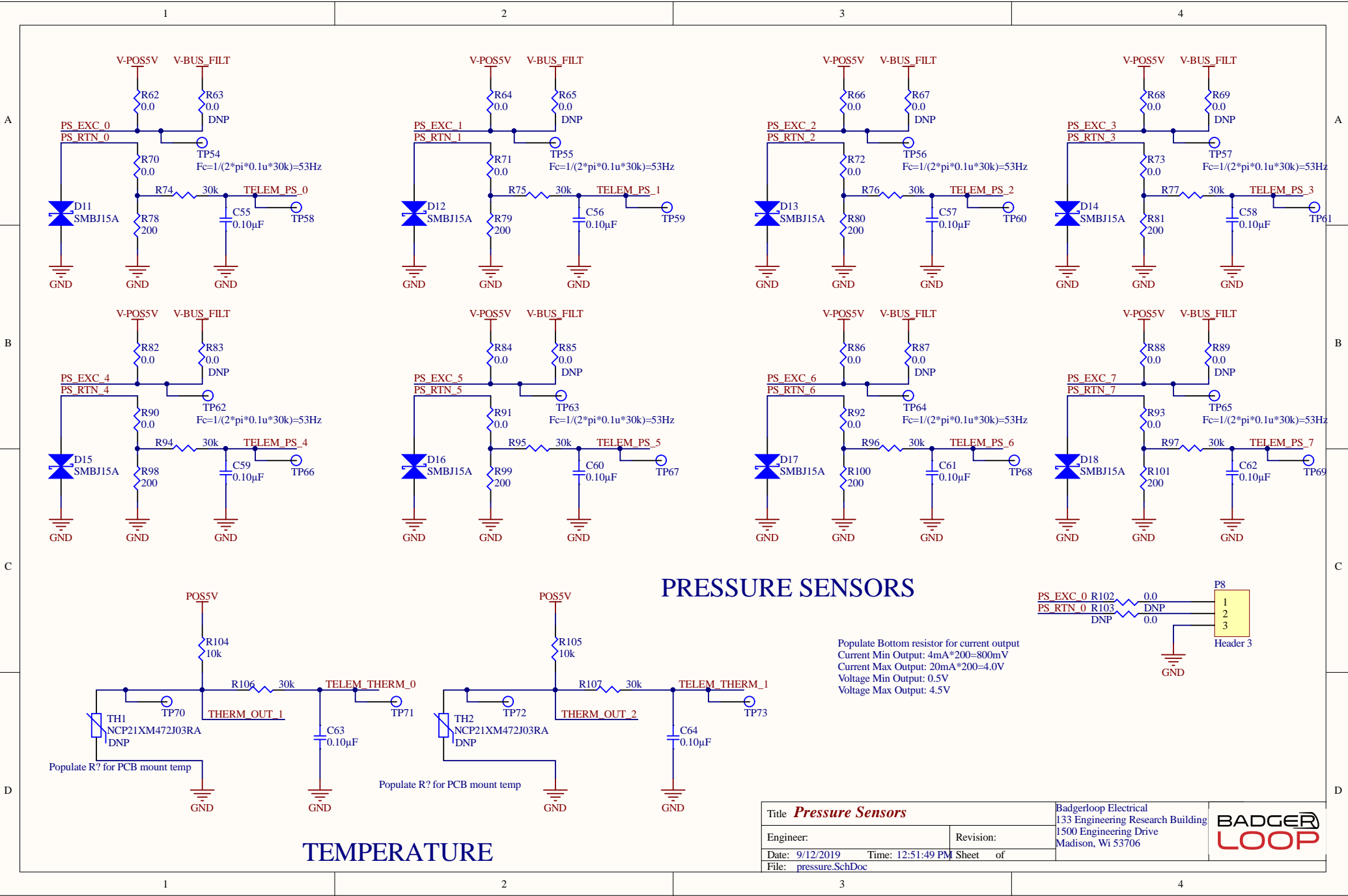


GAIN: 1.2 -> MAX ADC VOLTAGE 4.20V

## VOLTAGE TELEMETRY

Title <b>7V SUPPLY</b>		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:	Revision:	Sheet of	
Date: 9/12/2019	Time: 12:51:48 PM		
File: power_7V.SchDoc			

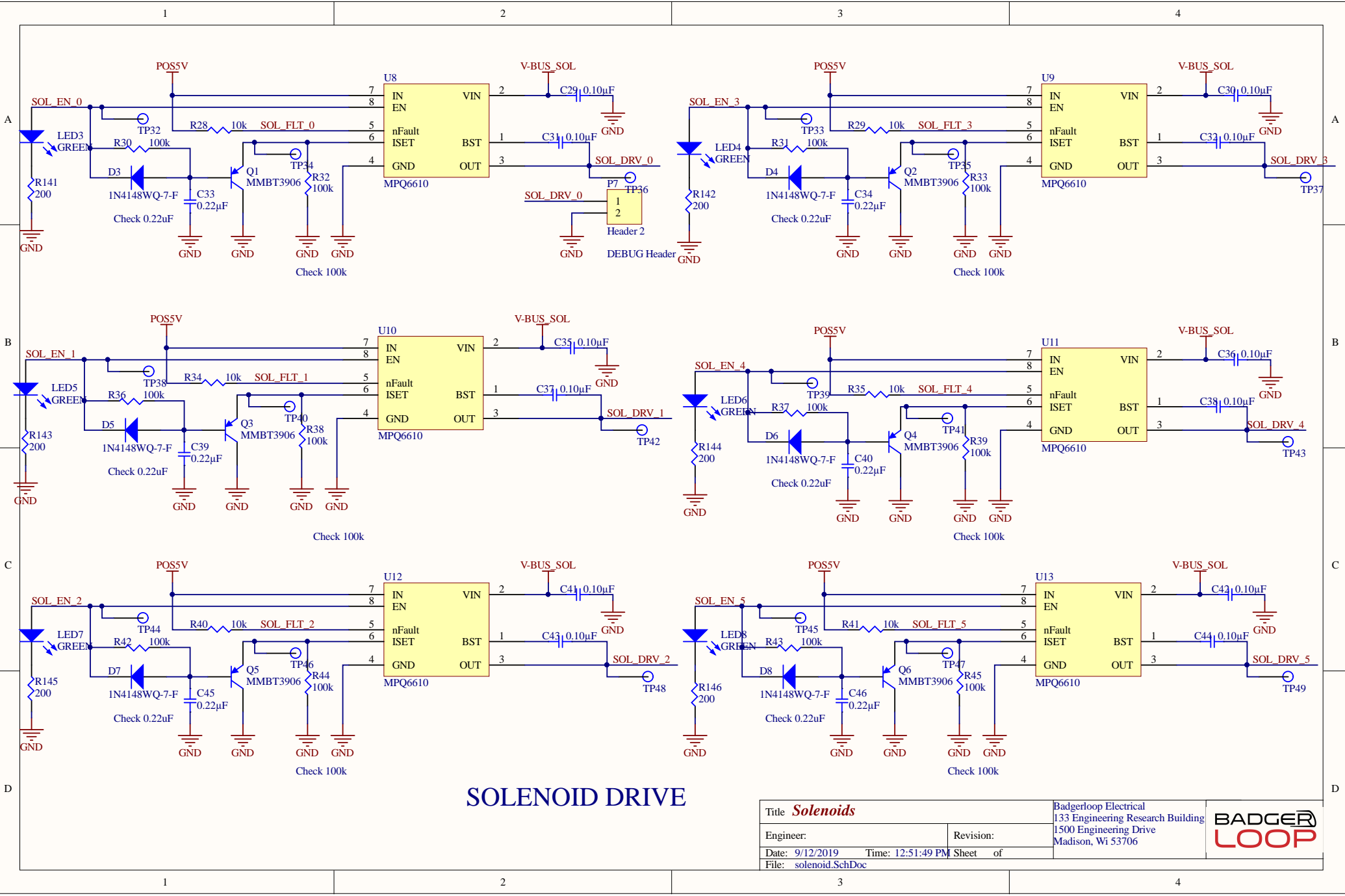
**BADGER  
LOOP**



TEMPERATURE

PRESSURE SENSORS

Populate Bottom resistor for current output  
Current Min Output: 4mA\*200=800mV  
Current Max Output: 20mA\*200=4.0V  
Voltage Min Output: 0.5V  
Voltage Max Output: 4.5V



SOLENOID DRIVE

Title <i>Solenoids</i>		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:		Revision:	
Date: 9/12/2019	Time: 12:51:49 PM	Sheet	of
File: solenoid.SchDoc		BADGER LOOP	

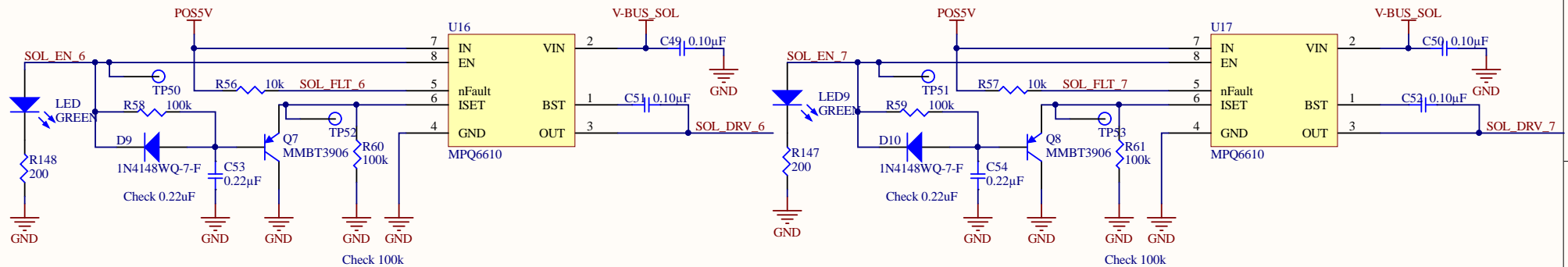
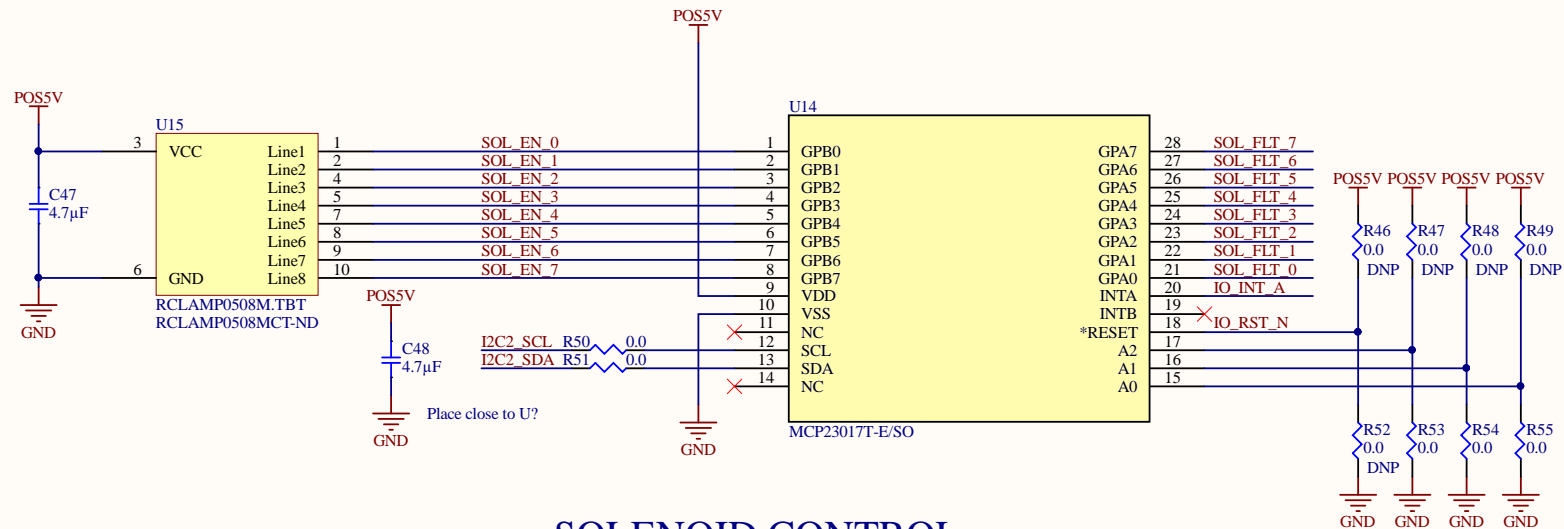


1

2

3

4

Title **Solenoid Control**

Engineer:

Date: 9/12/2019  
File: solenoid\_drv.SchDoc

Revision:

Sheet of

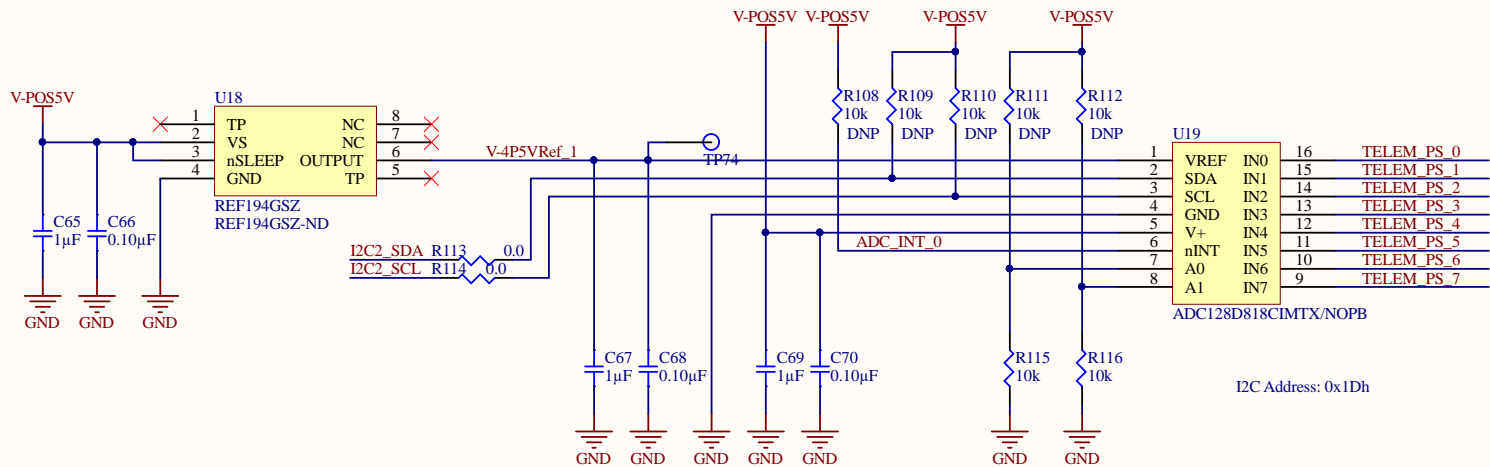
Badgerloop Electrical  
133 Engineering Research Building  
1500 Engineering Drive  
Madison, WI 53706**BADGER  
LOOP**

1

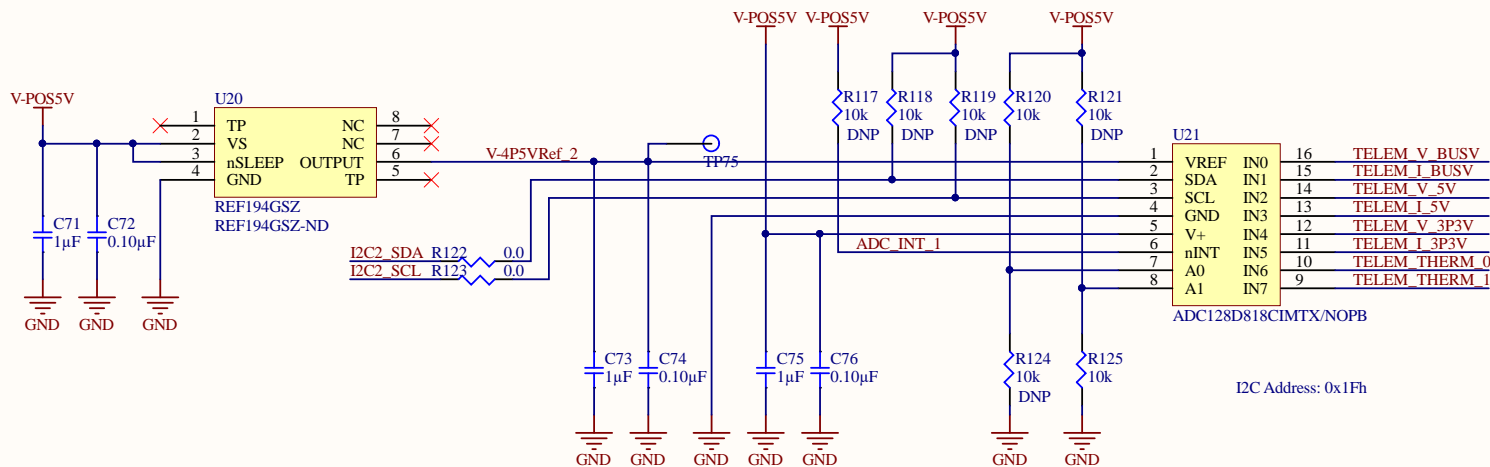
2

3

4



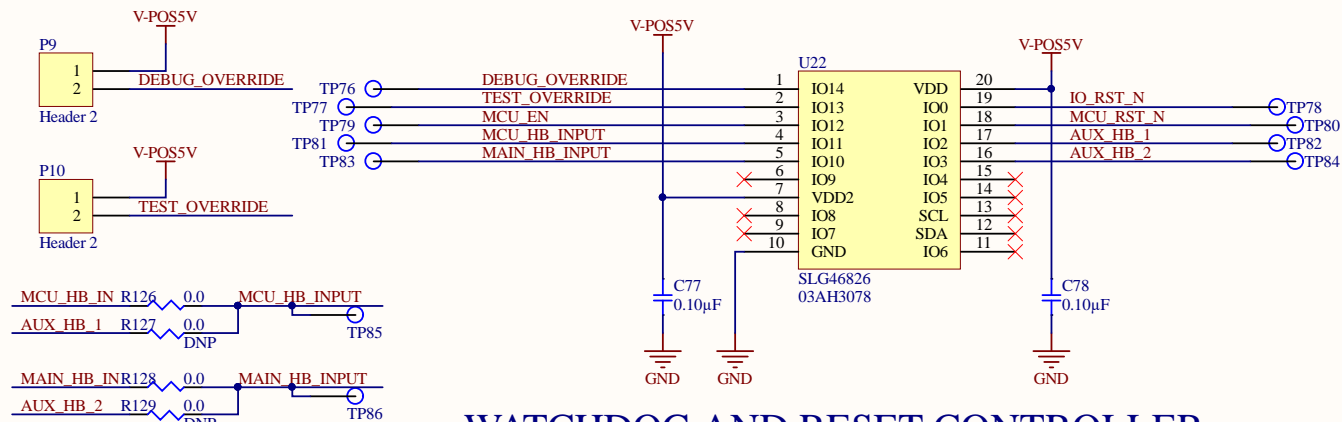
## PRESSURE



## RAIL AND TEMPERATURE

Title		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:	Revision:	Sheet of	
Date: 9/12/2019	Time: 12:51:50 PM		
File: telemetry_adc.SchDoc			





## WATCHDOG AND RESET CONTROLLER

### DEBUG

IO pin selection is arbitrary. Can be adjusted internally for better layout  
Currently- Inputs on Left, outputs on right

#### Modes of operation:

Debug: EN signal is always on when SLG has power

Populate Jumper 1

Test: 10Hz signal internal signal is recirculated to mimic heartbeat

Populate Jumper 2


Operation: U? expects 10Hz heartbeat. If no heartbeat for 1s after 20s Power on reset

MCP RST\_N will fall and MCU RST\_N will pulse for 200ms

Silego Image here:

<https://github.com/badgerloop-software/hardware/blob/master/silego/watchdog.gp6>

Silego Image PDF Outputs:

Title <i>Watchdog</i>			Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, Wi 53706		
Engineer:		Revision:			
Date: 9/12/2019	Time: 12:51:51 PM	Sheet	of		
File: watchdog.SchDoc					