


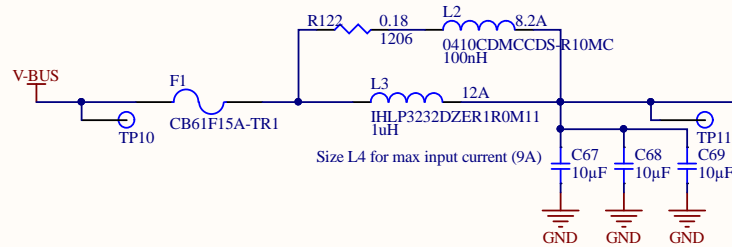
BRAKING IO

POD 5

REV 1

Title <i>Braking IO PCB</i>			Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706		
Engineer:		Revision:			
Date: 9/6/2019	Time: 10:42:39 PM	Sheet of			
File: braking_io.SchDoc					

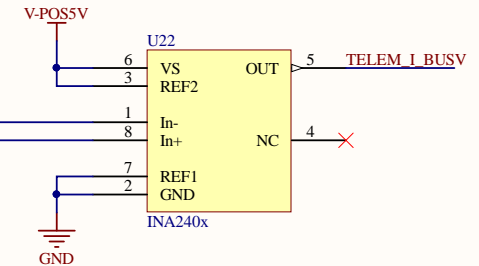
should change upstream fuse to be higher current rating than downstream.



BUS_FILTER

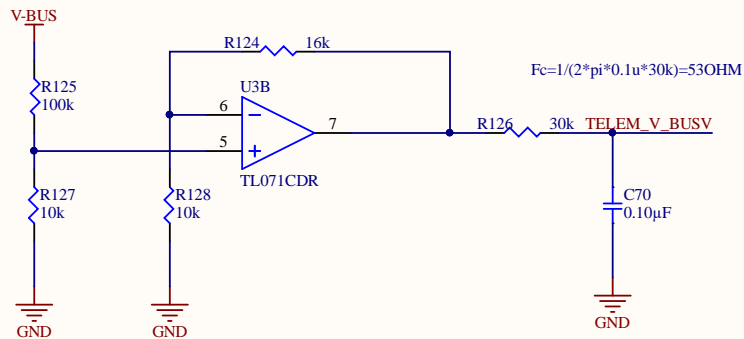
Filter design reference: <http://www.ti.com/lit/an/snva538/snva538.pdf>
<http://ece.colorado.edu/~rwe/papers/APEC99.pdf>

Max current draw: 9A -> 9A*0.01Ohm=0.09V
 INA Gain: 200V/V -> 4.0V at Max current
 Power: I²*R=4A*0.01=0.04W




CURRENT TELEM

Max current draw: 9A -> 9A*0.002Ohm=0.018V
 INA Gain: 200V/V -> 3.6V at Max current
 Power: I²*R=4A*0.01=0.04W

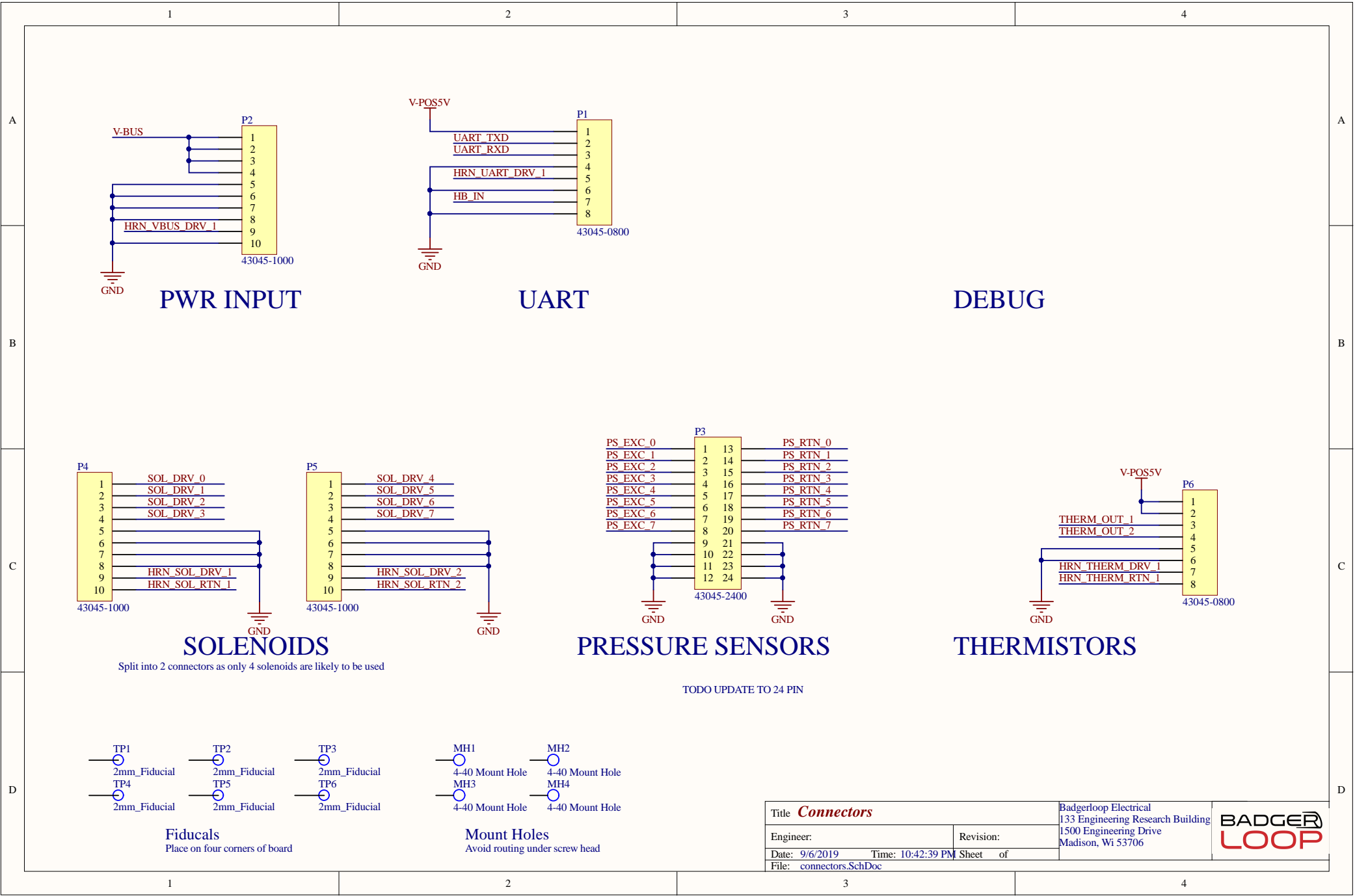


GAIN: 1.6V/V
 MIN BUS VOLTAGE: 20V -> 1.82V
 MIN BUS VOLTAGE: 28V -> 2.54V

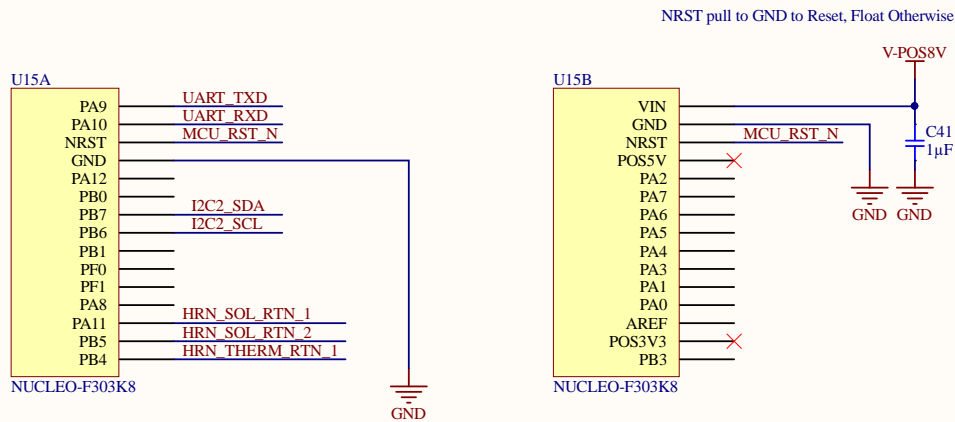
VOLTAGE TELEMETRY

Title			Badgerloop Electrical		
Engineer:		Revision:	133 Engineering Research Building		
Date: 9/6/2019		Time: 10:42:39 PM	1500 Engineering Drive		
File: bus_filter.SchDoc		Sheet of	Madison, WI 53706		

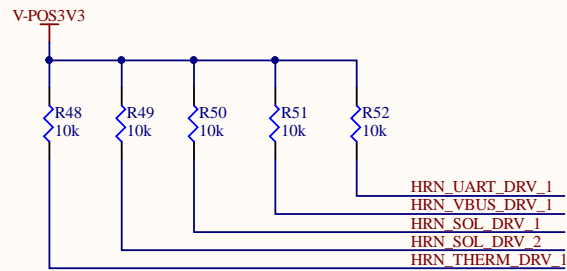
Revision: 10:42:39 PM Sheet 1 of 1



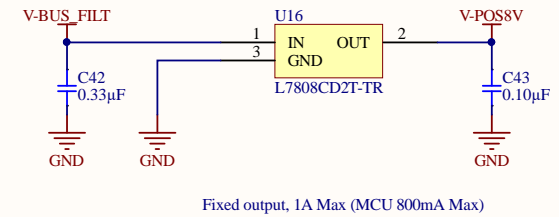
Title Connectors		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	BADGER LOOP
Engineer:	Revision:		
Date: 9/6/2019	Time: 10:42:39 PM		
File: connectors.SchDoc		Sheet 1 of 1	



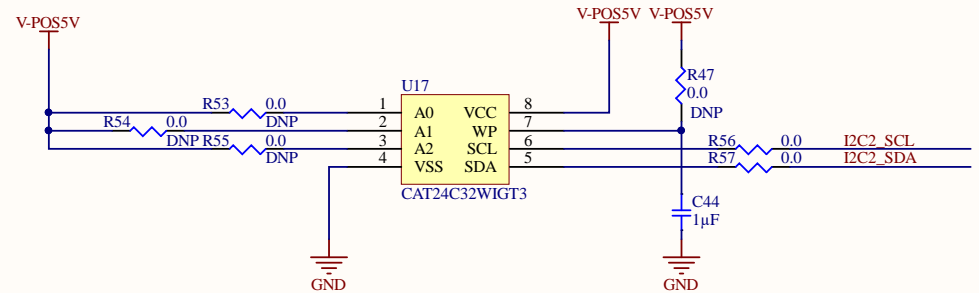
MCU BREAKOUT



HARNESS ID



8V LDO



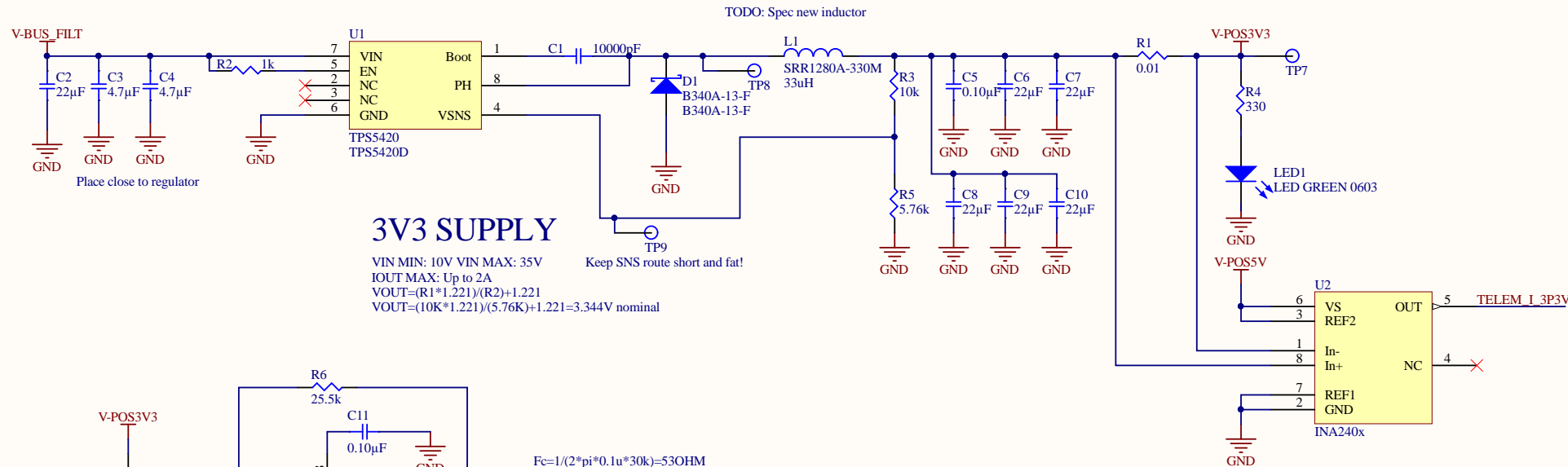
I2C Address: 0x50h

I2C EEPROM

Title Microcontroller		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:	Revision:		
Date: 9/6/2019	Time: 10:42:39 PM		
File: mcu.SchDoc	Sheet of		

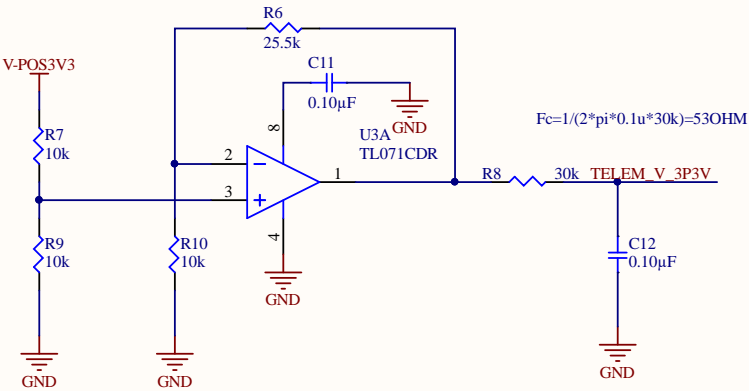
Notes:
Follow layout reference design
Place bypass caps close to regulator
Keep hot loops as short as possible
Possible to replace ceramic bulk cap with a tantalum.

Replace with Tantalum?
Place close to regulator
See https://github.com/badgerloop-software/hardware/tree/master/braking_io/design



CURRENT TELEM

Max current draw: 2A -> $2A * 0.01\Omega = 0.02V$
INA Gain: 200V/V -> 4.0V at Max current
Power: $I^2 * R = 4A^2 * 0.01 = 0.04W$

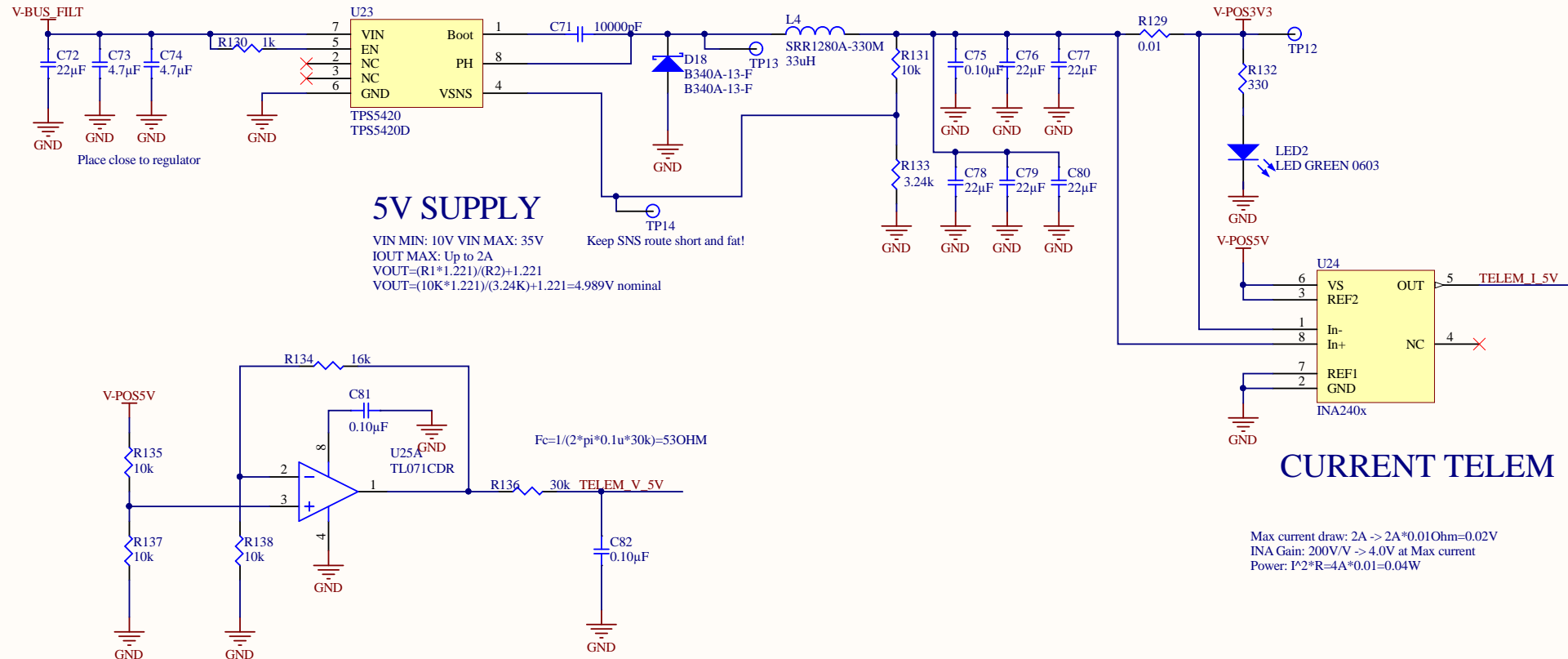


Title 3V3 SUPPLY		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:	Revision:	BADGER LOOP	
Date: 9/6/2019	Time: 10:42:39 PM		
File: power_3V3.SchDoc	Sheet of		

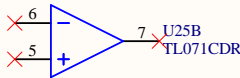
Notes:
 Follow layout reference design
 Place bypass caps close to regulator
 Keep hot loops as short as possible
 Possible to replace ceramic bulk cap with a tantalum.

Replace with Tantalum?
 Place close to regulator
[See https://github.com/badgerloop-software/hardware/tree/master/braking_io/design](https://github.com/badgerloop-software/hardware/tree/master/braking_io/design)

TODO: Spec new inductor



VOLTAGE TELEMETRY



Title		Badgerloop Electrical		
Engineer:		133 Engineering Research Building		
Date: 9/6/2019		1500 Engineering Drive		
File: power_5V.SchDoc		Madison, WI 53706		
Revision:		Time: 10:42:40 PM		Sheet of

1

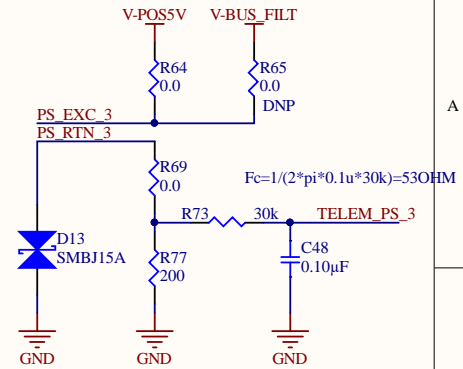
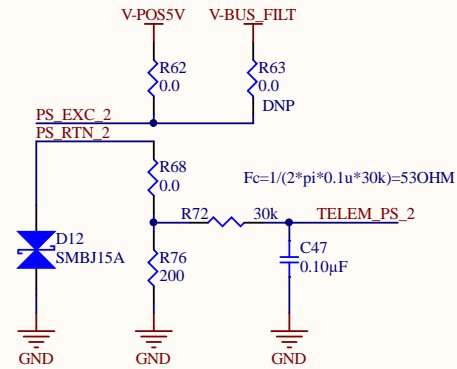
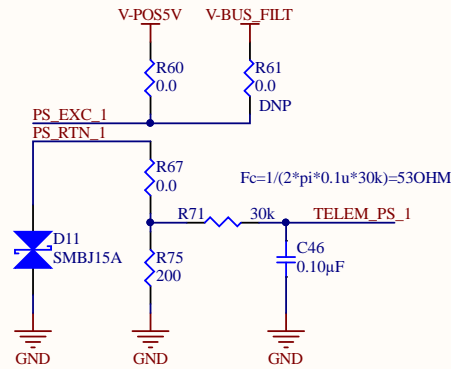
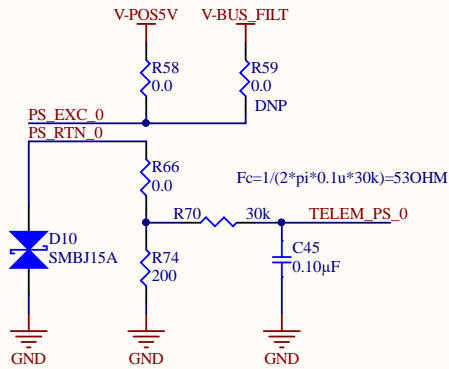
2

3

4

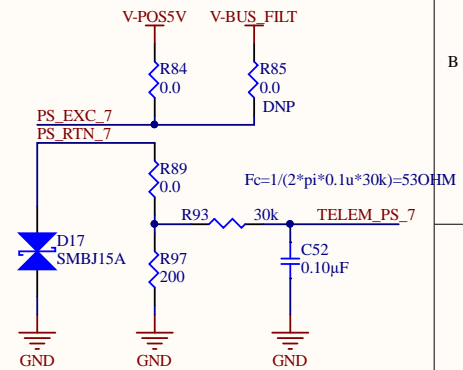
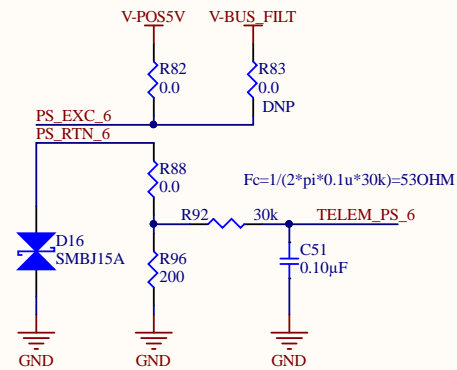
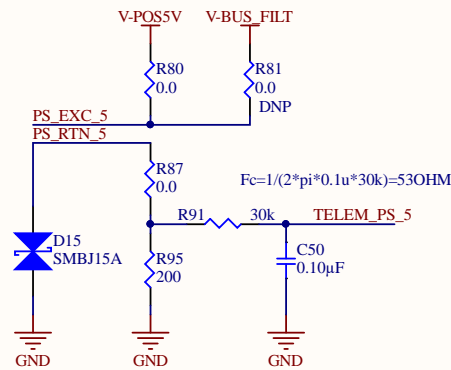
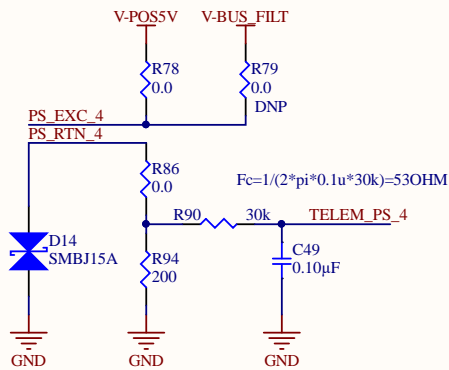
A

A



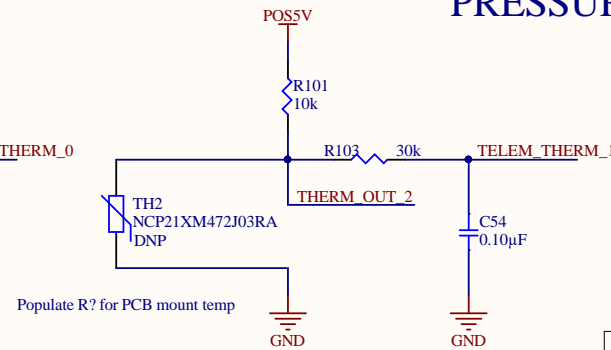
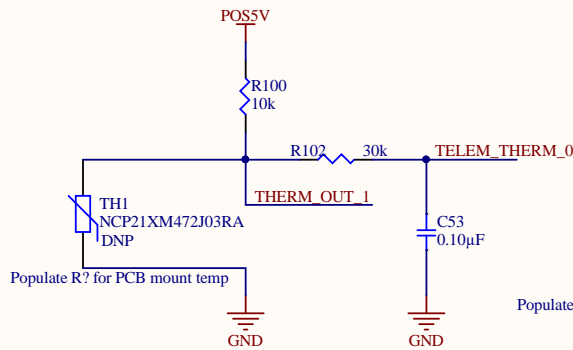
B

B



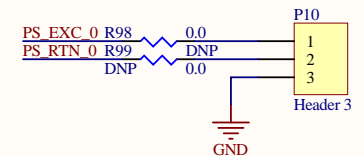
C

C



PRESSURE SENSORS

Populate Bottom resistor for current output
 Current Min Output: 4mA*200=800mV
 Current Max Output: 20mA*200=4.0V
 Voltage Min Output: 0.5V
 Voltage Max Output: 4.5V



D

D

TEMPERATURE

Title Pressure Sensors		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:		Revision:	
Date: 9/6/2019	Time: 10:42:40 PM	Sheet of	
File: pressure.SchDoc			

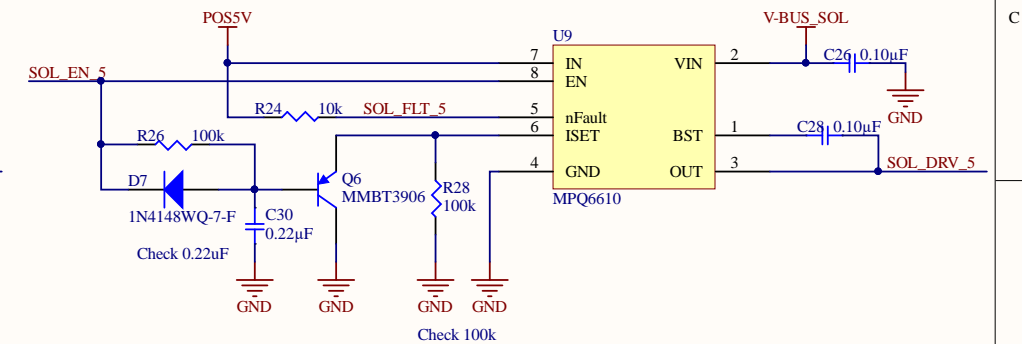
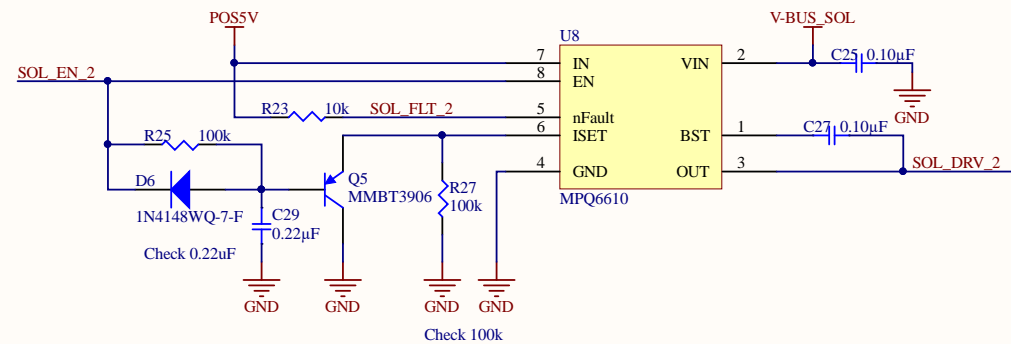
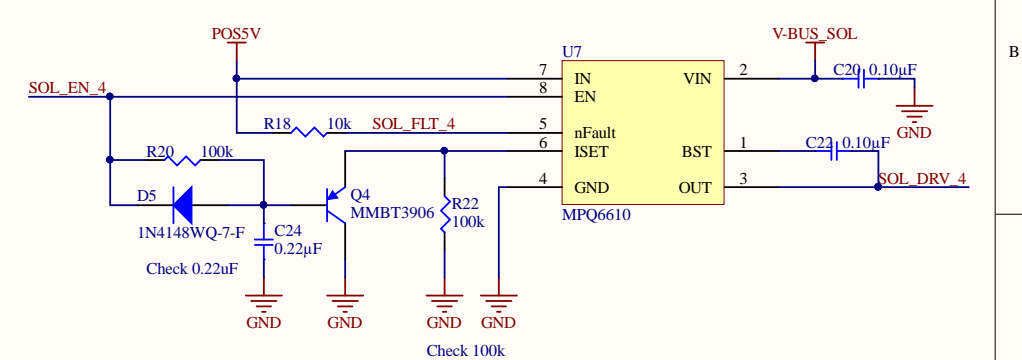
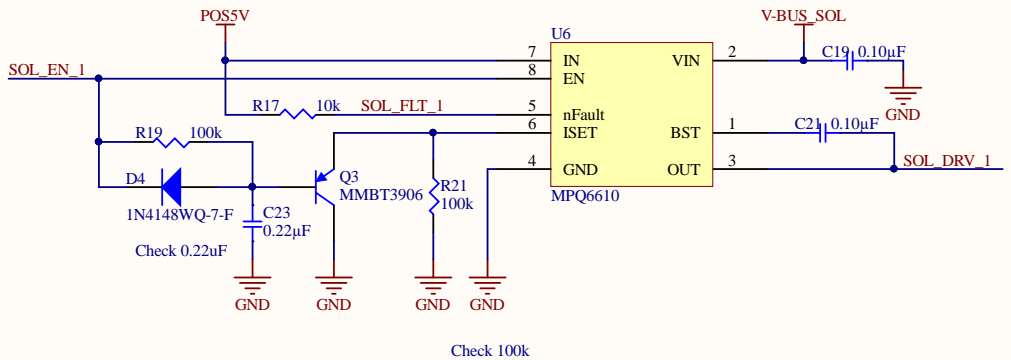
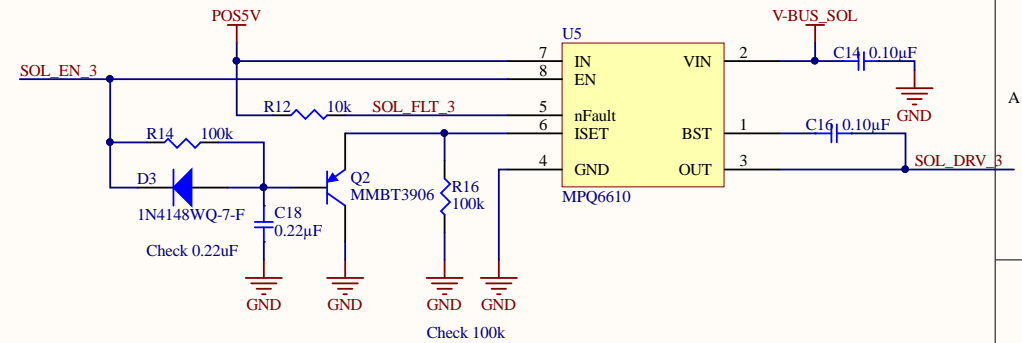
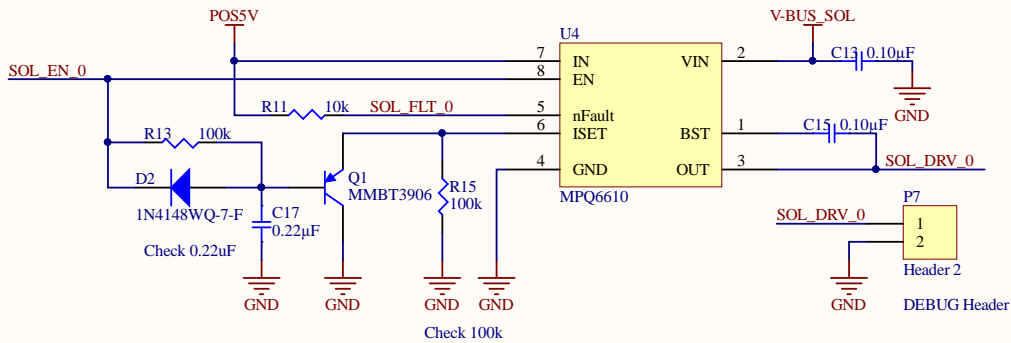
BADGER
LOOP

1

2

3

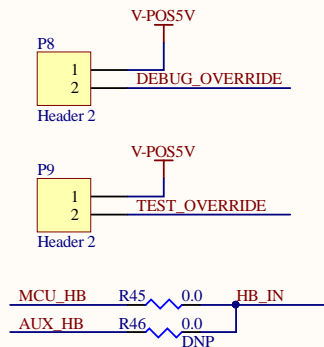
4



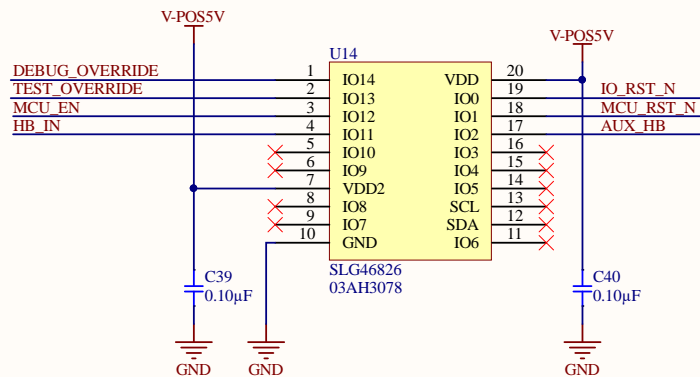
SOLENOID DRIVE

Title Solenoids		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:		Revision:	
Date: 9/6/2019	Time: 10:42:40 PM	Sheet	of
File: solenoid.SchDoc			

BADGER
LOOP



DEBUG



WATCHDOG AND RESET CONTROLLER

IO pin selection is arbitrary. Can be adjusted internally for better layout
Currently- Inputs on Left, outputs on right

Modes of operation:

Debug: EN signal is always on when SLG has power

Populate Jumper 1

Test: 10Hz signal internal signal is recirculated to mimic heartbeat

Populate Jumper 2

Operation: U? expects 10Hz heartbeat. If no heartbeat for 1s after 20s Power on reset

MCP RST_N will fall and MCU RST_N will pulse for 200ms

Silego Image here:

<https://github.com/badgerloop-software/hardware/blob/master/silego/watchdog.gp6>

Silego Image PDF Outputs:

Title Watchdog		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:		Revision:	
Date: 9/6/2019	Time: 10:42:41 PM	Sheet	of
File: watchdog_SchDoc			

**BADGER
LOOP**