


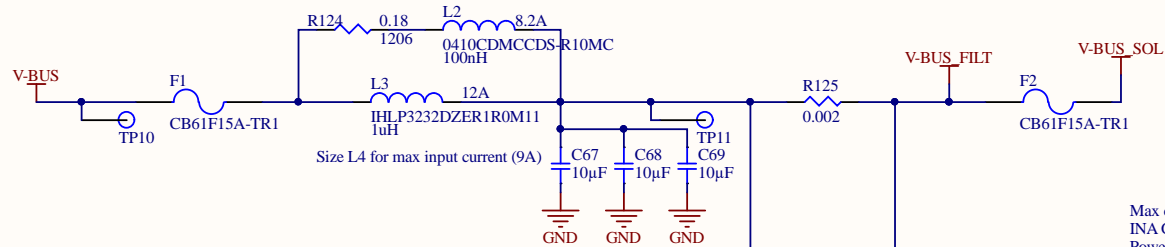
BRAKING IO

POD 5

REV 1

Title <b><i>Braking IO PCB</i></b>		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706		
Engineer:		Revision:		
Date: 9/7/2019	Time: 10:28:12 AM	Sheet of		
File: braking_io.SchDoc				

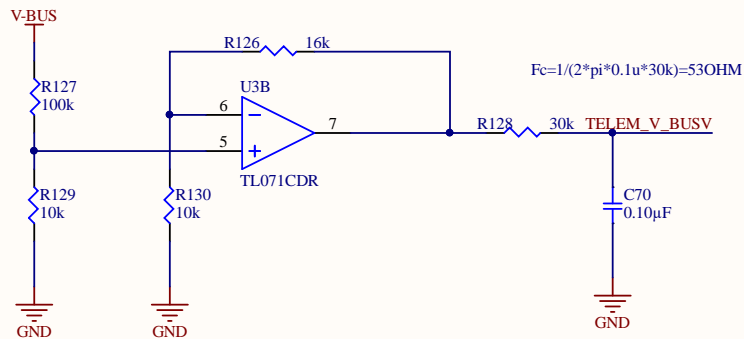
should change upstream fuse to be higher current rating than downstream.



## BUS\_FILTER

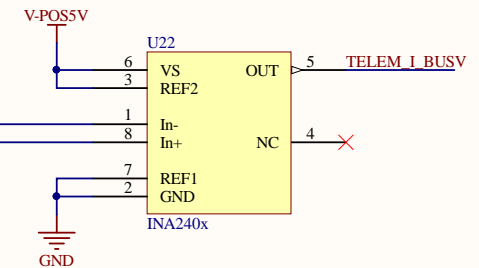
Filter design reference: <http://www.ti.com/lit/an/snva538/snva538.pdf>  
<http://ece.colorado.edu/~rwe/papers/APEC99.pdf>

Max current draw: 9A ->  $9A * 0.01\Omega = 0.09V$   
 INA Gain: 200V/V -> 4.0V at Max current  
 Power:  $I^2 * R = 4A * 0.01 = 0.04W$




GAIN: 1.6V/V  
 MIN BUS VOLTAGE: 20V -> 1.82V  
 MIN BUS VOLTAGE: 28V -> 2.54V

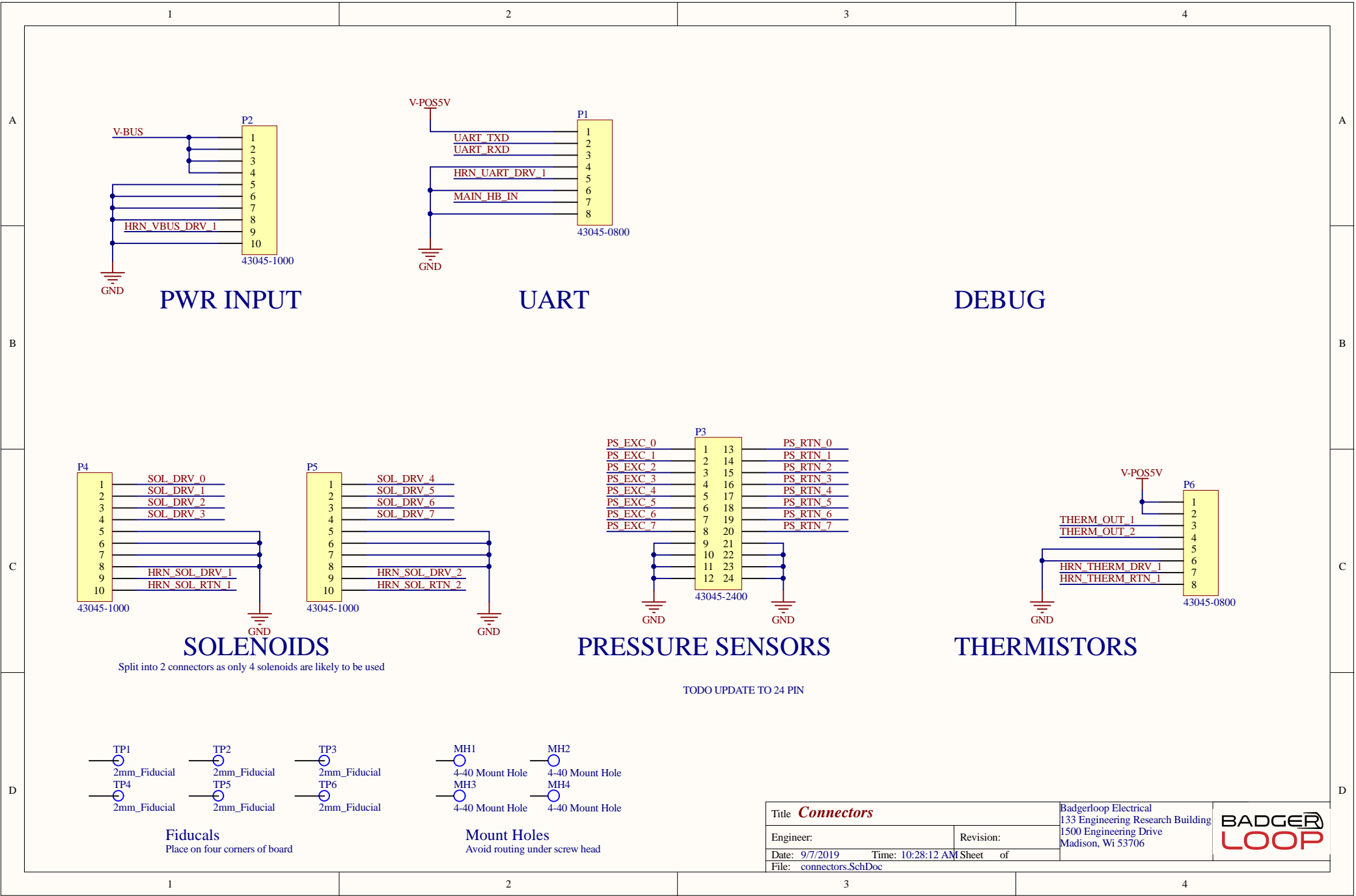
## VOLTAGE TELEMETRY



## CURRENT TELEM

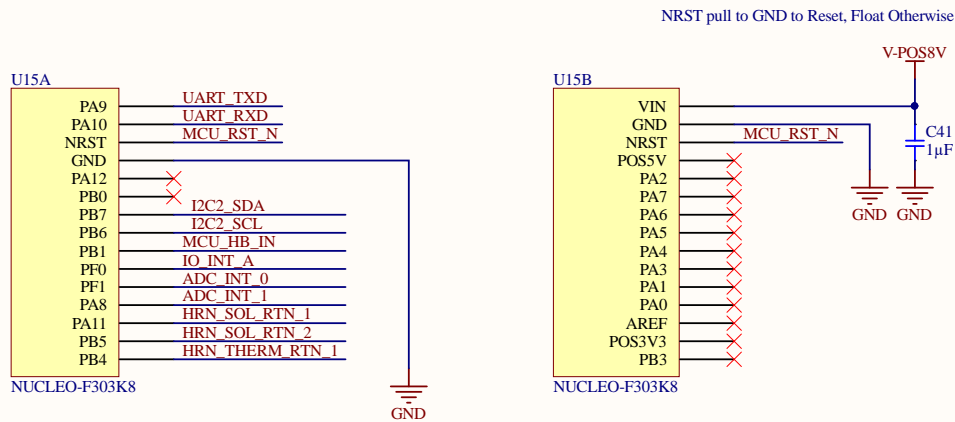
Max current draw: 9A ->  $9A * 0.002\Omega = 0.018V$   
 INA Gain: 200V/V -> 3.6V at Max current  
 Power:  $I^2 * R = 4A * 0.01 = 0.04W$

Title			Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:		Revision:		
Date: 9/7/2019	Time: 10:28:12 AM	Sheet of		
File: bus_filter.SchDoc				

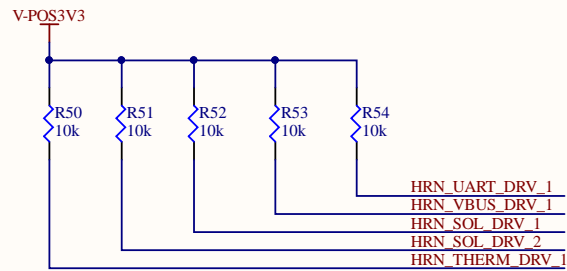


Title <b>Connectors</b>		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:	Revision:	Sheet of	
Date: 9/7/2019	Time: 10:28:12 AM		
File: connectors.SchDoc			

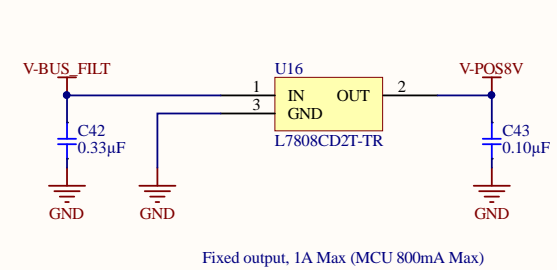




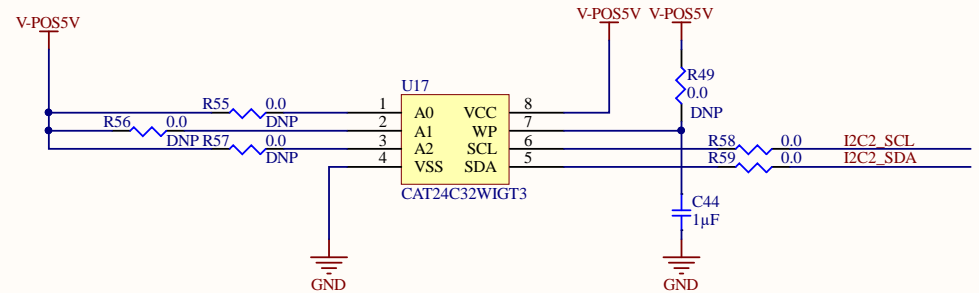
## MCU BREAKOUT



## HARNESS ID



## 8V LDO



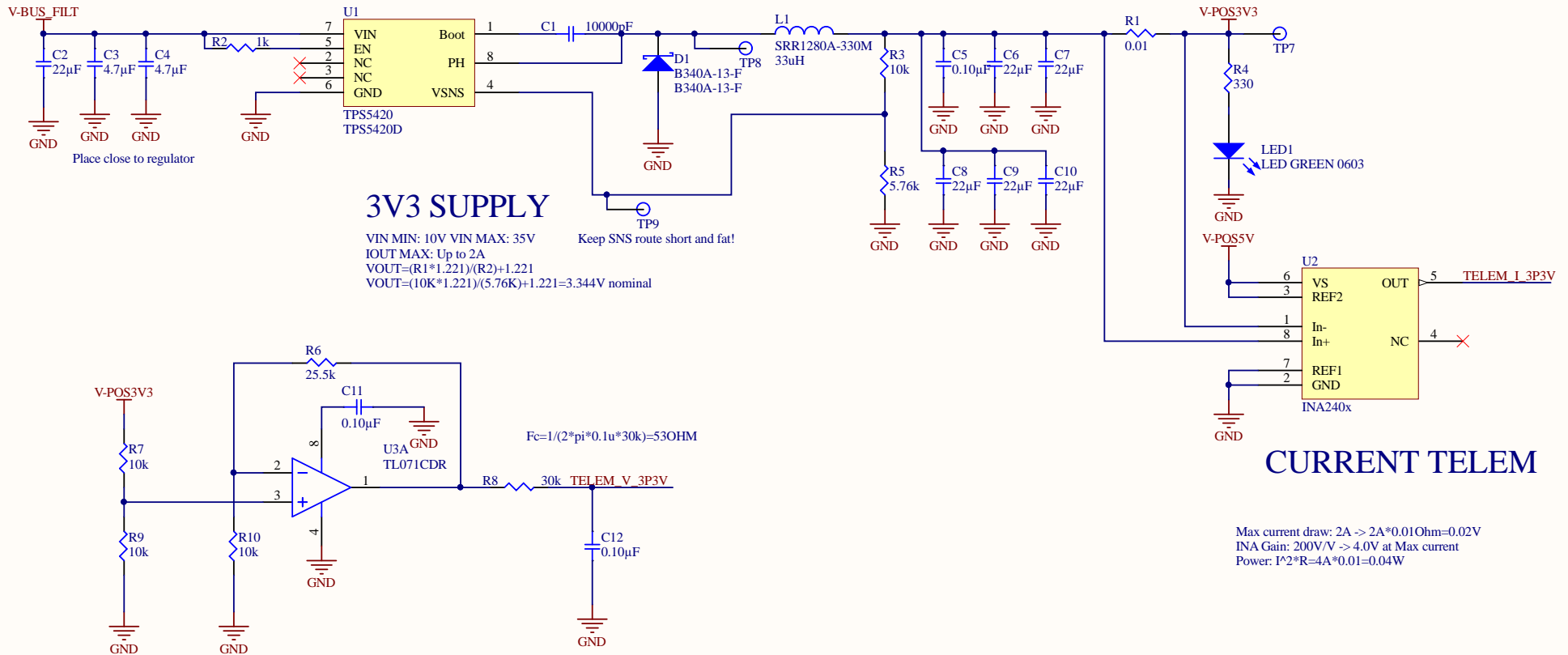
## I2C EEPROM

Title <b>Microcontroller</b>		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:	Revision:		
Date: 9/7/2019	Time: 10:28:12 AM		
File: mcu.SchDoc	Sheet of		

Notes:  
Follow layout reference design  
Place bypass caps close to regulator  
Keep hot loops as short as possible  
Possible to replace ceramic bulk cap with a tantalum.

Replace with Tantalum?  
Place close to regulator  
See [https://github.com/badgerloop-software/hardware/tree/master/braking\\_io/design](https://github.com/badgerloop-software/hardware/tree/master/braking_io/design)

TODO: Spec new inductor



GAIN: 2.55 -> MAX ADC VOLTAGE 4.21V

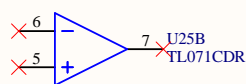
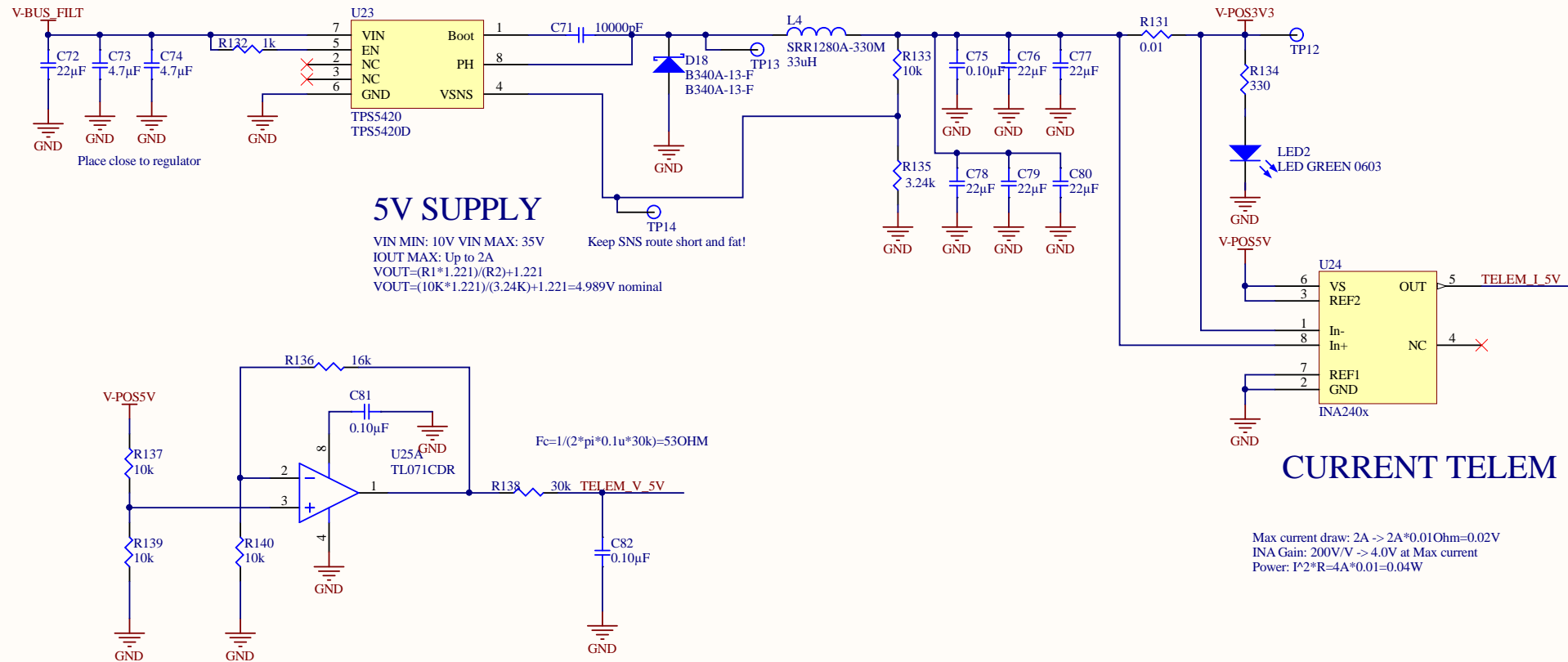
## VOLTAGE TELEMETRY

Title <b>3V3 SUPPLY</b>		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:	Revision:	Sheet of	
Date: 9/7/2019	Time: 10:28:12 AM		
File: power_3V3.SchDoc		BADGER LOOP	

Notes:  
Follow layout reference design  
Place bypass caps close to regulator  
Keep hot loops as short as possible  
Possible to replace ceramic bulk cap with a tantalum.


Replace with Tantalum?  
Place close to regulator  
See [https://github.com/badgerloop-software/hardware/tree/master/braking\\_io/design](https://github.com/badgerloop-software/hardware/tree/master/braking_io/design)

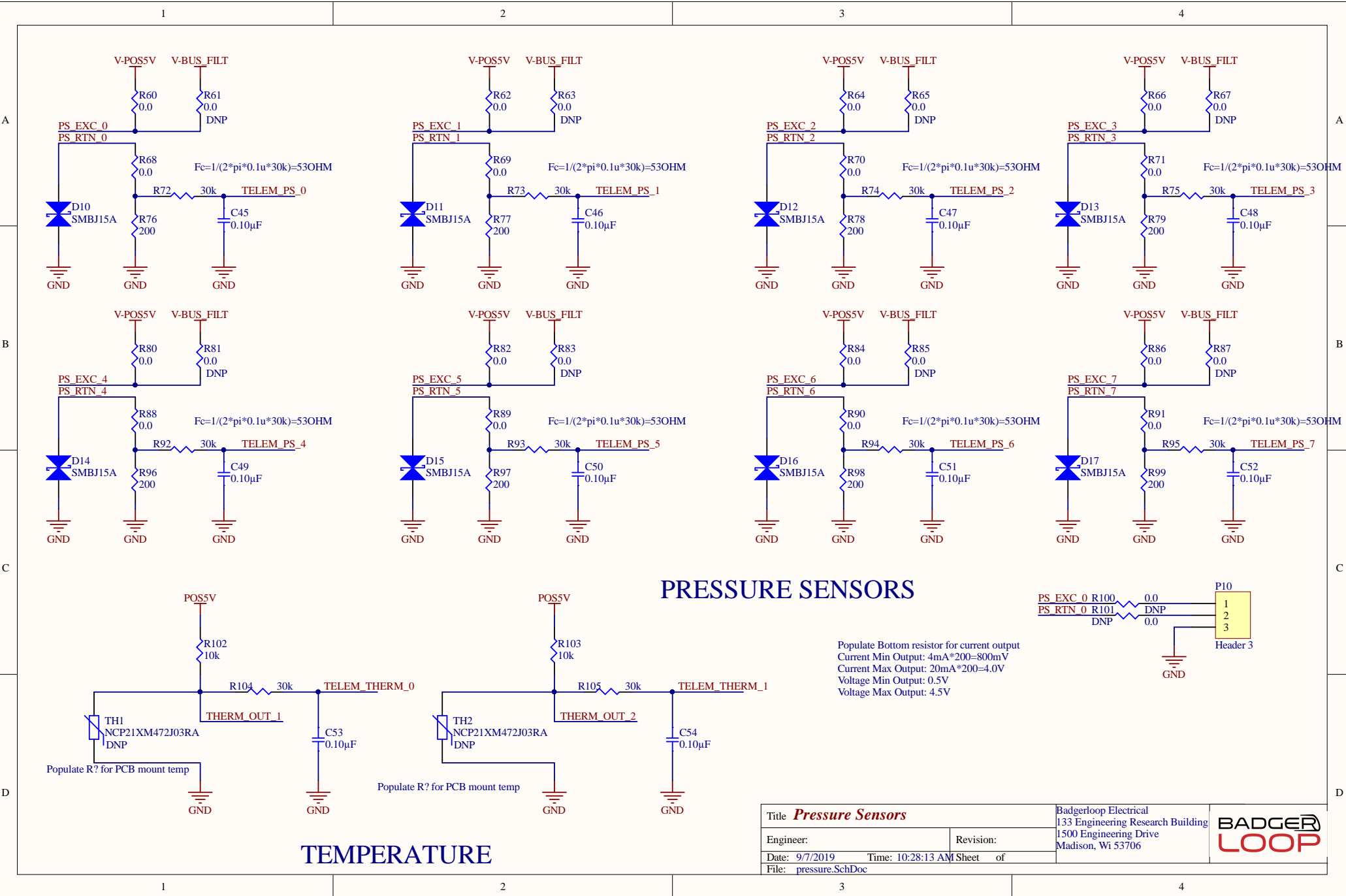
TODO: Spec new inductor

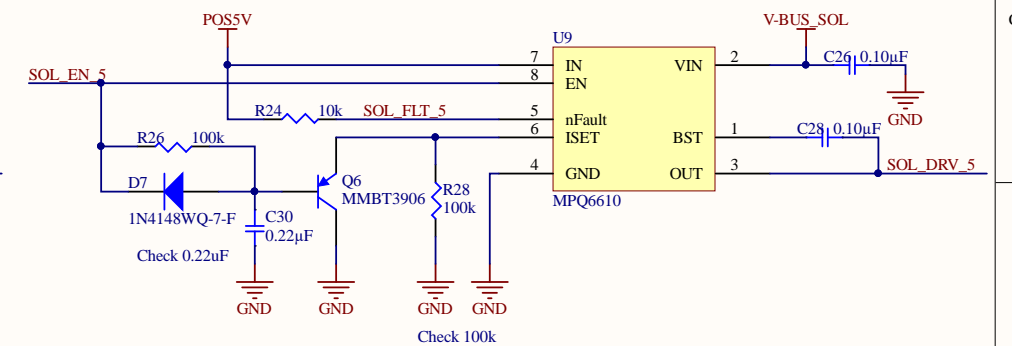
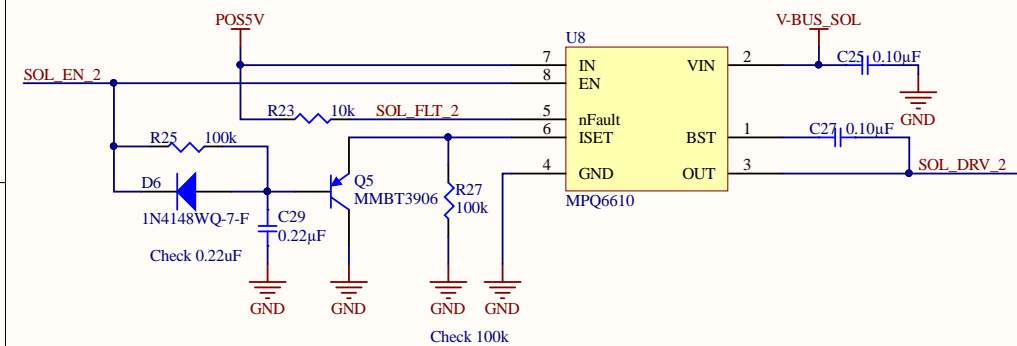
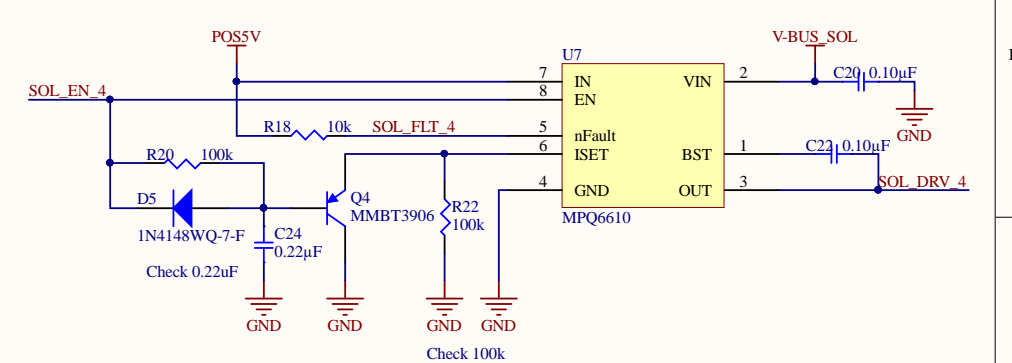
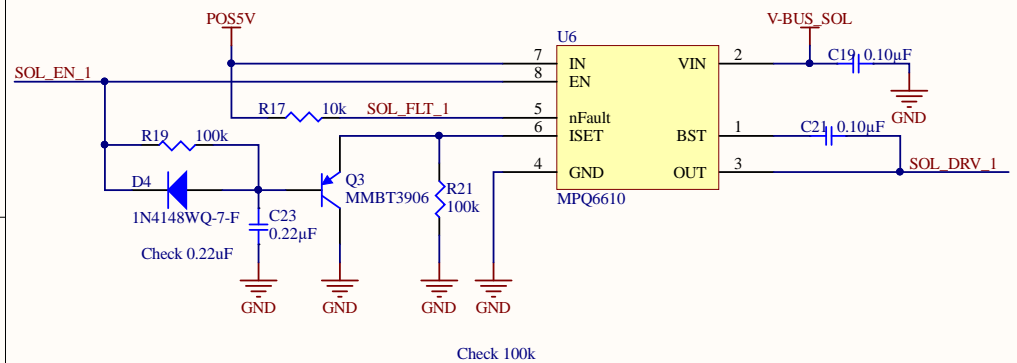
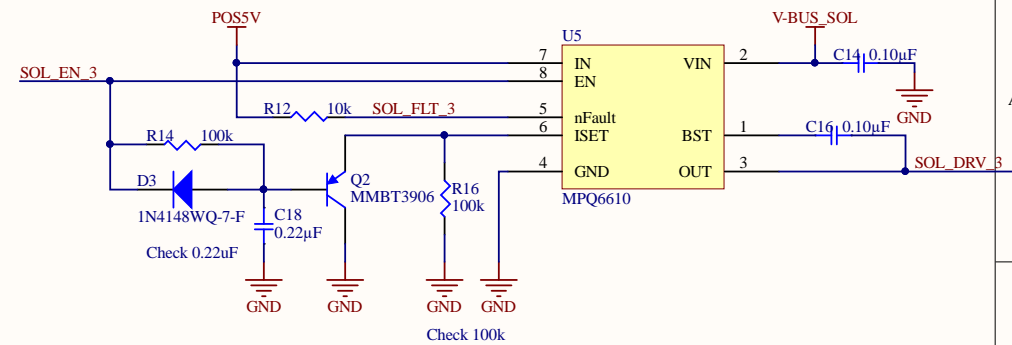
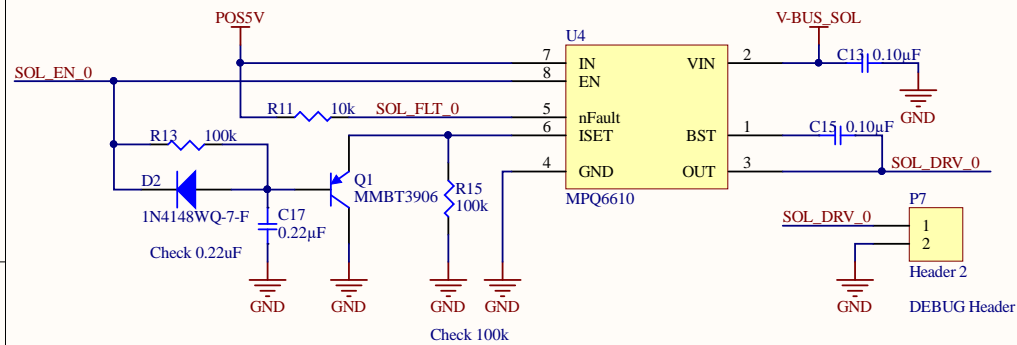


GAIN: 1.6 -> MAX ADC VOLTAGE 4.0V

## VOLTAGE TELEMETRY

Title			Badgerloop Electrical		
Engineer:		Revision:	133 Engineering Research Building		
Date: 9/7/2019		Time: 10:28:12 AM	1500 Engineering Drive		
File: power_5V.SchDoc		Sheet of	Madison, WI 53706		



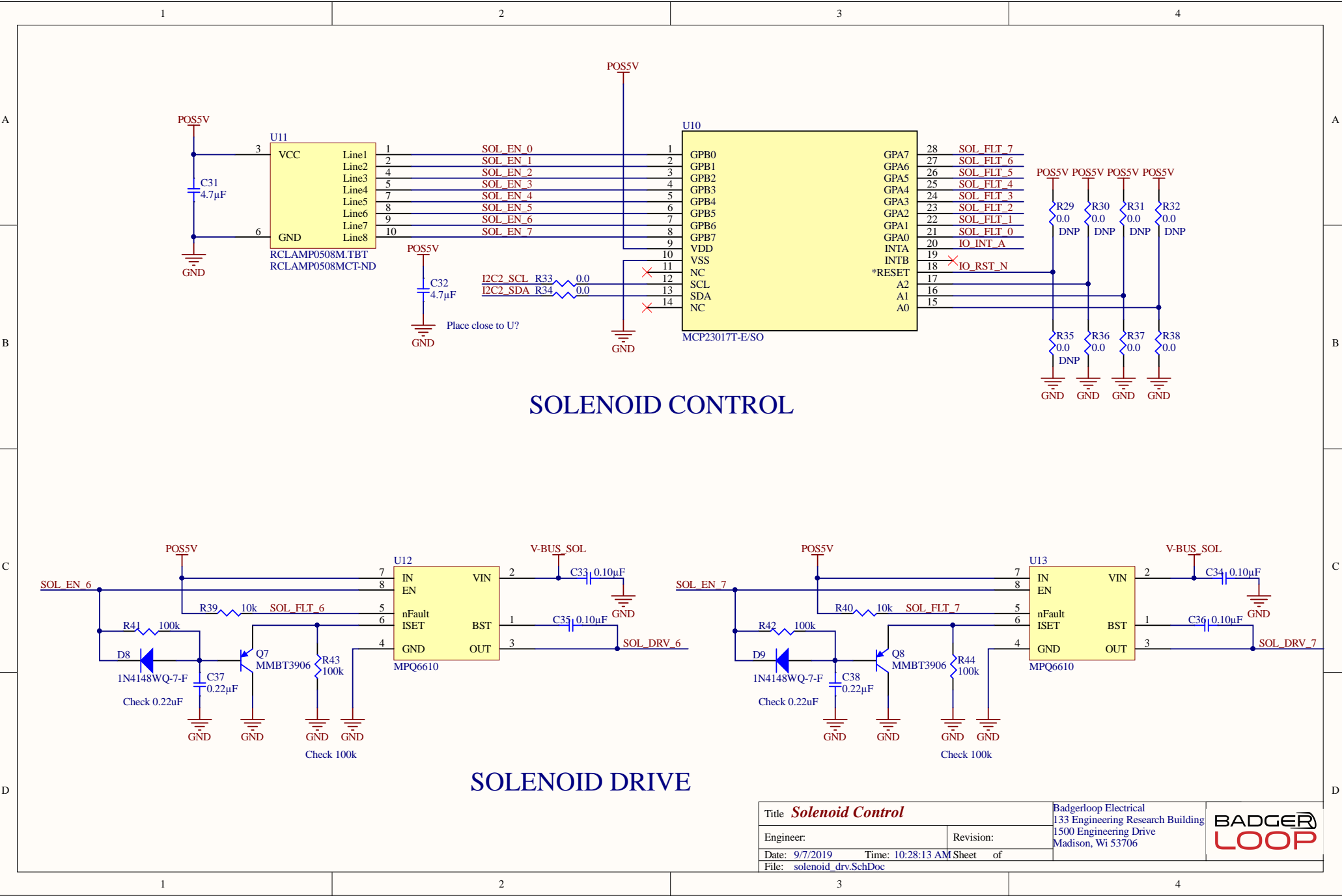


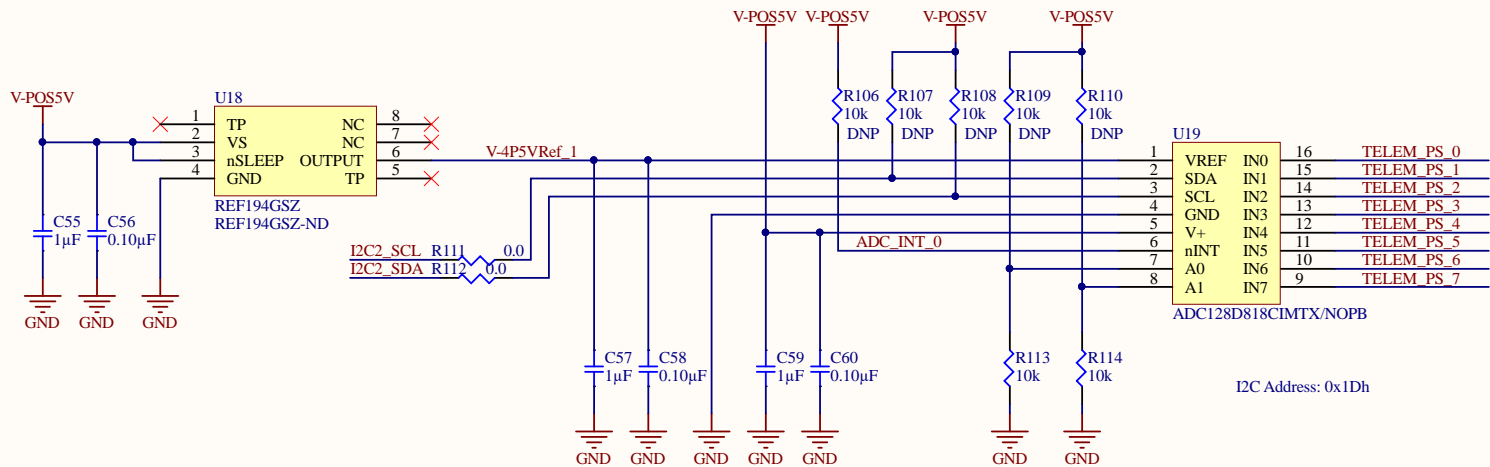
## SOLENOID DRIVE

Title <b>Solenoids</b>		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, WI 53706	
Engineer:		Revision:	
Date: 9/7/2019	Time: 10:28:13 AM	Sheet	of
File: solenoid.SchDoc			

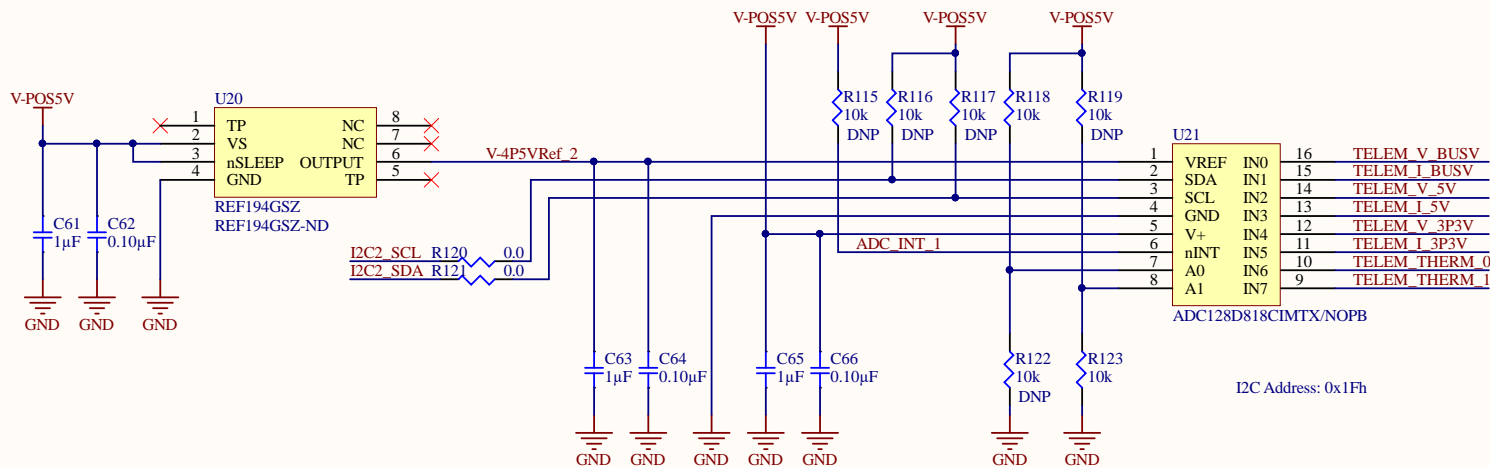
**BADGER**  
**LOOP**






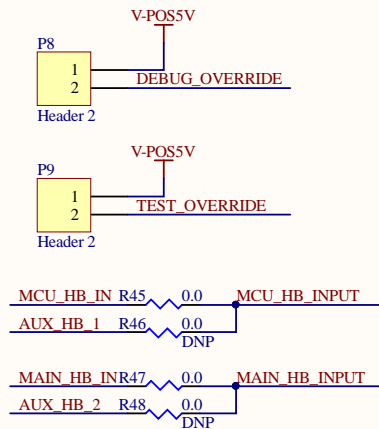


## PRESSURE

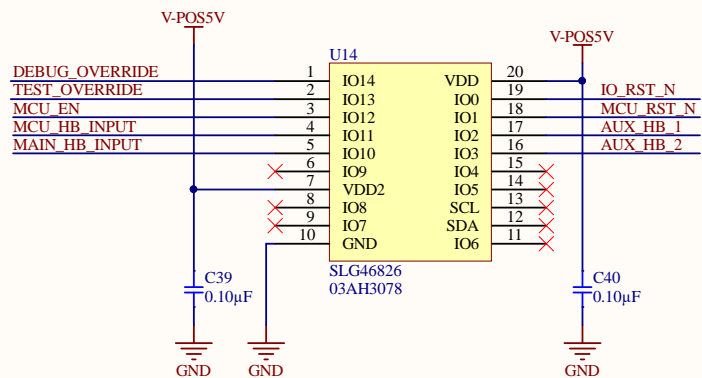


## RAIL AND TEMPERATURE

Title		Badgerloop Electrical		
Engineer:		133 Engineering Research Building		
Revision:		1500 Engineering Drive		
Date: 9/7/2019		Madison, WI 53706		
Time: 10:28:13 AM		Sheet of		
File: telemetry_adc.SchDoc				



## DEBUG



## WATCHDOG AND RESET CONTROLLER

IO pin selection is arbitrary. Can be adjusted internally for better layout  
Currently- Inputs on Left, outputs on right

### Modes of operation:

Debug: EN signal is always on when SLG has power

Populate Jumper 1

Test: 10Hz signal internal signal is recirculated to mimic heartbeat

Populate Jumper 2


Operation: U? expects 10Hz heartbeat. If no heartbeat for 1s after 20s Power on reset

MCP RST\_N will fall and MCU RST\_N will pulse for 200ms

Silego Image here:

<https://github.com/badgerloop-software/hardware/blob/master/silego/watchdog.gp6>

Silego Image PDF Outputs:

Title <b>Watchdog</b>		Badgerloop Electrical 133 Engineering Research Building 1500 Engineering Drive Madison, Wi 53706		
Engineer:		Revision:		
Date: 9/7/2019	Time: 10:28:13 AM	Sheet	of	
File: watchdog.SchDoc				