

PWR INPUT	SA.	
		P15 PN C2
	P 21 P 4 P X T X T X T X T X T X T X T X T X T X	THE IZCZ GPIOTESTT GND SDA GND X 5V
	R18 R15 P35 R36 R36 R23 R12	
3V3 GND	R13	
		R31 R30 R29 R28 R32R33R34 BADGER LOOP rocessor Breakout EV 1 A

Bopt draderyer

Layer	Name	Material	Thickness	Constant	Board Layer Stac
1	Top Overlay				
2	Top Solder	Solder Resist	0.40mil	3.5	
3	Top Layer	Copper	1.40mil		
4	Dielectric 1	FR-4	12.60mil	4.8	
5	Bottom Layer	Copper	1.40mil		
6	Bottom Solder	Solder Resist	0.40mil	3.5	
7	Bottom Overlay				

BADGER) LOP	ENGINEER: R. Castle PCB DESIGNER: R. Castle	processor_pcb.PcbDoc	
Badgerloop ERB Room 133 1400 Engineering Drive Madison, WI 53706	DATE: 1/21/2019	PART NO.: P4_LV_Processor	REV:
	FILE NAME: processor_pcb.PcbDoc	DWG NO:	SCALE: 1:1



