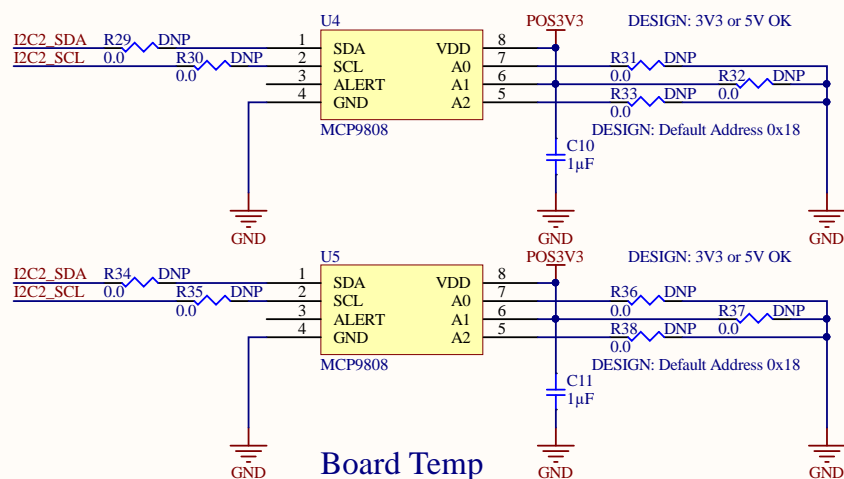
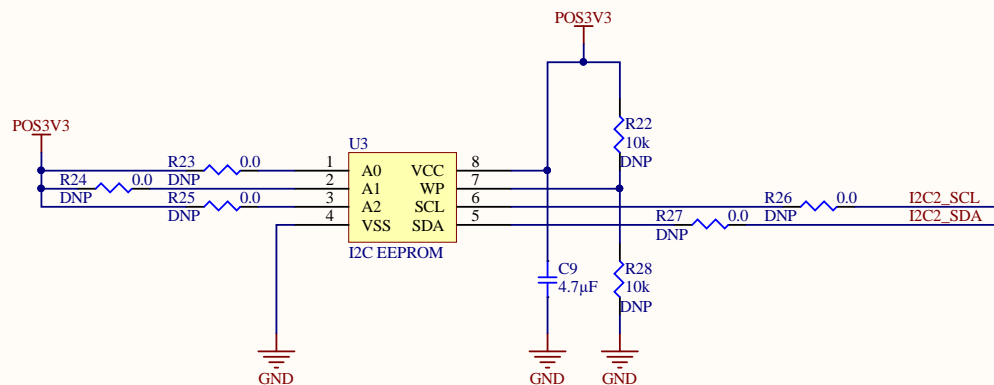


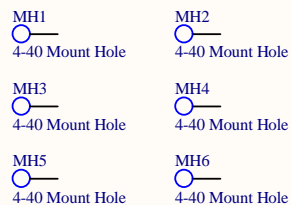
Main Connector



Board Temp




I2C EEPROM




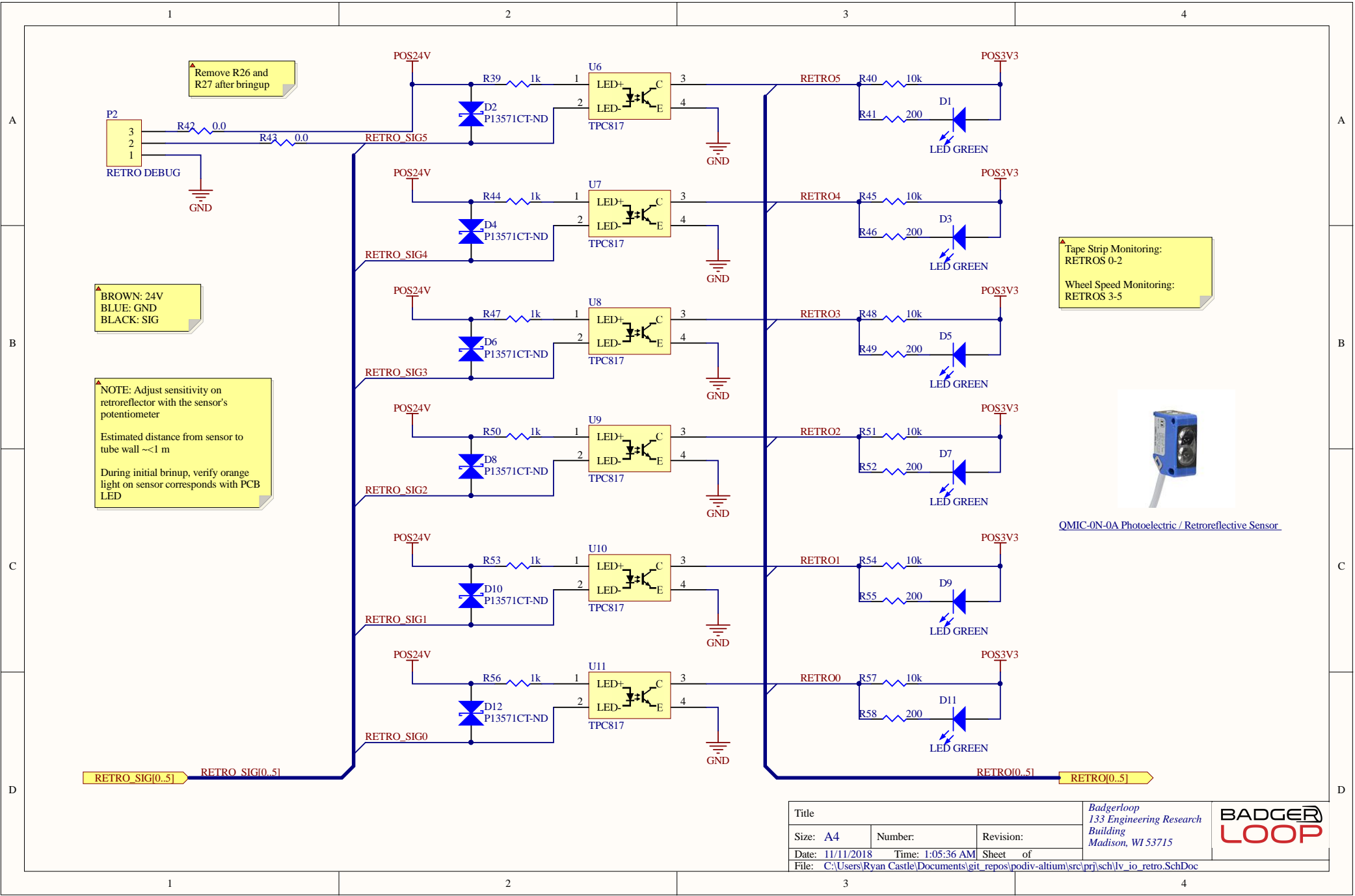
MECHANICAL

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Date: 11/11/2018	Time: 1:05:36 AM	Sheet 2 of 2	
File: C:\Users\Ryan Castle\Documents\git_repos\podiv-altium\src\prj\sch\lv_io_main_connector.SchDoc			


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B				B
C				C
D				D
1	2	3	4	


Title			Badgerloop 133 Engineering Research Building Madison, WI 53715	
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Title			Badgerloop 133 Engineering Research Building Madison, WI 53715	
Size: A4	Number:	Revision:		
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1	2	3	4	
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B				B
C				C
D				D
1	2	3	4	

Title			<i>Badgerloop</i> <i>133 Engineering Research</i> <i>Building</i> <i>Madison, WI 53715</i>	
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Date: 11/11/2018	Time: 1:05:37 AM	Sheet of		
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Title			<i>Badgerloop</i> <i>133 Engineering Research</i> <i>Building</i> <i>Madison, WI 53715</i>	
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