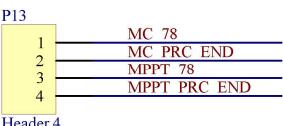
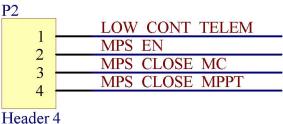
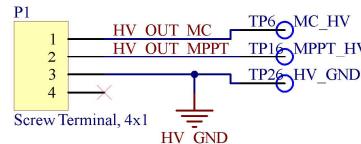
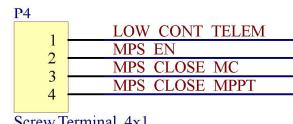
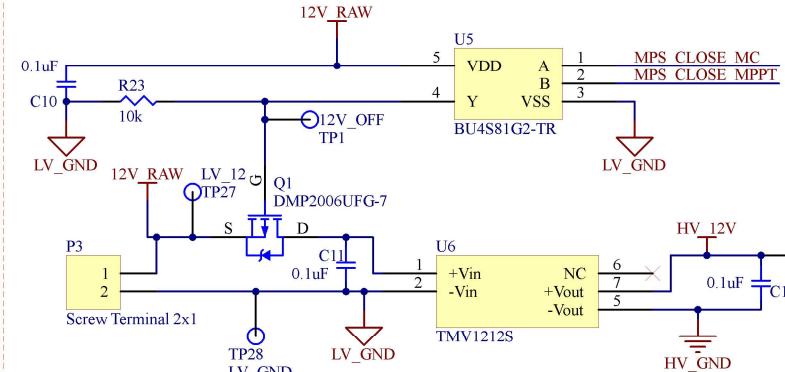
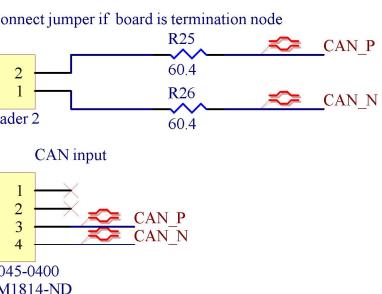
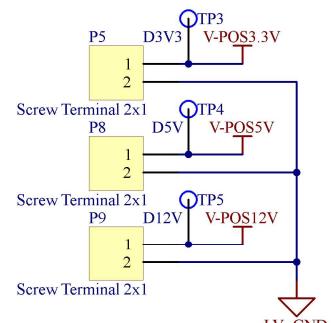
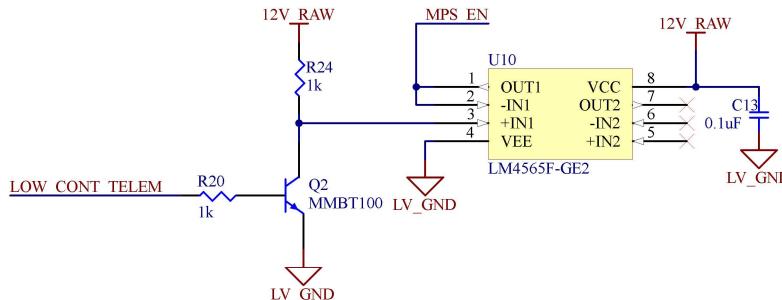


#### Title **Precharge Breakout**

Engineer: \* Revision: 1  
 Date: 2/6/2023 Time: 3:03:04 PM Sheet 1 of 4  
 File: ic\_madness.SchDoc

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**LOOP**

**A Test Signals****High Voltage Input****Input / Output****Low Voltage Input****CAN Connections****Even Lower Voltage (Debug)****Signal Inverter****Title *Precharge Breakout***

Engineer: \*

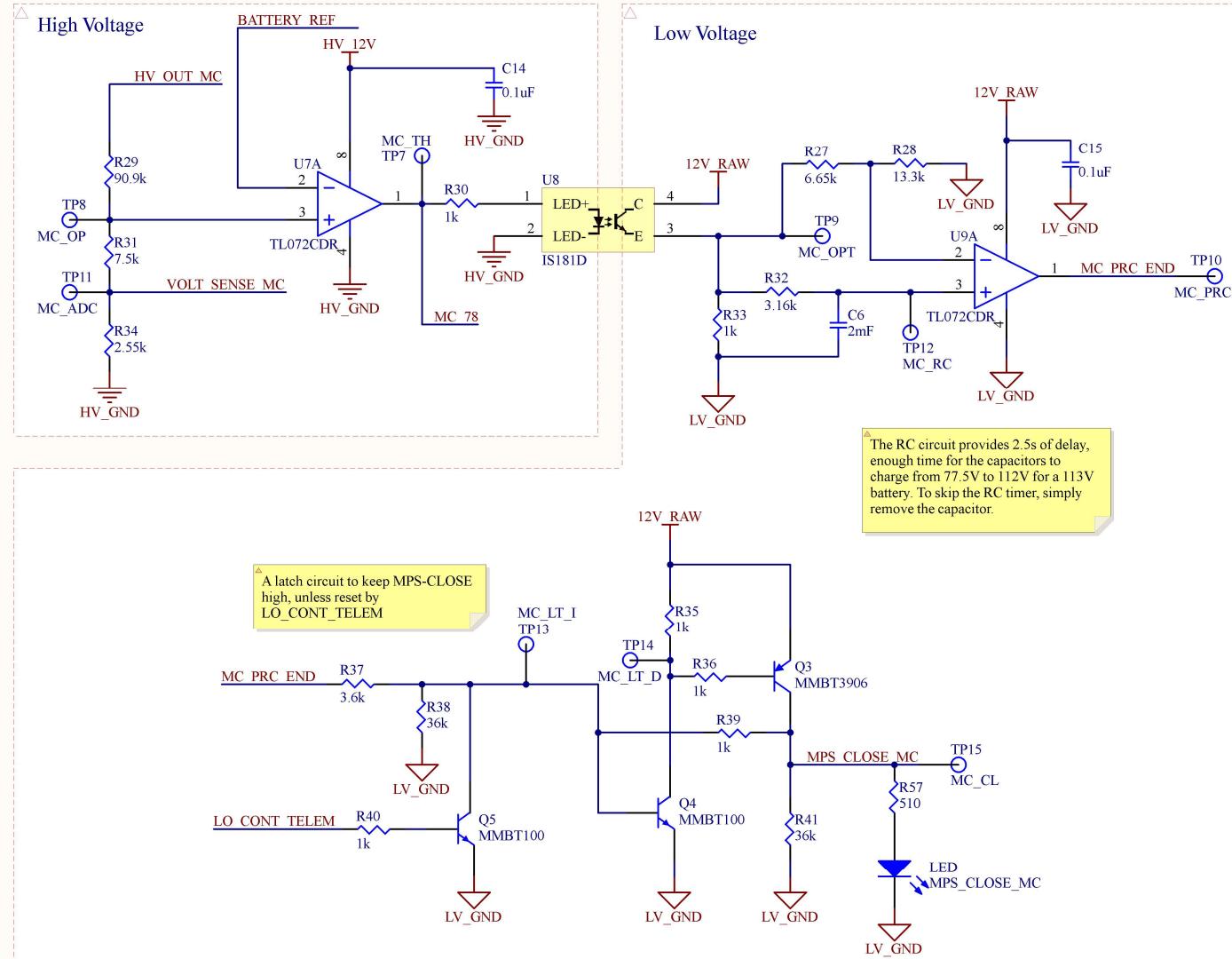
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Madison, WI 53706**BADGER**  
**LOOP**

Revision: \*

Date: 2/6/2023 Time: 3:03:04 PM

Sheet 2 of 4

File: connectors.SchDoc

Title **Precharge Breakout**

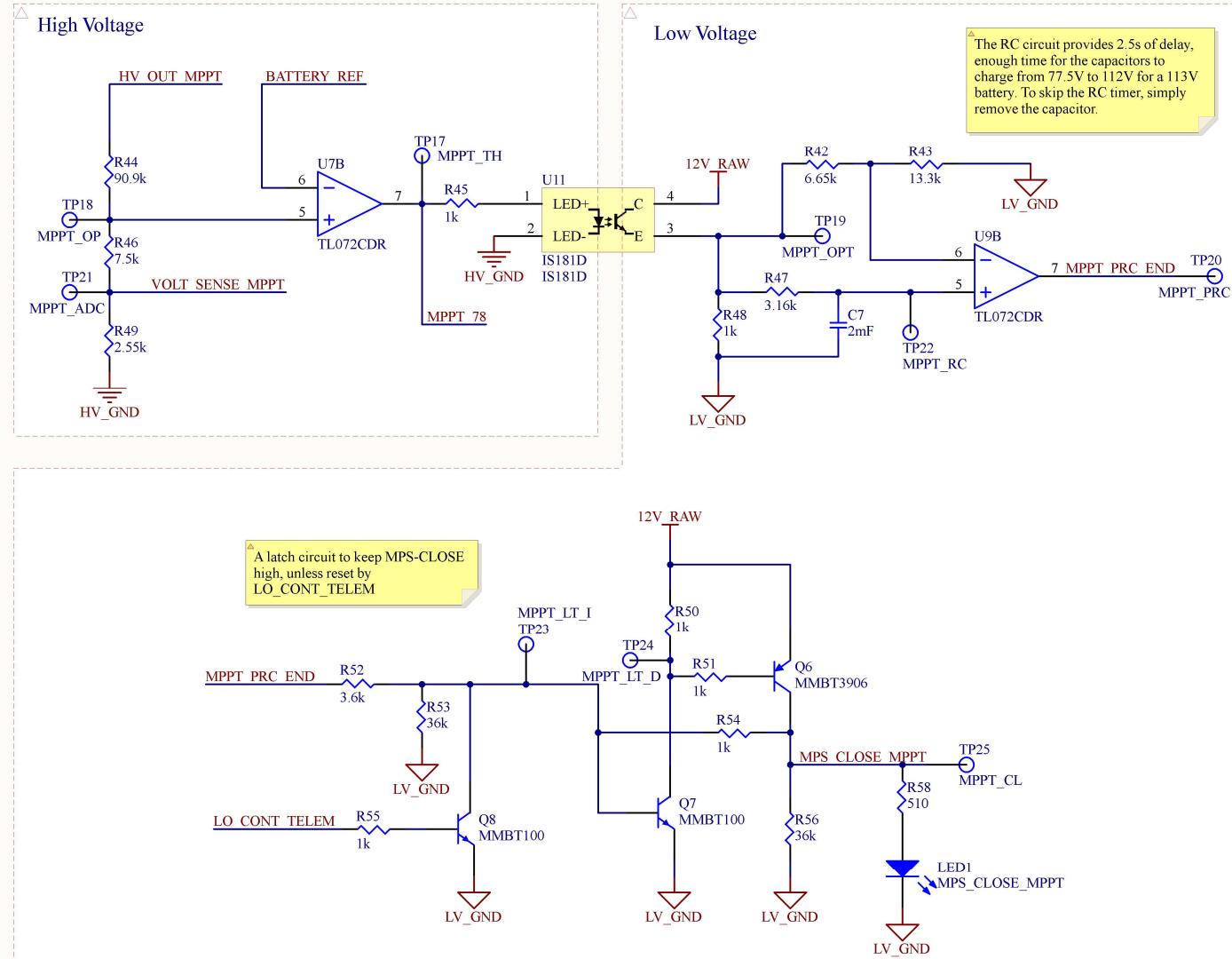
Engineer: \* Revision: 1

Date: 2/6/2023 Time: 3:03:05 PM

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Title **Precharge Breakout**

Engineer: \* Revision: \*

Date: 2/6/2023 Time: 3:03:05 PM

File: mpppt\_signals.SchDoc

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