

# BOARD TEMPLATE

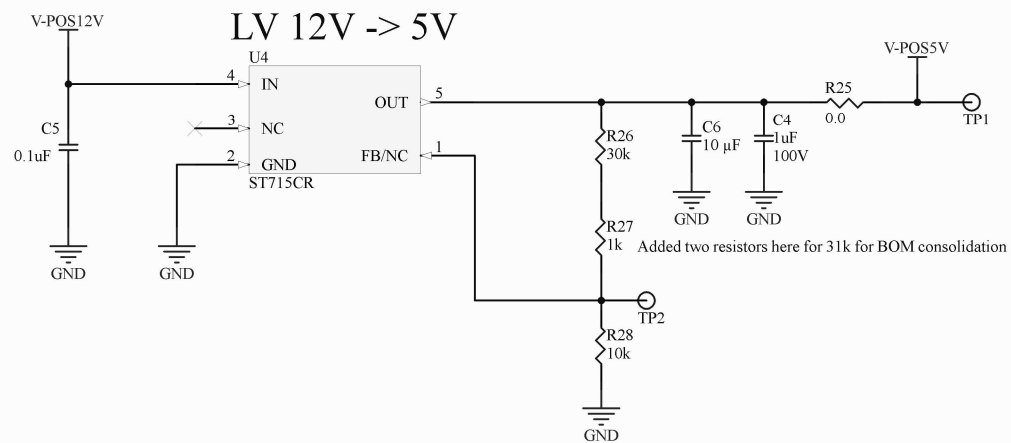
## SOLAR CAR 2021

### REV 1

# DO NOT EDIT

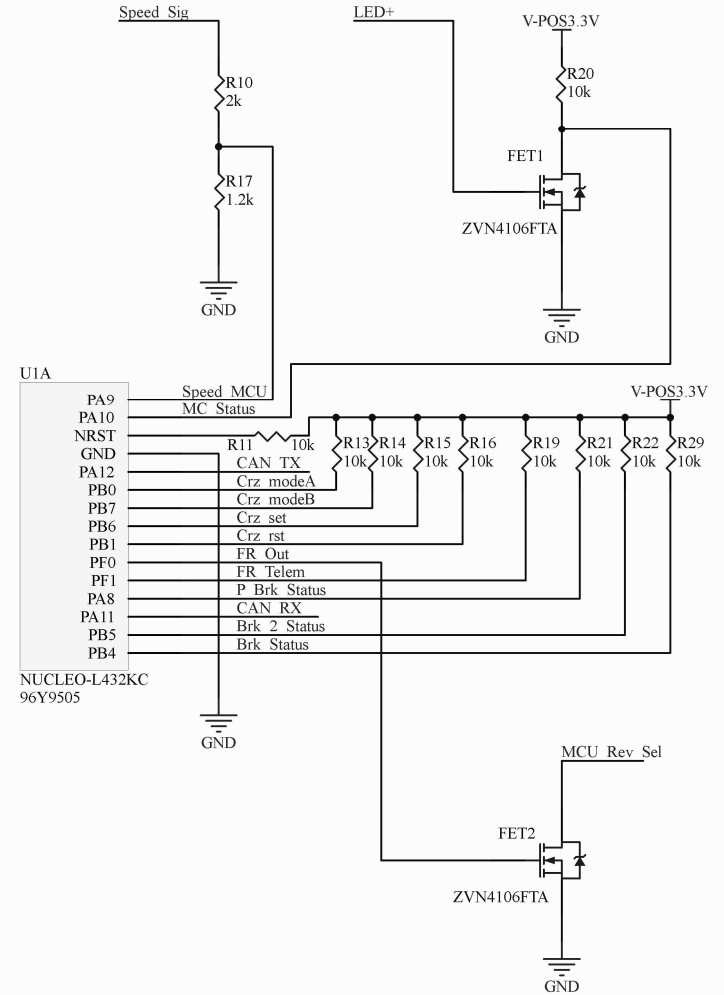
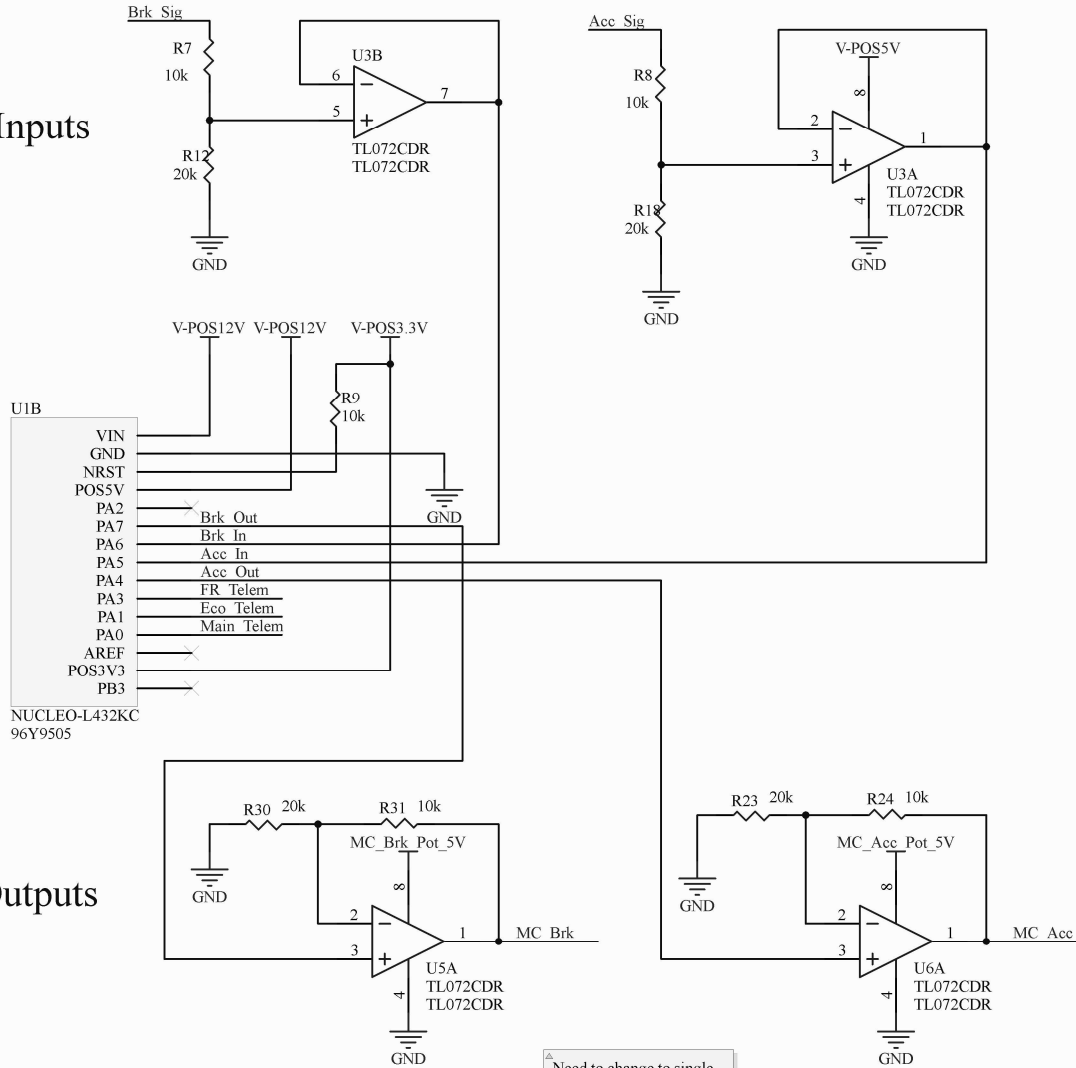
Title <b>TEMPLATE PCB COVER</b>			<b>BADGER LOOP</b>
Engineer:		Revision:	
Date: 4/6/2023	Time: 9:19:32 PM	Sheet 1 of 2	
File: cover.SchDoc			

Badgerloop Electrical  
133 Engineering Research Building  
1500 Engineering Drive  
Madison, WI 53706



Title <b><i>Power</i></b>		* * *	BADGER LOOP
Engineer: S. Riggleman, M. Farah	Revision:*		
Date: 4/6/2023	Time: 9:19:33 PM		
File: Power.SchDoc	Sheet2 of 2		

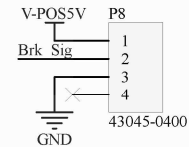
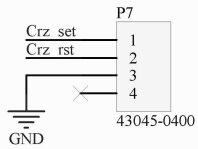
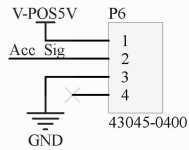
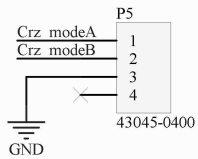
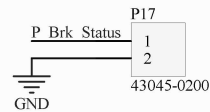
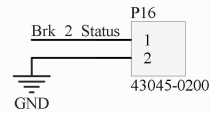
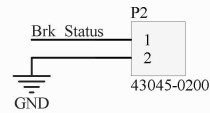
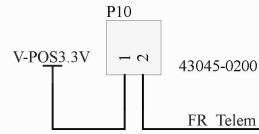
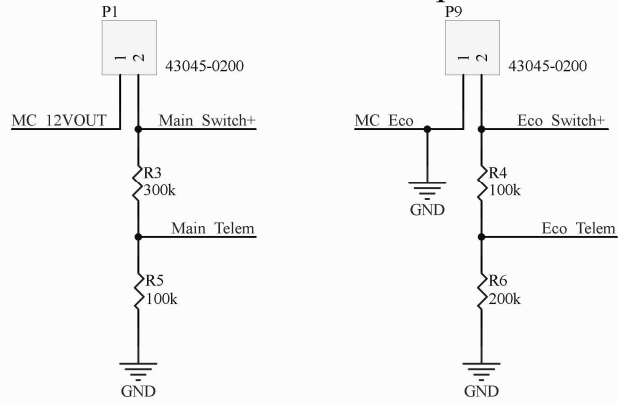
## Inputs



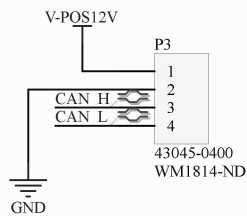
Title <i>Nucleo &amp; Serial</i>		*
Engineer: M. Farah, S. Riggleman	Revision:*	*
Date: 4/6/2023	Time: 9:19:33 PM	Sheet2 of 2
File: Nucleo.SchDoc		*

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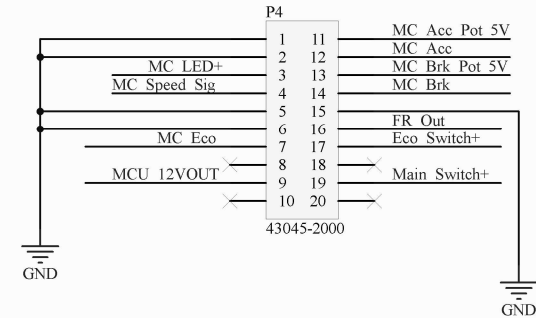
## Driver Inputs



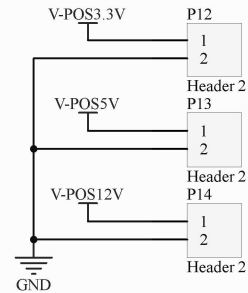
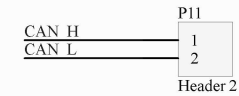
## Main IO



## Motor Controller Interface

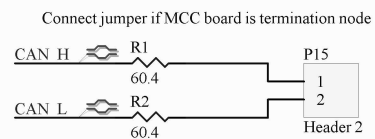



## Debug Headers



Title <b>Connectors</b>		*	
Engineer: McGregor Farah		Revision:*	
Date: 4/6/2023	Time: 9:19:33 PM	Sheet 2	of 2
File: Connectors.SchDoc			

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Title *		
Engineer: *	Revision:*	
Date: 4/6/2023      Time: 9:19:34 PM	Sheet2 of 2	
File: CAN.SchDoc		

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**LOOP**