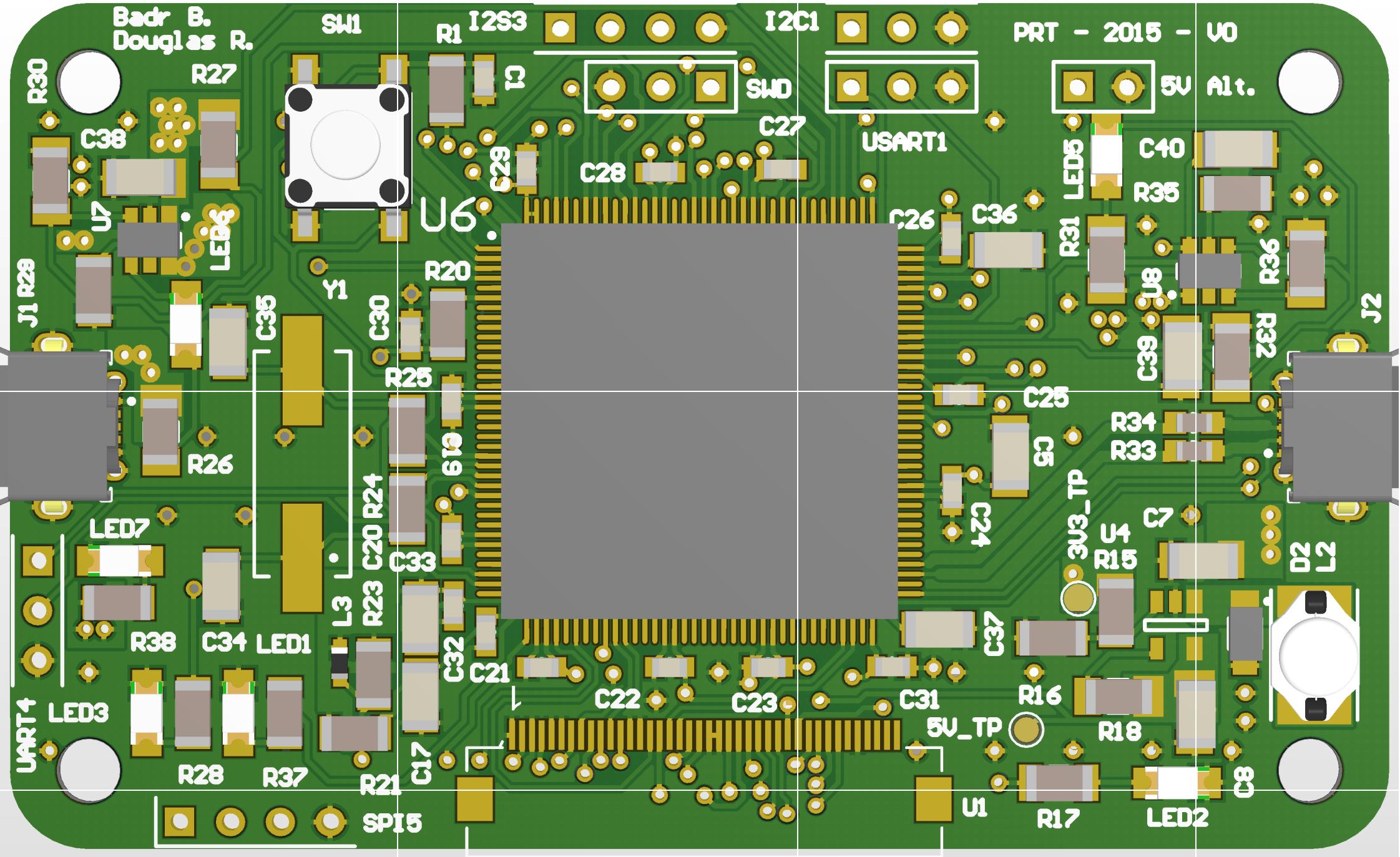
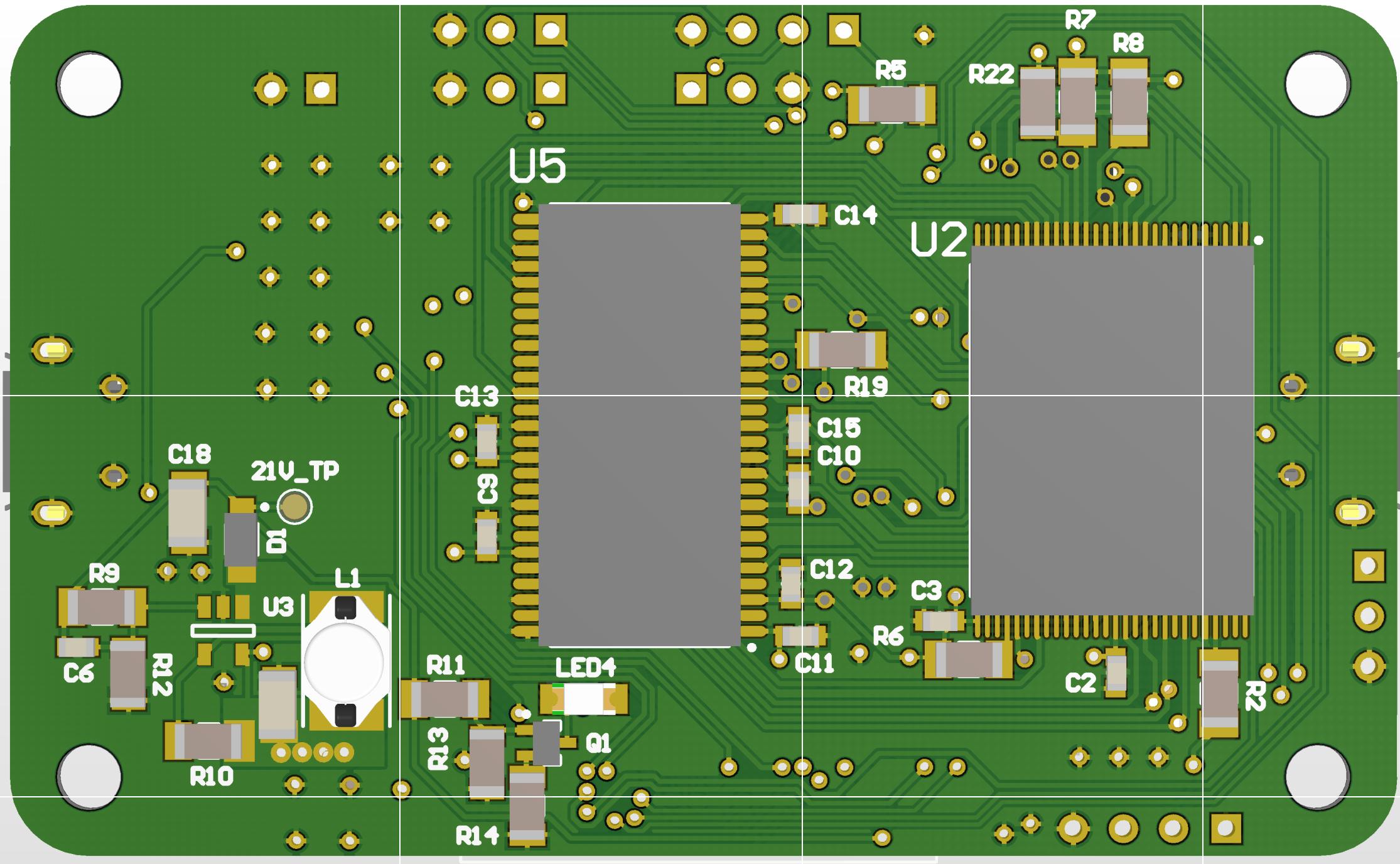
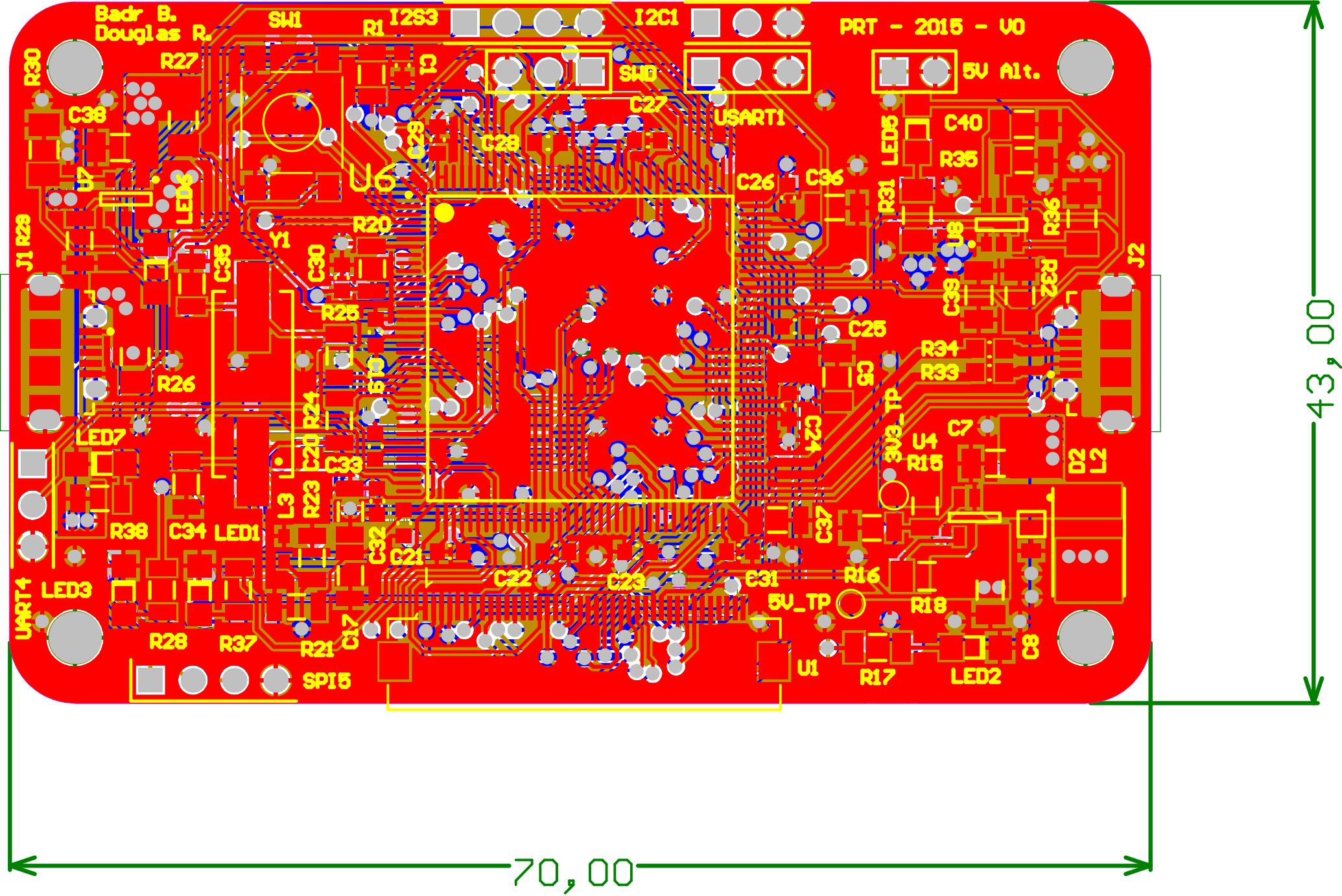


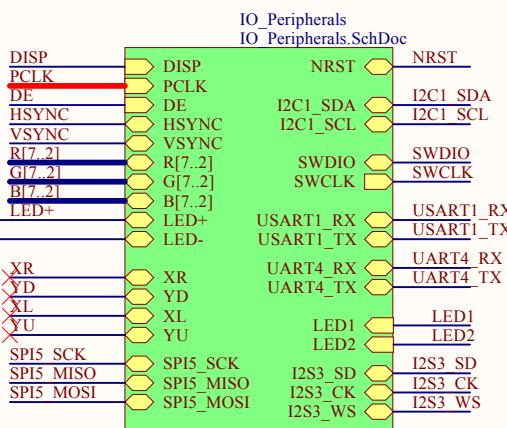
Badr B.
Douglas R.



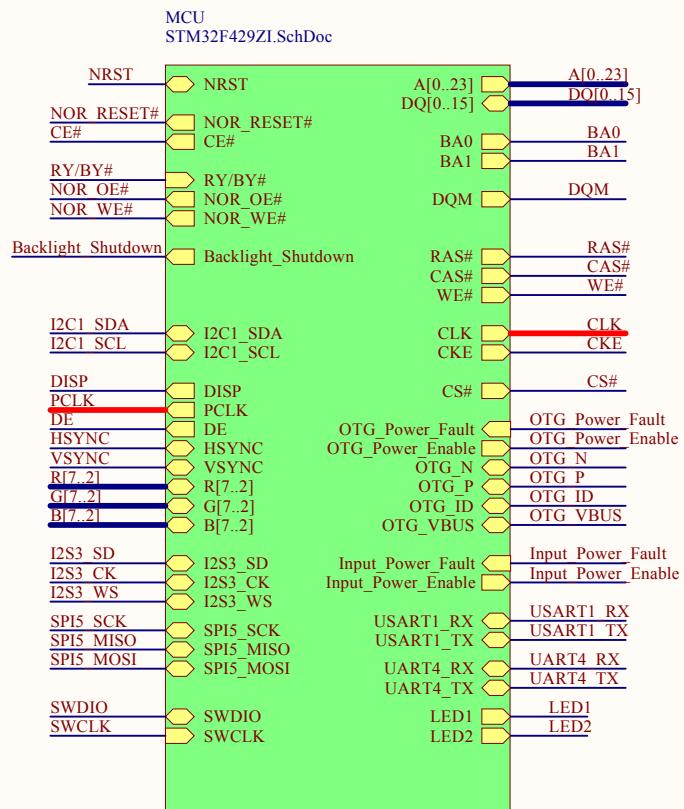




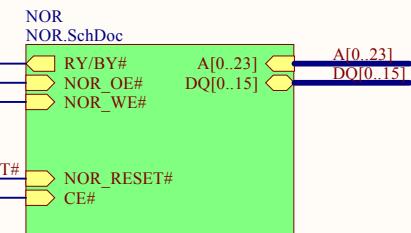
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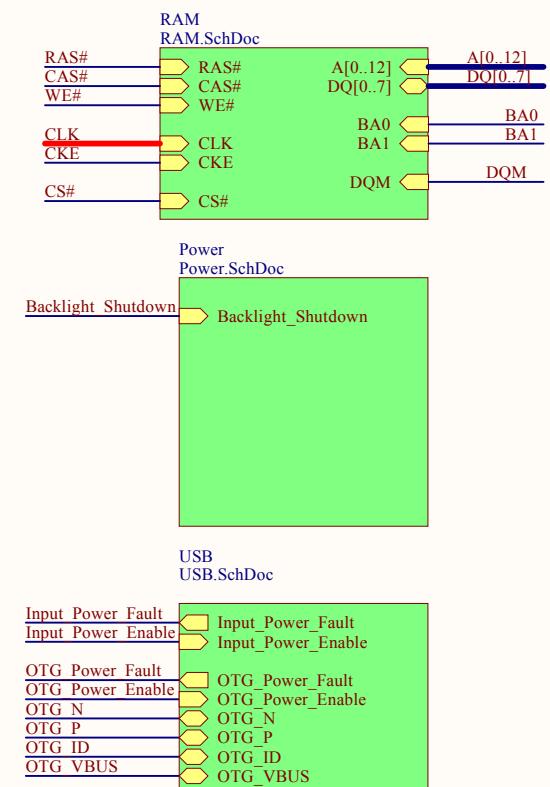
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C



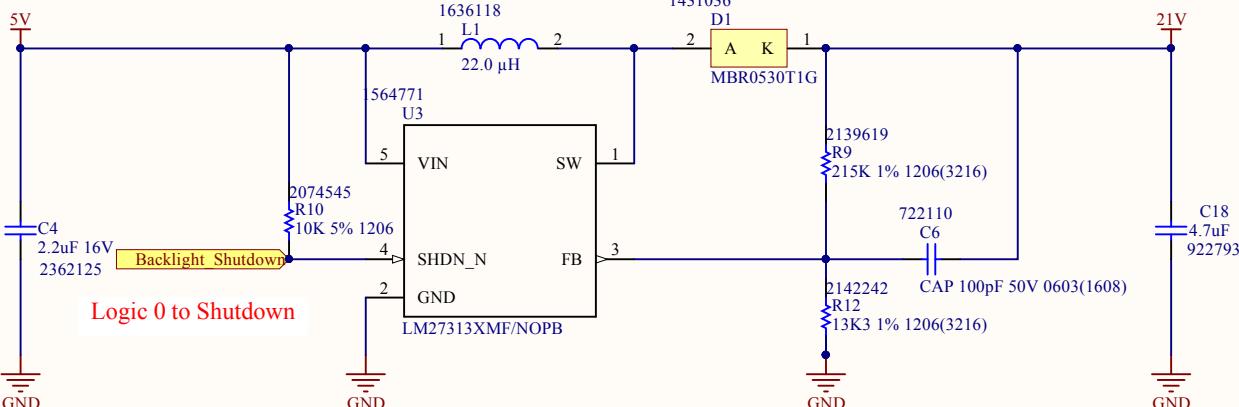
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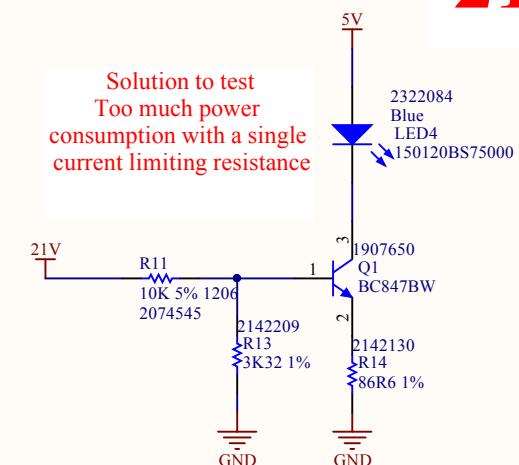
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Size: A4	Number:*	Revision:1.0
Date: 20/07/2015	Time: 18:53:21	Sheet1 of 7
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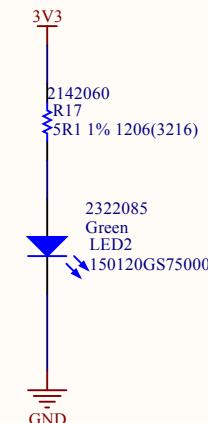
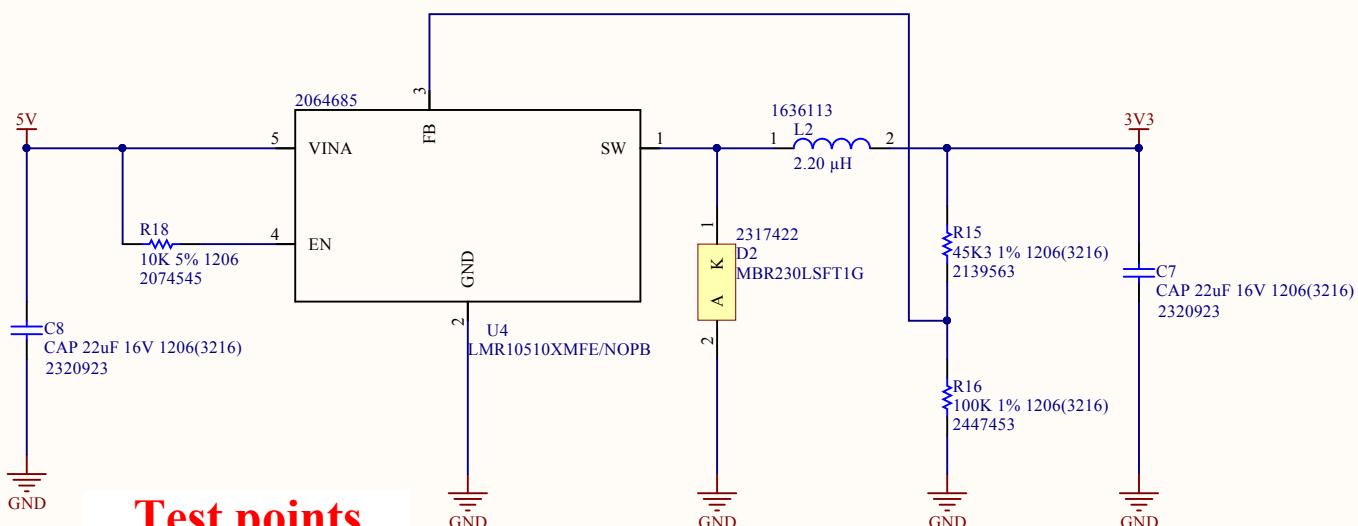
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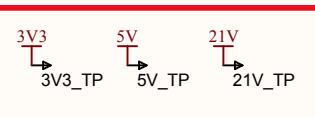
Solution to test
Too much power
consumption with a single
current limiting resistance



3V3



Test points



Title **Power** Drawn By:Badr BOUSLIKHN

Size: A4 Number.* Revision:1.0

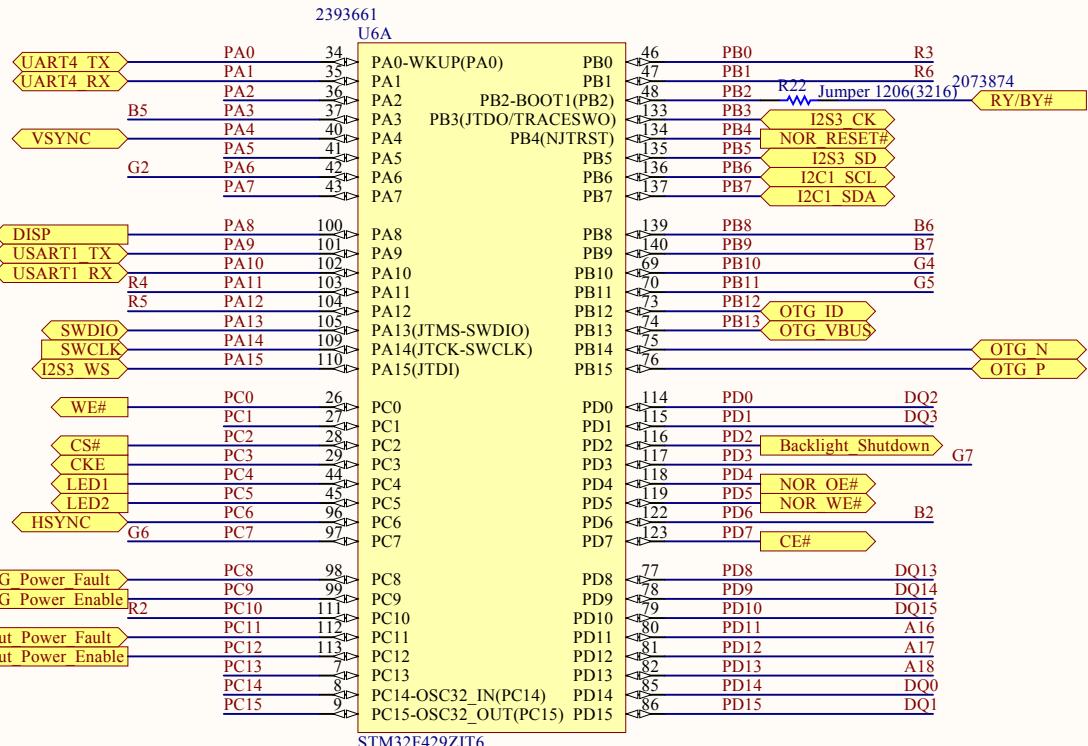
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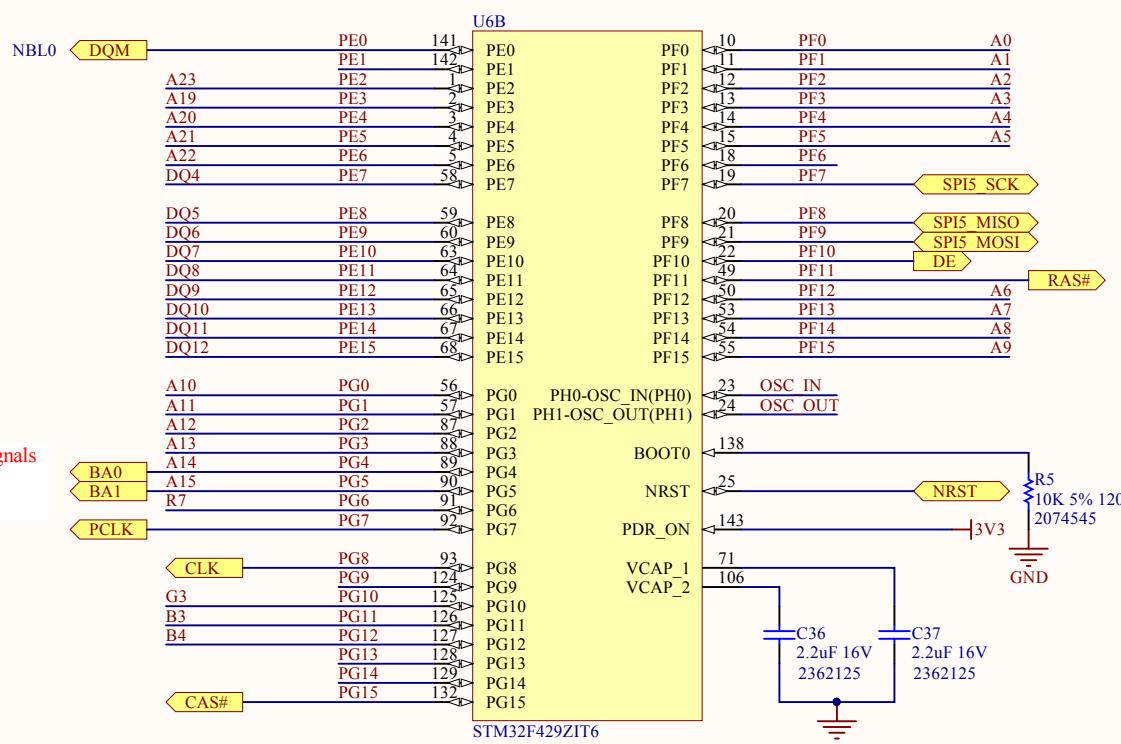
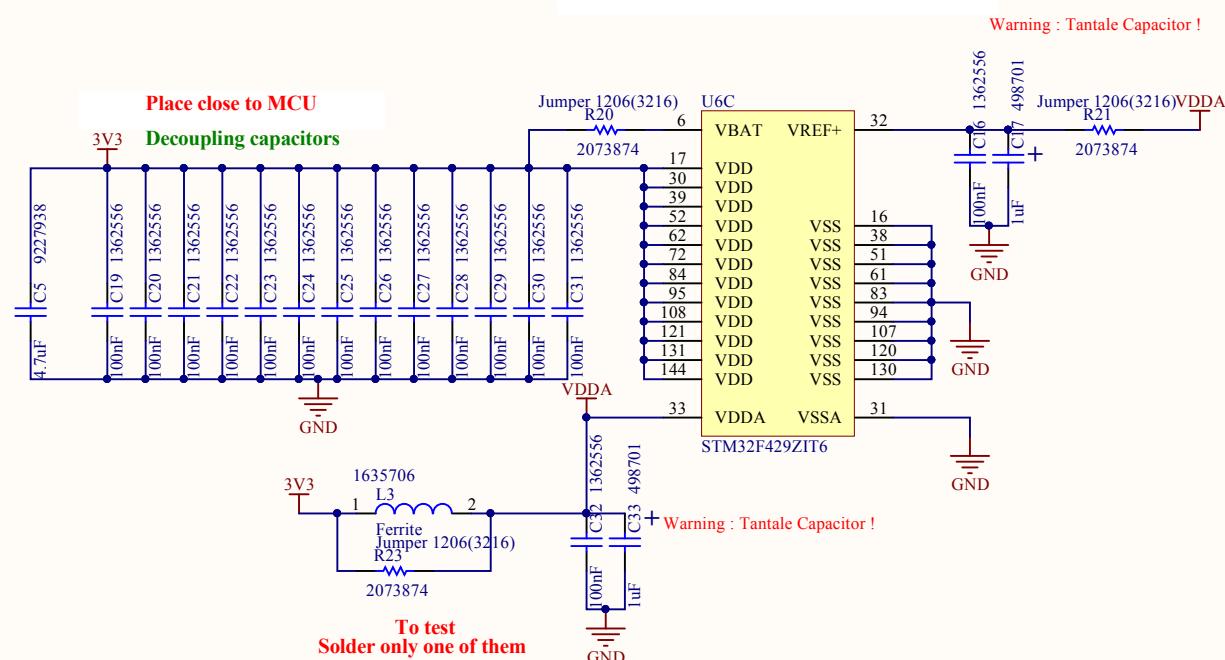
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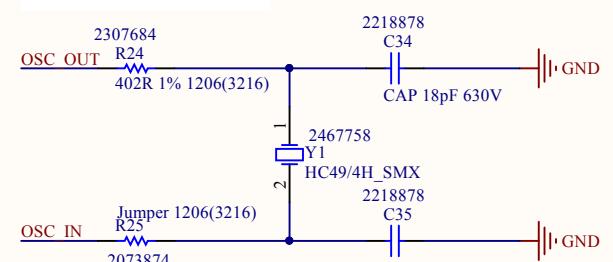


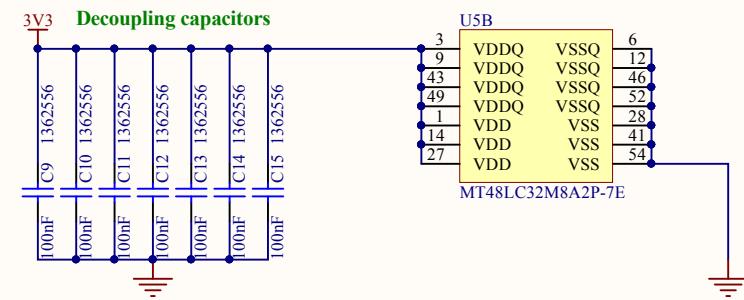
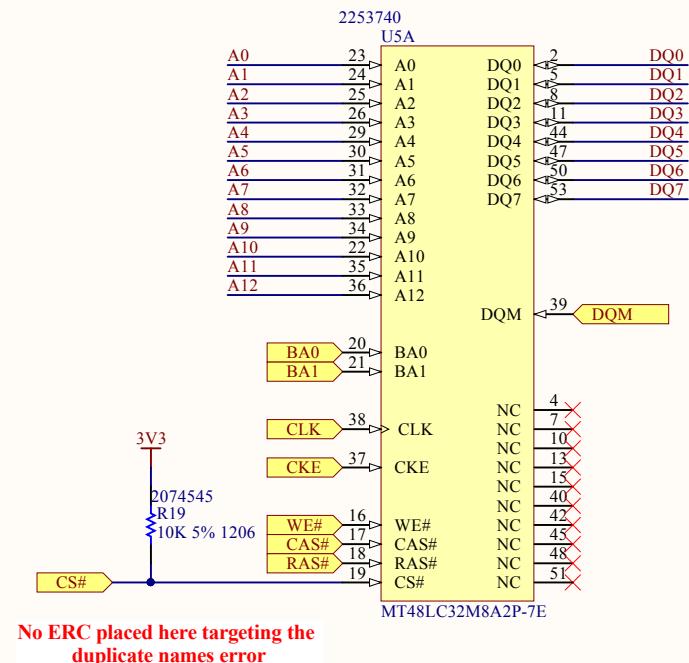
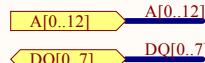
Boot mode selection pins		Boot mode	Aliasing
BOOT1	BOOT0		
x	0	Main Flash memory	Main Flash memory is selected as the boot space
0	1	System memory	System memory is selected as the boot space
1	1	Embedded SRAM	Embedded SRAM is selected as the boot space

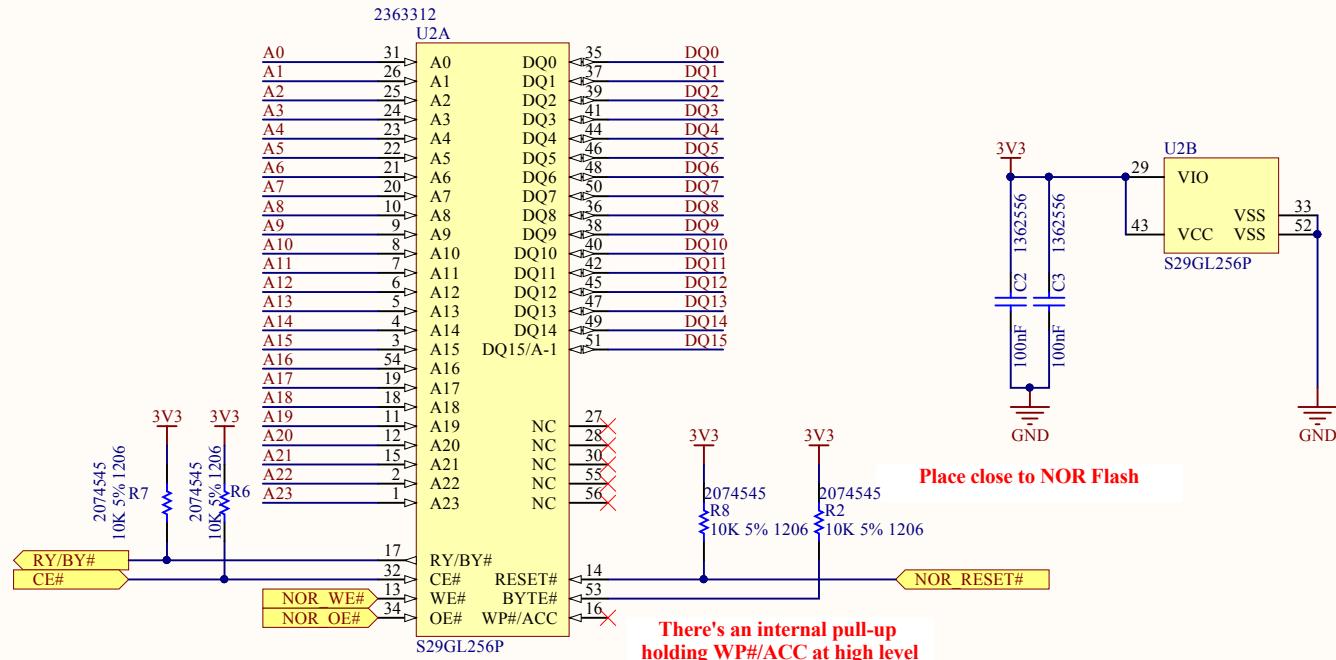
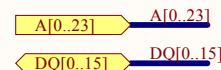
MCU Power



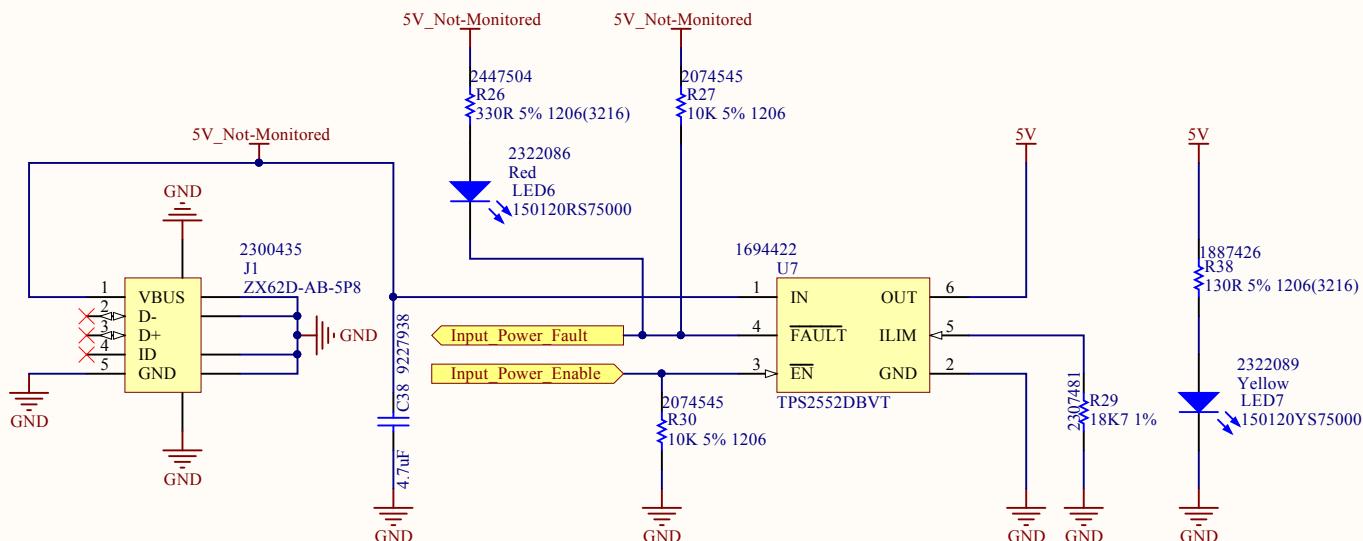
402 Ohm value to tune
5 to 6 times ESR value of
the Quartz



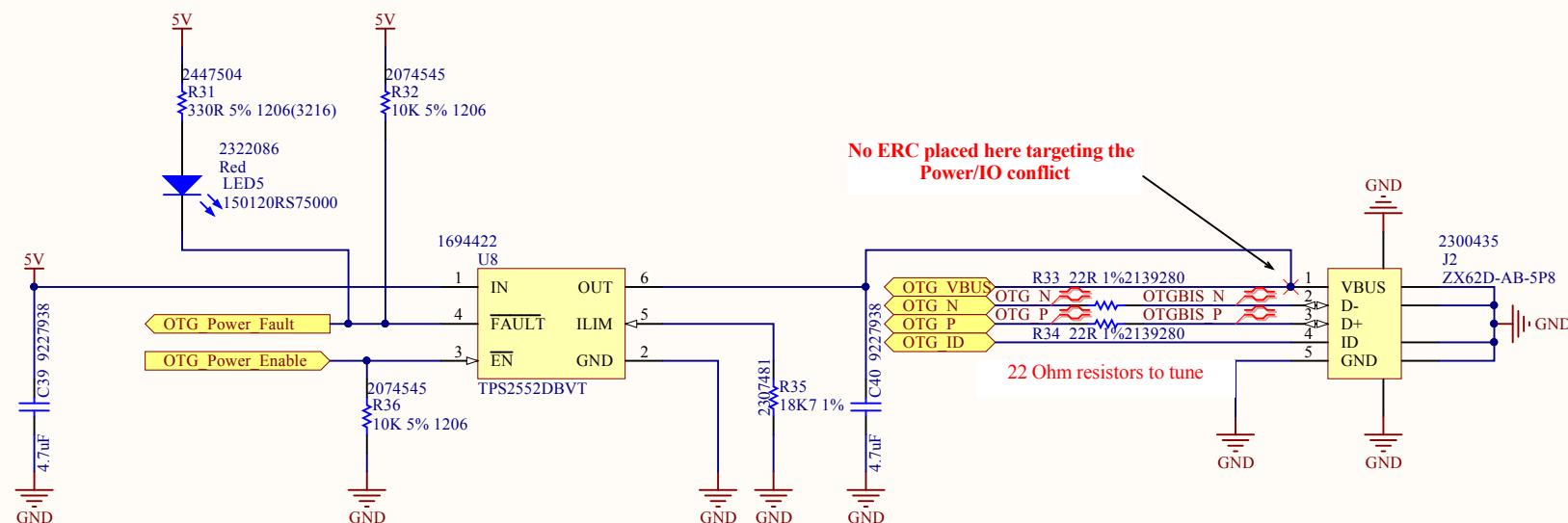




5V Power Input



USB OTG FS



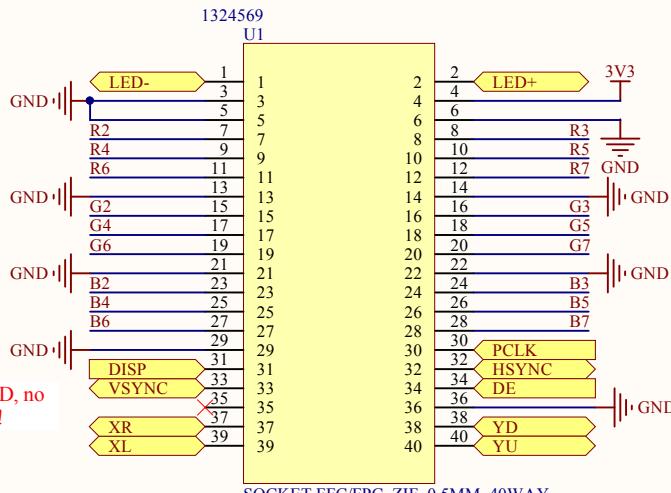
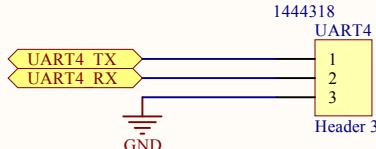
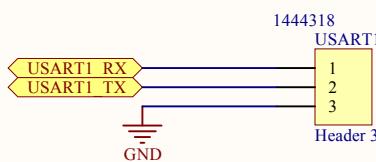
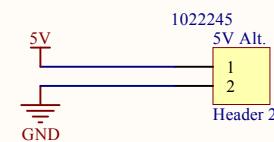
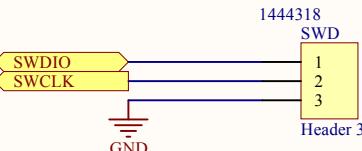
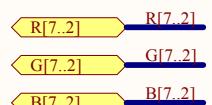
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Size: A4	Number.*:
Date: 20/07/2015	Revision: 1.0
Time: 18:53:21	Sheet 6 of 7
File: C:\Users\Badr\Documents\prt-ge-s2-2015-svn\trunk\Board\02_Schema_BoM_Layout\PRT STM32F429ZI_Eval-Board\USB.SchD	

1

2

3

4



SWD DBG

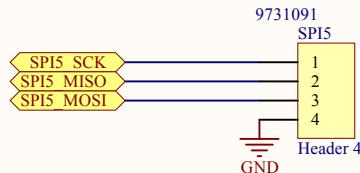
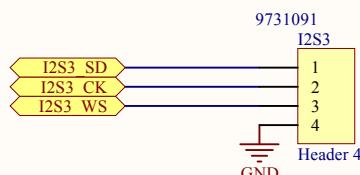
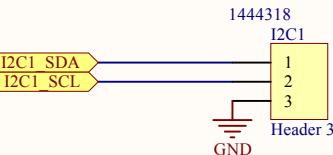
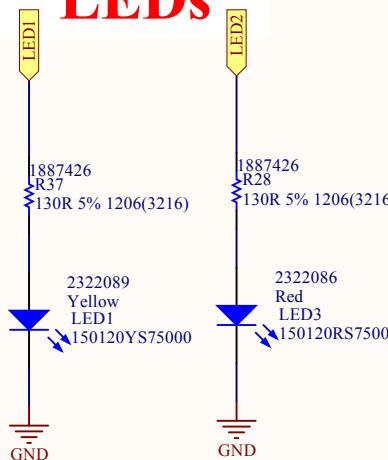
Alternate Power Input

USART1 interface

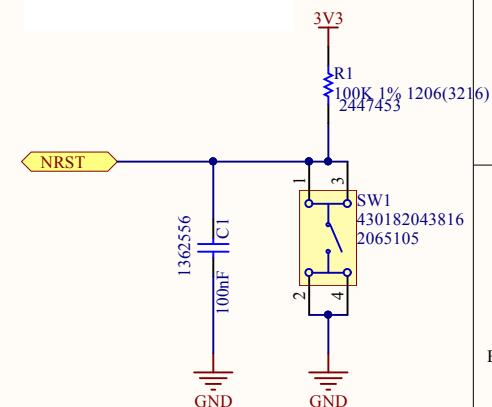
GPIO/UART4

LCD Interface

User LEDs



Reset Switch



I2C1 Interface

GPIO/I2S3

GPIO/SPI5

Title IO Peripherals	Drawn By: Badr BOUSLIKHN
Size: A4	Number: * 1.0
Date: 20/07/2015	Time: 18:53:22
File: C:\Users\Badr\Documents\prt-ge-s2-2015-svn\trunk\Board\02_Schema_BoM_Layout\PRT_STM32F429ZI_Eval-Board\IO_Peripher	Sheet 7 of 7