

14 ULN2003stepper motor driving 2

1 Project process:

```
Pi@raspberrypi ~/keyes_树莓派高级版套件资料/ULN2003_python $ ls ULN2003.py pi@raspberrypi ~/keyes_树莓派高级版套件资料/ULN2003_python $ vi ULN2003.py pi@raspberrypi ~/keyes_树莓派高级版套件资料/ULN2003_python $ vi ULN2003.py pi@raspberrypi ~/keyes_树莓派高级版套件资料/ULN2003_python $ sudo python ULN2003.py
```

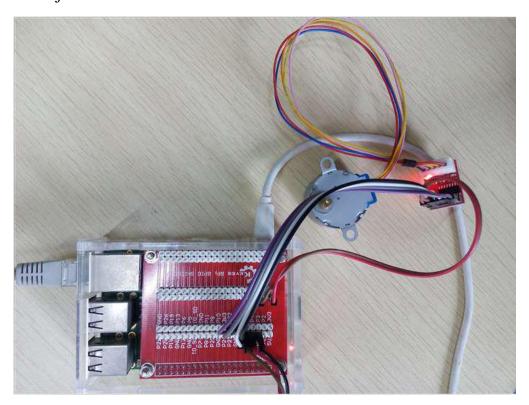
2 Project source code:

```
- - X
pi@raspberrypi: ~/keyes_鏍致帗娲鹃珮绾x增濂椾欢豎勬枡/ULN2003_python
 Name: bujindianji
 Created: 03/16/2014
#!/usr/bin/env python
# Import required libraries
import time
import RPi.GPIO as GPIO
GPIO.setmode(GPIO.BCM)
StepPins = [17, 18, 27, 22]
# Set all pins as output
for pin in StepPins:
  print "Setup pins"
  GPIO.setup(pin, GPIO.OUT)
  GPIO.output(pin, False)
# Define some settings
StepCounter = 0
WaitTime = 0.02
"ULN2003.py" 73L, 1411C
```

3 Result

```
pi@raspberrypi: ~/keyes_螺颈核蜗膝頭缝x增漉榆欢鑒動桥/ULN2003_python
pi@raspberrypi ~/keyes 树莓派高级版套件资料/ULN2003 python $ 1s
ULN2003.py
pi@raspberrypi ~/keyes_树莓派高级版套件资料/ULN2003_python $ vi ULN2003.py
pi@raspberrypi ~/keyes 树莓派高级版套件资料/ULN2003 python $ sudo python ULN2003.py
ULN2003.py:18: RuntimeWarning: This channel is already in use, continuing anyway.
se GPIO.setwarnings(False) to disable warnings.
 GPIO.setup(pin,GPIO.OUT)
Setup pins
Setup pins
Setup pins
 Step 0 Enable 17
 Step 1 Enable 18
 Step 2 Enable 27
 Step 3 Enable 22
 Step 0 Enable 17
 Step 1 Enable 18
 Step 2 Enable 27
 Step 3 Enable 22
 Step 0 Enable 17
 Step 1 Enable 18
 Step 2 Enable 27
 Step 3 Enable 22
 Step 0 Enable 17
 Step 1 Enable 18
 Step 2 Enable 27
 Step 3 Enable 22
 Step 0 Enable 17
```

4 Project circuit connection:



5 Pin definition

Red circled area are GPIO ports corresponding to ULN2003 IN1 IN2 IN3 IN4.

BCM	wPi	Name	Mode	V	Phy:	sical	I V	Mode	Name	wPi	BCM
			+	+	+	++	+	+	+		+
		3.3v		1	1	2	1	l	5 v		ļ
2	8	SDA.1	ALT0	1	3	4	1	ļ.	5⊽		1
3	9	SCL.1	ALT0	1 1	5	6	1		0v		1
4	7	GPIO. 7	IN	1	1 7	8	1	ALT0	TXD	15	1 14
		0v		l	9	10	1	ALT0	RXD	16	15
17	0	GPIO. 0	OUT	1	11	12	10	OUT	GPIO. 1	1	18
27	2	GPIO. 2	OUT	0	1 13	14			0 v		
22	3	GPIO. 3	OUT	0	15	16	1 0	IN	GPIO. 4	4	23
		3.3v			1 17	18	10	IN	GPIO. 5	5	24
10	12	MOSI	ALT0	0	1 19	11 20	1	1	I 0⊽		1
9	13	MISO	ALT0	10	1 21	1 22	10	IN	GPIO. 6	1 6	25
11	14	SCLK	ALT0	0	1 23	24	1 1	ALT0	CEO	1 10	1 8
		0v		1	25	26	11	ALT0	CE1	11	1 7
0 1	30	SDA.0	IN	1	1 27	28	1 1	IN	SCL.0	31	1
5	21	GPIO.21	IN	1	1 29	30	1	1	0v		1
6	22	GPIO.22	IN	1	31	32	1 0	IN	GPI0.26	26	12
13	23	GPIO.23	IN	0	33	34	1	1	0 V		1
19	24	GPIO.24	IN	1 0	1 35	36	1 0	IN	GPIO.27	1 27	1 16
26	25	GPIO.25	IN	0	37	38	10	IN	GPIO.28	28	20
		0v	!	!	1 39	40	1 0	IN	GPIO.29	29	21
BCM	wPi	Name	Mode	V	Phy:	sical	V	Mode	Name	wPi	BCM

