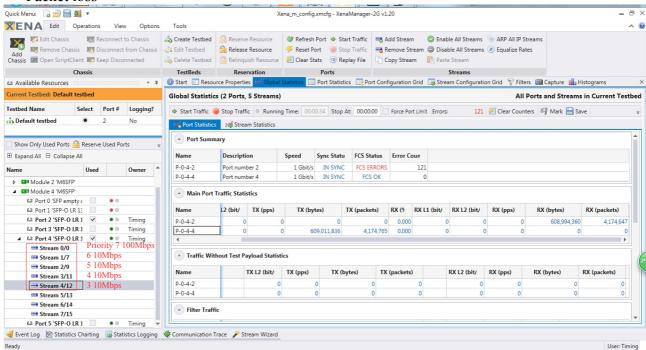
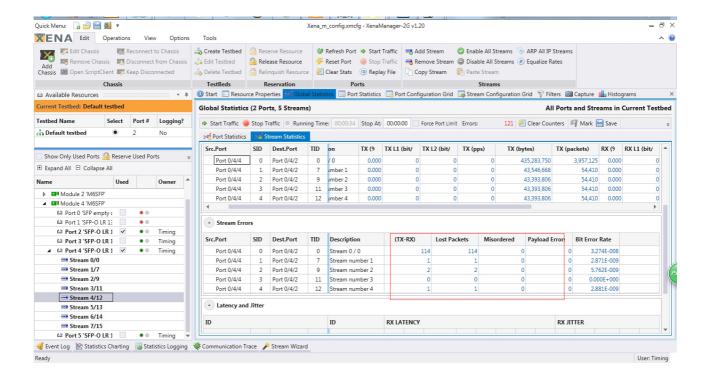


2 WR switch layer Priority 7 100Mbps 13burst/100us Priority 6.5.4 10Mbps 5 burst/3ms Pritory 3 10Mbps 5 burst/3ms







The 1st layer switch

1 6111														4:Rpcl_4 15:R 80:RTUrsp 31:R
					7:RTUfwd 38:		(DLT_5 54	:Kpr1_3 25	:Kpr1_4 26	:Kpr1_5 2/	:Kbt.T_015	0:Kpr1_/ 2	9:KIUred 3	on:Kiorsp 31:F
		4174765												
					4174765									
	0	4174765	0	0 [0		0		0				0
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	01		01							0 [0	01	01	0
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01	4174765	01	4174765	4174765	01	0								
0	01	0 4174765	01	01	0	0	01	01	01	01	01	01	01	0
01	01	11/4/63	01	01	4174765	0		0						
01	01	0	0	01	0	0	0	0	0	0	0	0	0	0
0	01	41747651	0	01	01	0	01	0	01	01	01	0	01	0
01	01	01	0	01	4174765	0								
0	01	01	01	01	01	01	0	0	0	0	0	01	01	0
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01	01	0	01	01	41/4/62	0	01	01	01	01	0	01	01	01
01	01	4174765	01	01	01	01	01	01	01	01	01	01	01	01

The 2nd layer switch

timing	g@tsl006:	~													
0:1	[u-run	1:Ro-run	2:Riv-cd	3:Rsyn-1	4:Rpause	5:Rpf-dp 6:	Rpcs-e 7:E	Rgiant 8	:Rrunt 9	:Rcrc e 10	:Rpcl 0 11	:Rpcl 1 1	2:Rpcl 2 1	3:Rpcl 3 1	4:Rpcl 4 15:Rp
16:F	Rpcl_6 1	7:Rpcl_7 1	18:Tframe 1	19:Rframe 2	0:Rrtu_f 2	1:Rpri_0 22:	Rpri_1 23:E								0:RTUrsp 31:RT
32:F	RTUhp 3	3:RTUf-f	34:RTUn-f 3	35:RTUfst 3	6:RTUful 3	7:RTUfwd 38:	TRUrsp								
	0	0	01	01	01	01	0	0	0	01	0	0	0 [01	01
	0 [0	0	4174765	115	o i	0	0	54410	54410	54410	54409	3957011	4174650	4174650
	0 [4174650	0	4174650	4174650	0	0 [
	3	0	0 [0 [0 [0 [0	0 [0	0	0	0 [0	0	0
	0 [01	4174650	0 [0	0 [01	01	01	0		01	0	01
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			4174650												
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			4174650												
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			4174650												
			0		0	4174650									
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			4174650												
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		0 [4174650		0 [0	0			0 [0 [
			0		0 [4174650									
		01			01	0 [0 [0 [0 [01		
	0 [0 [4174650	01	0 [0	01								
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			01												
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	01	01	01	01	01	4174650	01								
	160	0 [0	0	0 [0	0	0 [0 [0 [0	0 [0 [0 [0