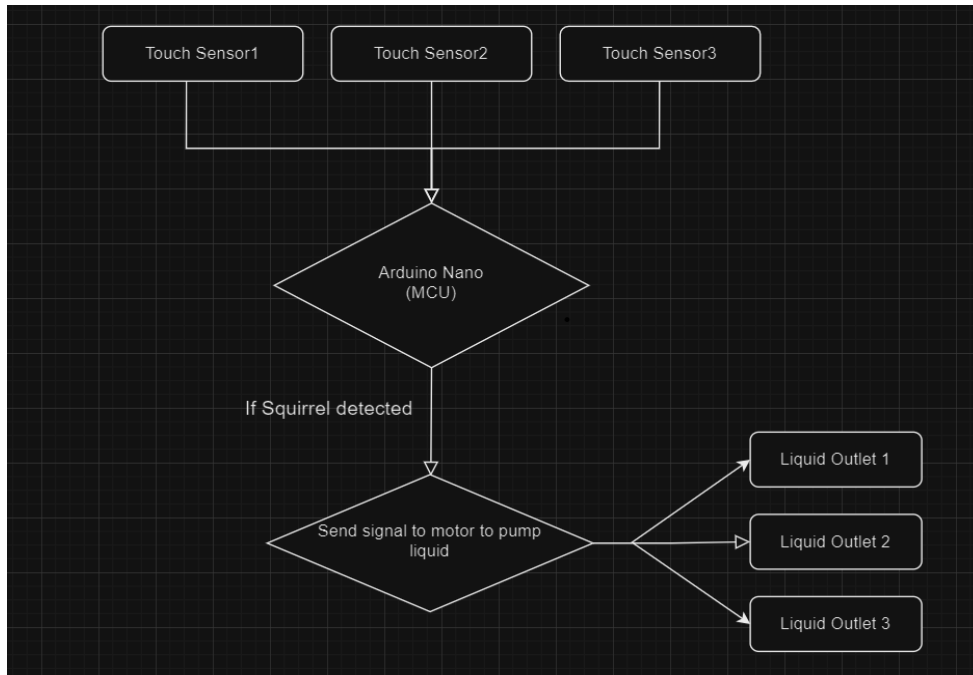
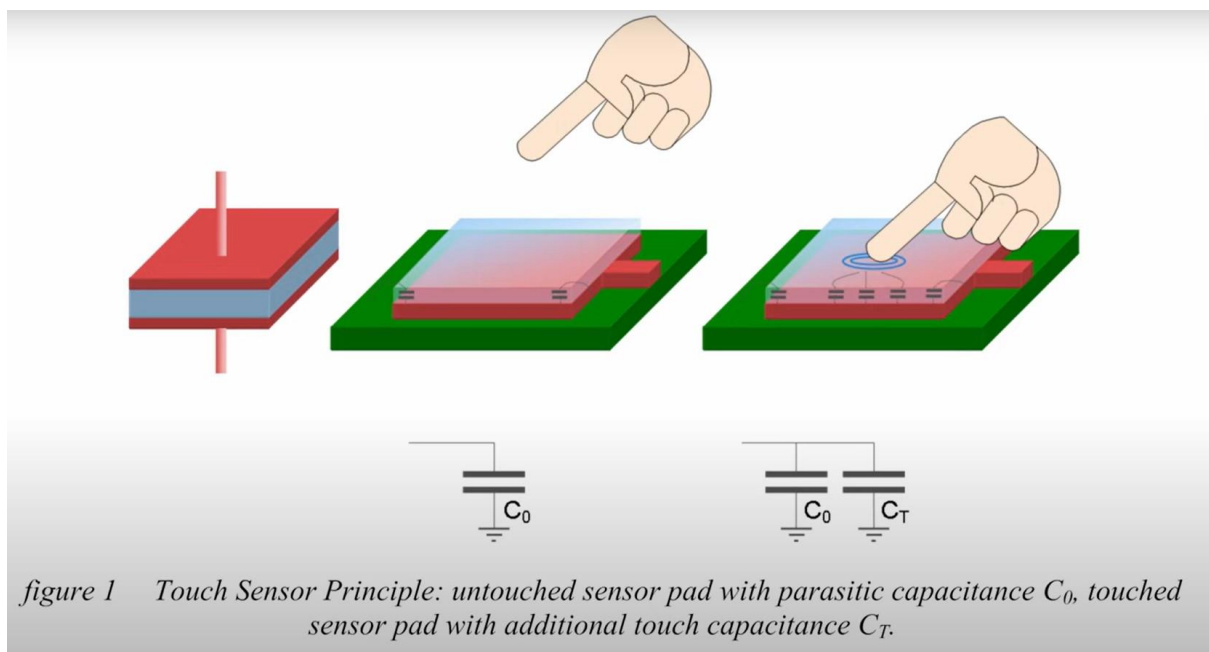


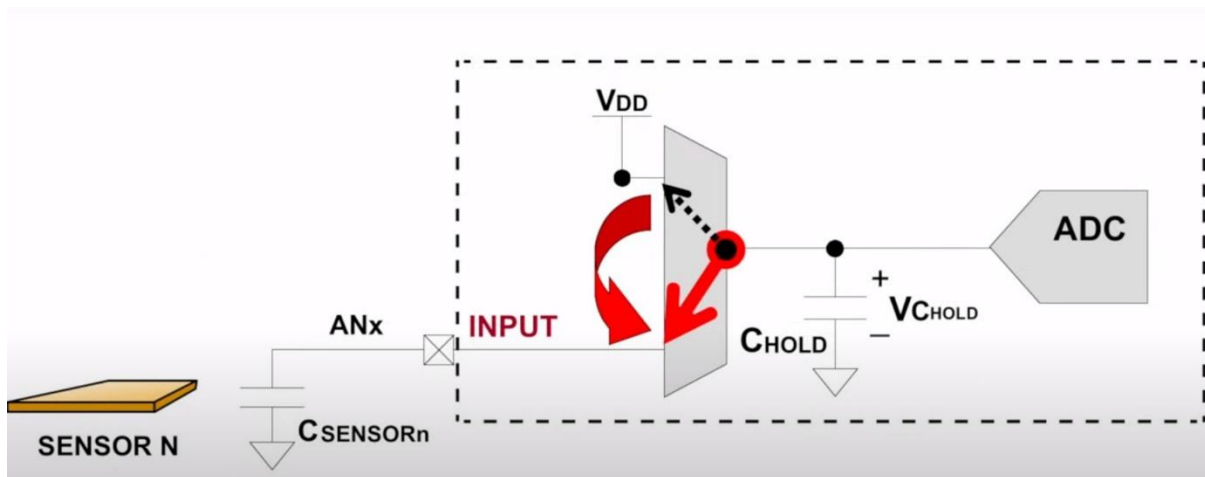
Detection System-

Flow diagram-



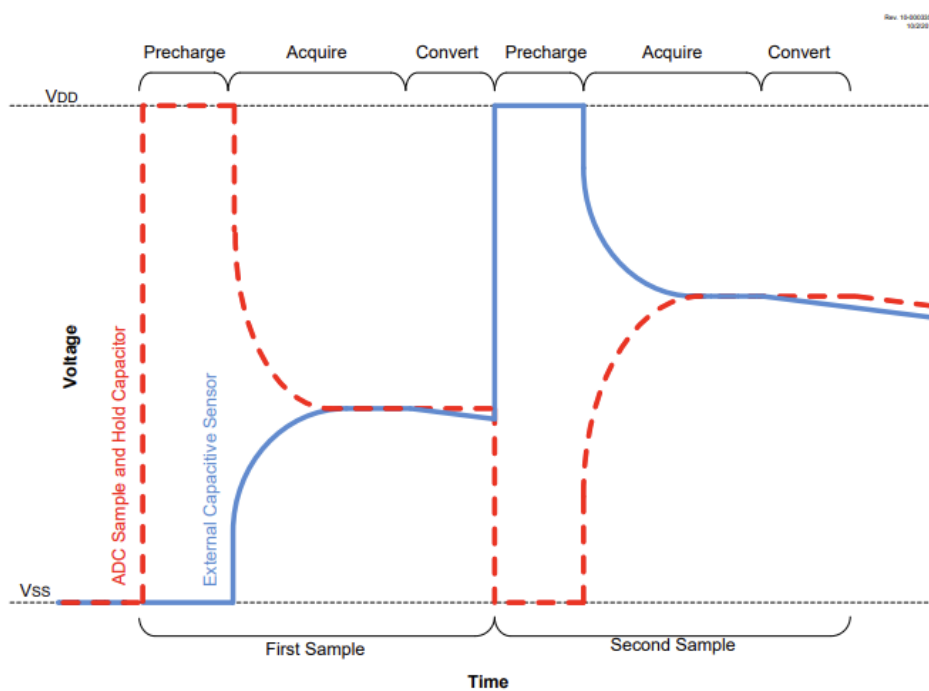
Principles (Working)-





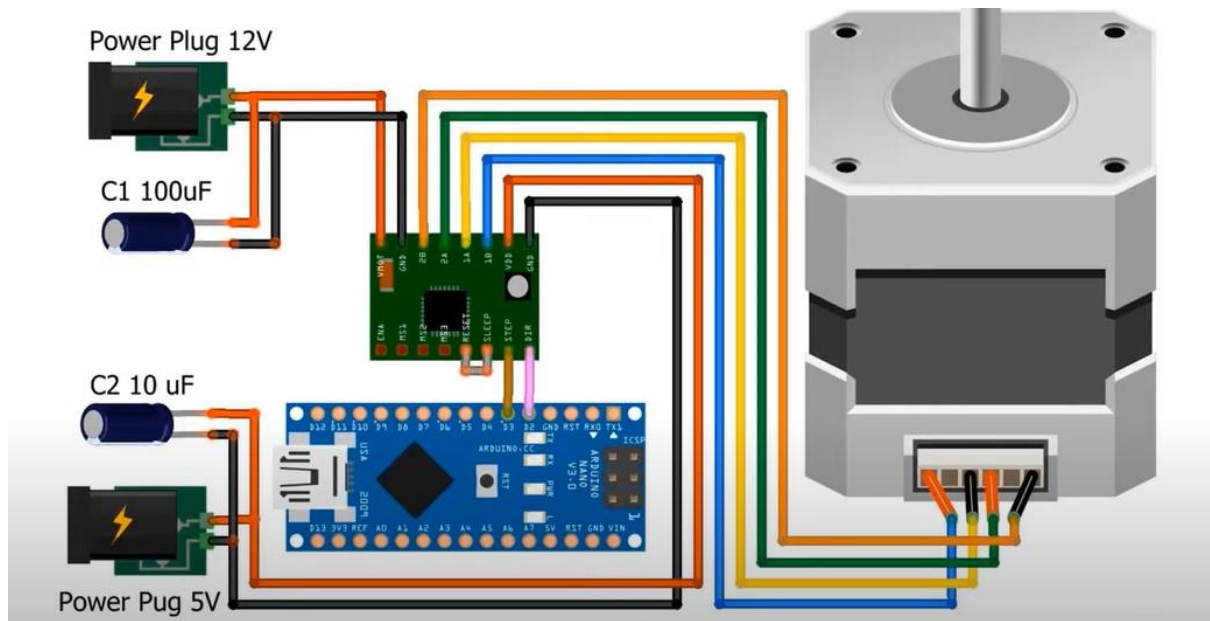
TB3198 CVD Overview

Figure 1-2. Differential CVD Measurement Waveform

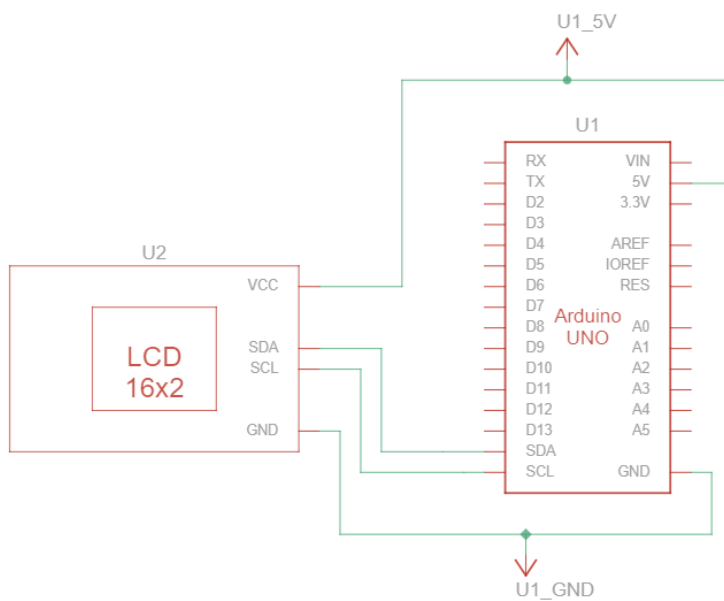


Pumping System Part-

Schematics-



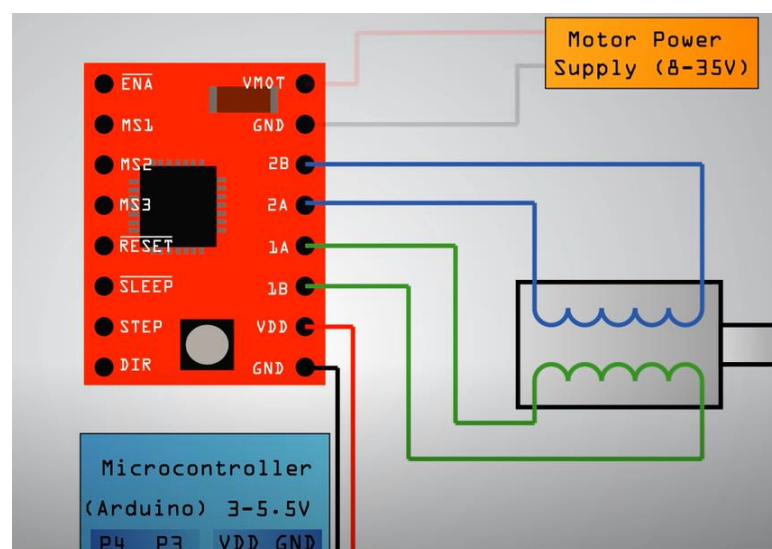
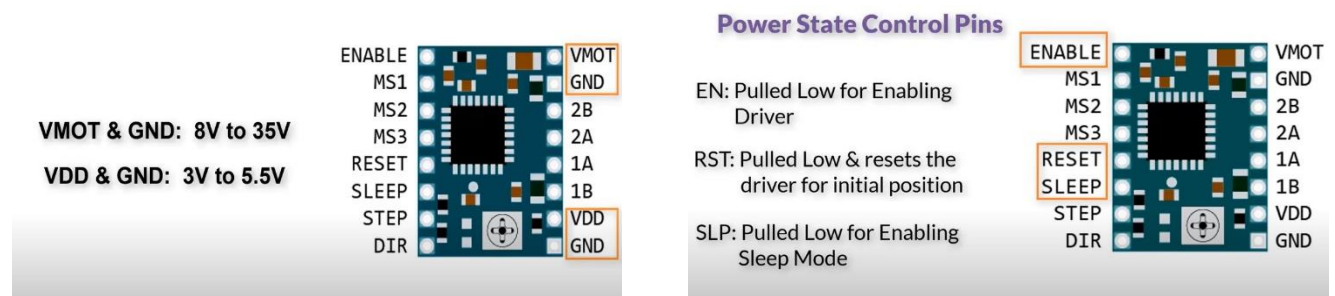
Display unit(schematics)-



(from tinkercad)

Reference (for display && motor)-

A4988 motor driver (pin diagram)



LCD display-

- SDA and SCL lines use pull-up resistors
- Bus Master and Slave device(s)
- Each device on bus has a unique address
- Master addresses Slave to communicate

4-pin Connection:

- VCC - 3.3 or 5 Volts
- GND - Ground
- SDA - Serial Data
- SCL - Serial Clock

Switches-

For powering on/off the main and individual outlet circuits