USB4 1.0 ENGINEERING CHANGE NOTICE FORM

Title: TBT3 CM NVM Access to Address 0Ah
Applied to: USB4 Specification Version 1.0

Brief description of the functional changes:
A Device Router that support TBT3 Compatibility shall respond to NVM access of address 0Ah.

Benefits as a result of the changes:
Aligns to the TBT3 CM flow.

An assessment of the impact to the existing revision and systems that currently conform to the USB specification:
None

An analysis of the hardware implications:
None

An analysis of the software implications:
None

An analysis of the compliance testing implications:
Add this check to CTS

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Actual Change

(a). 13.4.3 Bit Banging Interface

This section only applies to a Device Router that supports TBT3-Compatibility on its UFP.

A Router shall support the "bit banging" interface defined in Vendor Specific 1 Capability (see Section 13.6.1.1).

A Router shall respond when the byte at address 0Ah is read. The Router may return any value.

A Router shall return the value 00000080h when the 32 bits at addresses [78h:75h] are read.

A Router shall return the value 01h when the byte at address 0148h is read.

A Router shall return the value 00000111h when the 32 bits at addresses [1A7h:1A4h] are read.