

PATI SRI SAI BALAJI

CAREER OBJECTIVE

Seeking an opportunity in a dynamic organization to apply knowledge, develop new skills, and contribute to innovative projects. Committed to continuous learning and professional growth in a collaborative environment.

INTERNSHIP AND CERTIFICATIONS

- **NPTEL: SYSTEM DESIGN THROUGH VERILOG (8-week)**
 - Gate level modelling
 - Behavioral modelling
 - Data flow modelling
 - Switch level modelling
 - Synthesis of combinational logic using verilog
- **IIDT collaborated Black bucks: Embedded systems and IOT (8-week)**
 - Embedded System Concepts
 - Develop Embedded System Applications
 - Apply IoT Architecture and Communication Protocols
 - Design and Implement IoT Applications
 - Analyze Power and Performance Optimization
- **APTRANSCO-Electrical transmission (24-week)**
 - practical exposure to power transmission systems
 - grid operations and electrical infrastructure
 - Maintenance of electrical equipment and battery room
 - Developed a strong understanding of power quality load flow analysis, fault detection, and grid stability
- **AutoCAD (EEE) (8-week)**
 - Electrical System Design & Analysis
 - Single Line Diagram (SLD) Development
 - Substation Design,Power Distribution Systems
 - Lighting System Design,Industrial Electrical Systems & Automation
 - Electrical Floor Plan Development

☎ 9493541966
✉ pssbalaji04@gmail.com
📍 Eluru
🌐 www.linkedin.com/in/srisai-balaji-pati-143487373

EDUCATION

2023-2026

◆ **SRKR ENGINEERING
COLLEGE**

- B.tech in Electrical and Electronics Engineering

2020-2023

◆ **SMVM POLYTECHNIC**

- Diploma in Electrical and Electronics Engineering

SKILLS

- Embedded systems
- MATLAB software
- basics in C language
- basics in verilog

PROJECTS

- Wireless controlled dry type Solar panel cleaner using NodeMCU(ESP32)
- Innovation for Sustainability: Driving smart resource conservation((energy and water) in home appliances (refrigerators, air conditioners and washing machines) using NodeMCU(ESP8226) and arduino UNO