EFR32xG22 QFN40 Radio Board

2.4 GHz 6 dBm

Revision History

Rev.	Description
A00	Initial production release.
A01	Mounted U1. Updated C1, C2 P/N.
A02	Matching layout improvement at C1 GND pad.
B00	X3-->X1. Updated U1, X1 P/N, matching and USART markings. PCB sikscreen fix.
B01	Updated matching network.
B02	Updated matching network.
B03	Updated U1 P/N. Fixed X2 P/N.
B04	Removed optional/not mounted components. Fixed L101 P/N.
B05	New P/N for X2.
B06	Updated P200, P201, P2, U200 OPNs.

PCB1

PCB4182A

SILICON LABS

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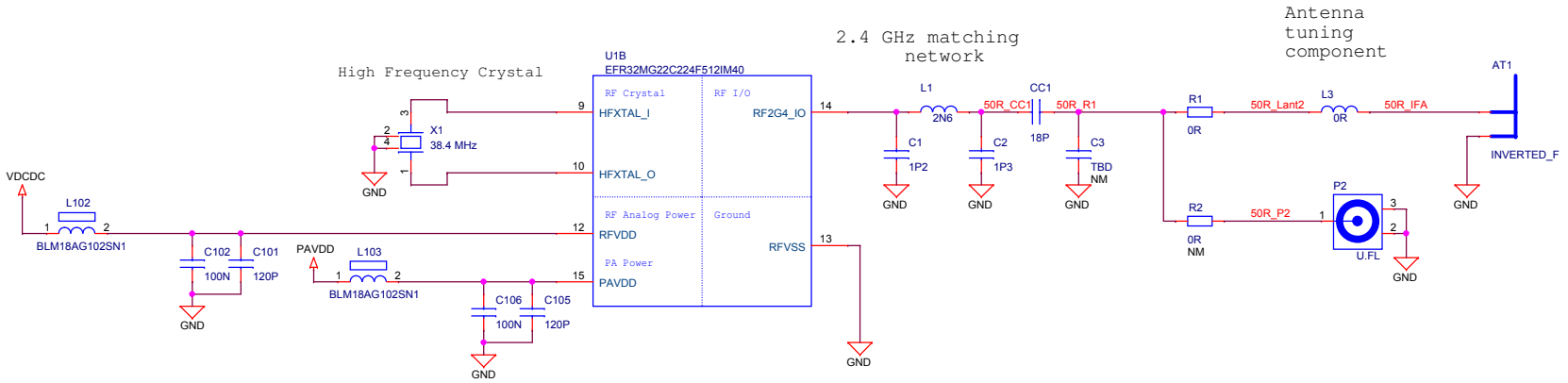
Page Title: **Title Page**

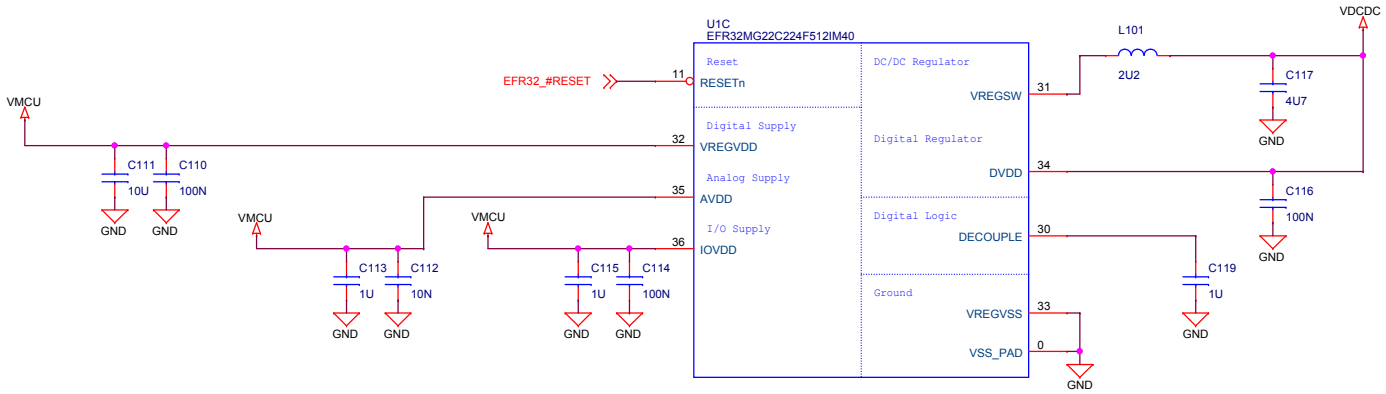
Board Number: **BRD4182A**

Revision: **B06**

Antenna & Radio Interface

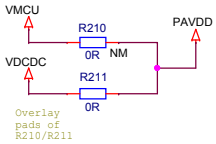


Power & Decoupling

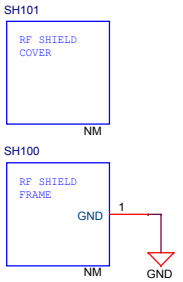


PAVDD Configuration

	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount



RF Shielding



SILICON LABS

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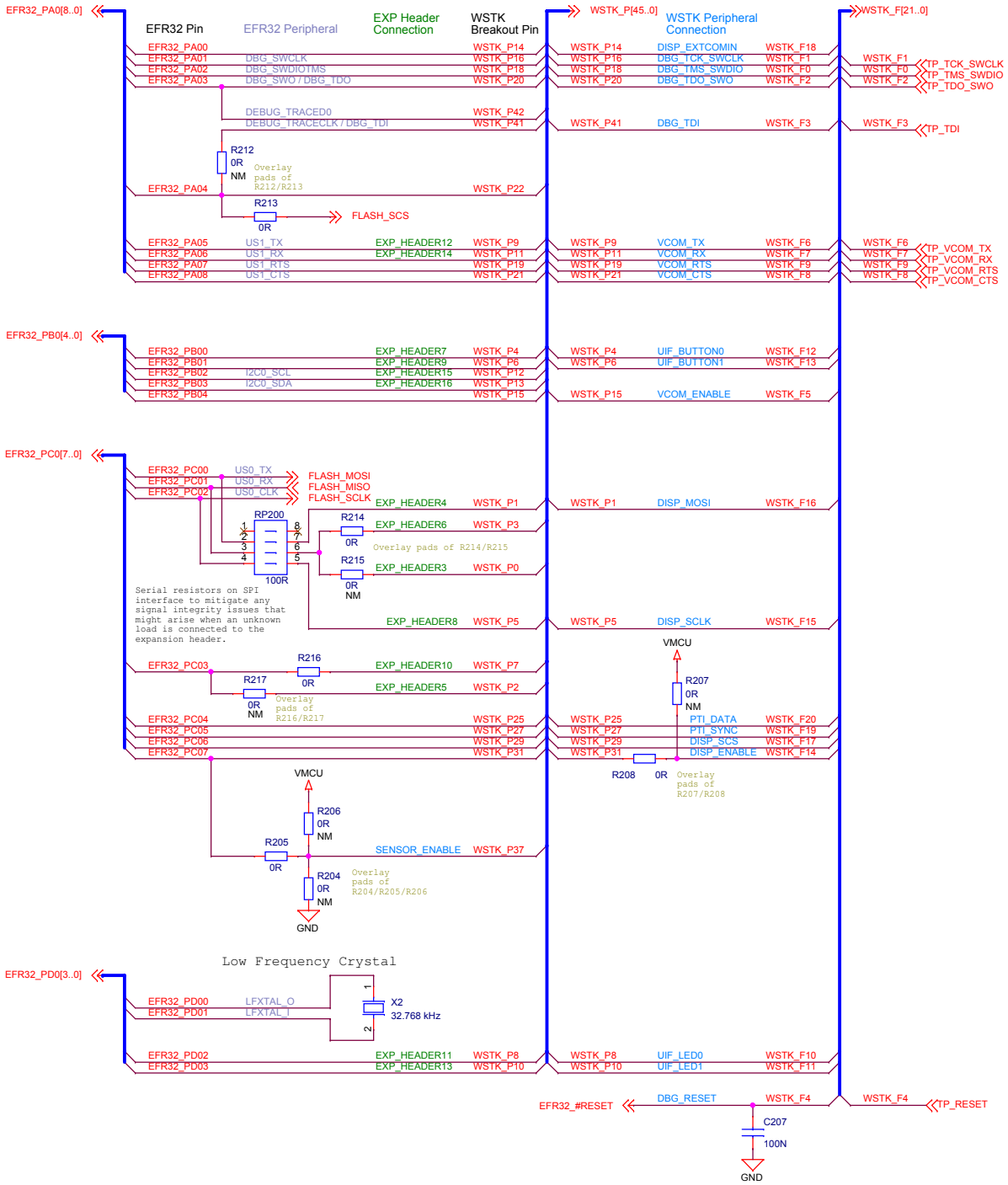
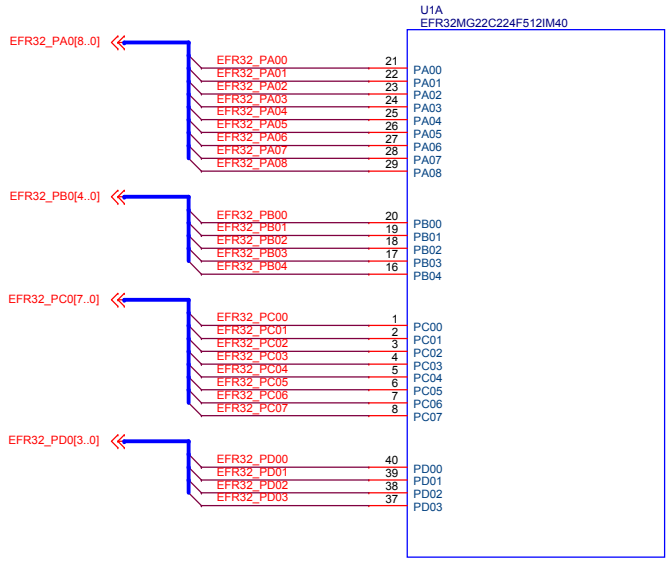
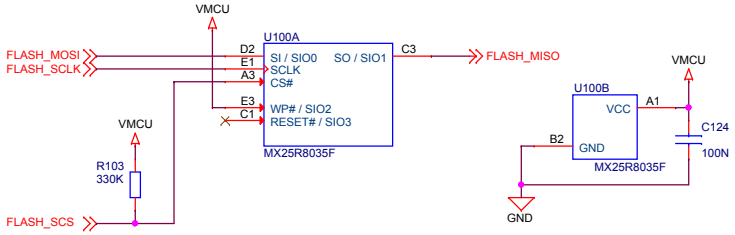
EFR32 Pin Mapping



EFR32 I/O Port Pins



Serial Flash



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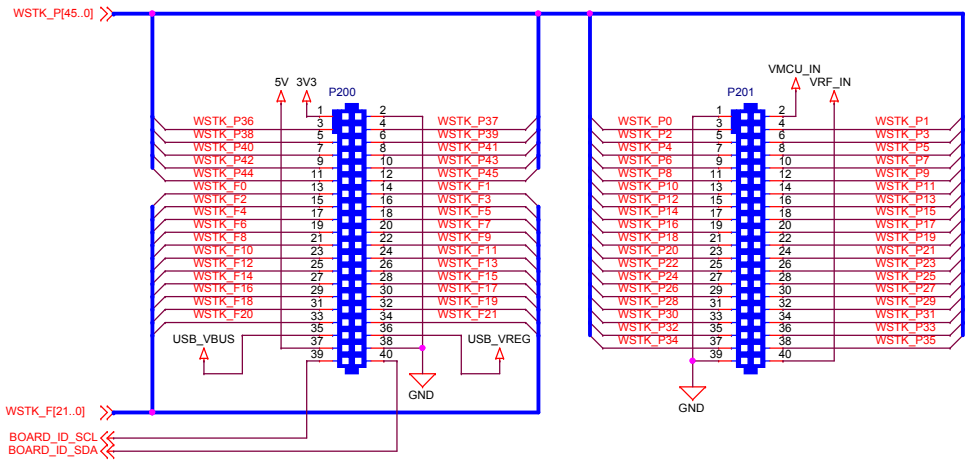
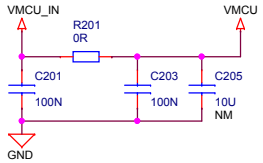
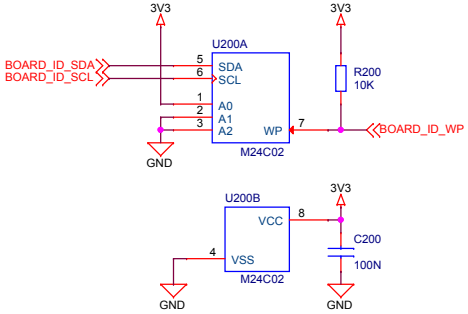
WSTK Connectors



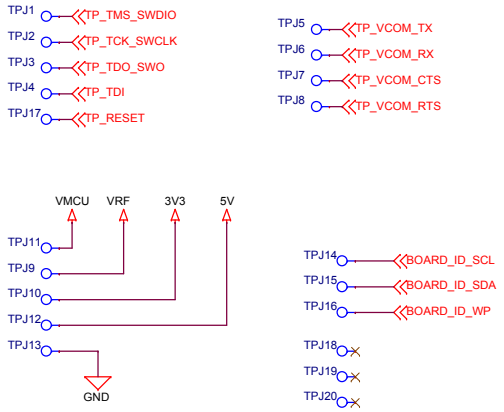
WSTK Power Decoupling



Board Identification



Test Points



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