

Spartan-6 FPGA Integrated Endpoint Block for PCI Express

User Guide

Document ID: 64632

© 2006 Xilinx, Inc. All rights reserved. Xilinx, the Xilinx logo, and the Xilinx logo with "XC" are registered trademarks or trademarks of Xilinx, Inc. in the United States and other countries.

Other names and brands may be claimed as the property of their respective owners.

