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|  |
| **Push button** | **PC0[A0] voltage** | **ADC value (calculated)** | **ADC value (measured)** |
| Right | 0 V | 0 | 0 |
| Up | 0.495 V | 101 | 100 |
| Down | 1.202 V | 246 | 256 |
| Left | 1.97 V | 403 | 410 |
| Select | 3.18 V | 651 | 640 |
| none | 5 V | 1023 | 1023 |

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|  |
| Operation | **Register(s)** | **Bit(s)** | **Description** |
|  |  |  |  |
| Voltage reference | ADMUX | REFS1:0 | 01: AVcc voltage reference, 5V |
| Input channel | ADMUX | MUX3:0 | 0000: ADC0, 0001: ADC1, ... |
| ADC enable | ADCSRA | Bit 7 – ADEN: | 0: turned off |
| Start conversion | ADCSRA | Bit 6 – ADSC | 1: in Single - starts each conversion, in Freerunning - starts the first conv.; returns to 0 when conv. completed |
| ADC interrupt enable | ADCSRA | Bit 3 – ADIE | 1: activated, if 1-bit in SREG is set |
| ADC clock prescaler | ADCSRA | ADPS2:0 | 000: Division factor 2, 001: 2, 010: 4, ... |
| ADC result | ADLAR | ADC9:0 |  |