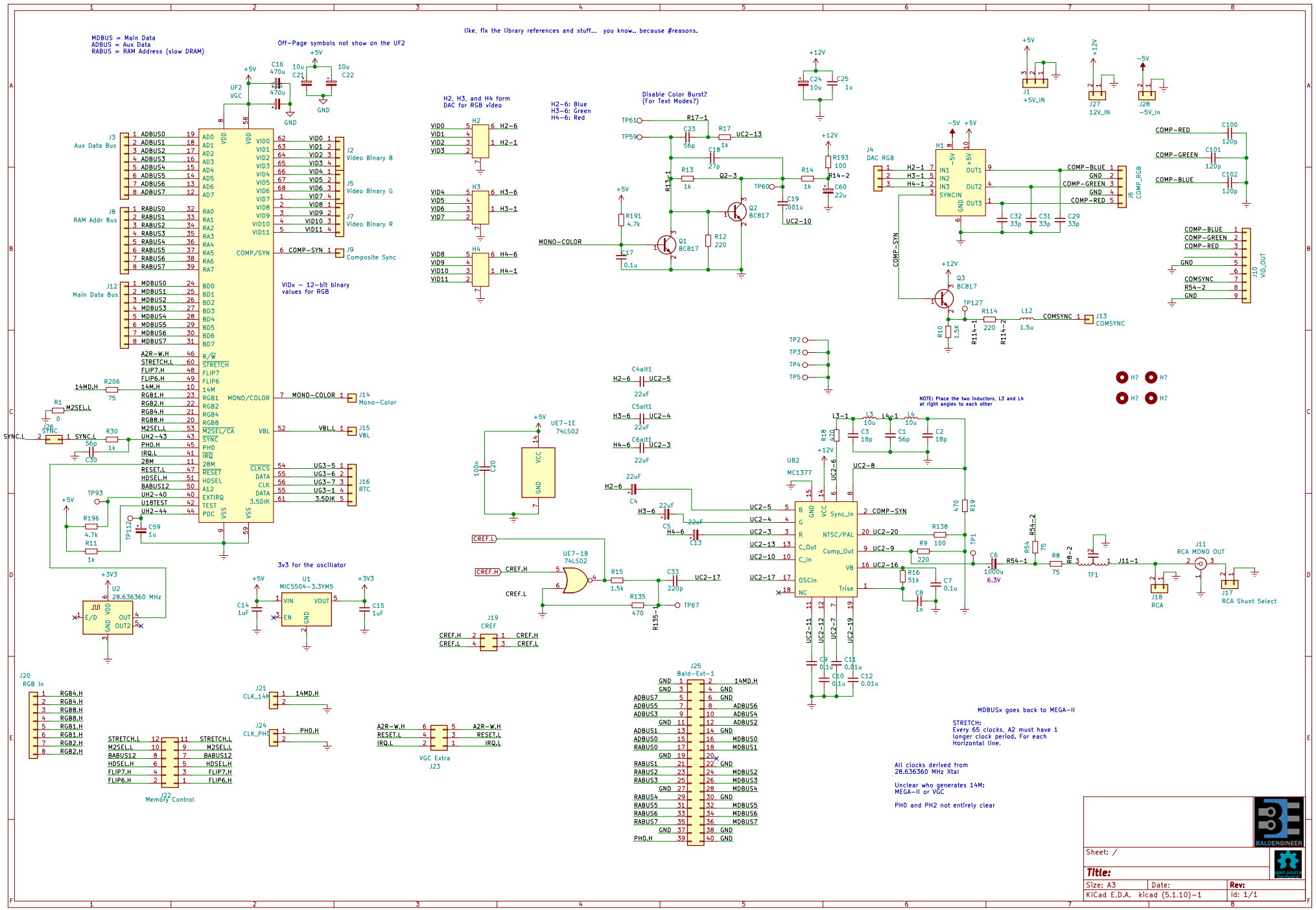
MDBUS = Main Data
ADBUS = Aux Data
RABUS = RAM Address (slow DRAM)
Off-Page symbols not show on the UF2
like, fix the library references and stuff... you know.. because #reasons.
H2, H3, and H4 form DAC for RGB video
H2-6: Blue
H3-6: Green
H4-6: Red
Disable Color Burst? (For Text Modes?)
VIDx - 12-bit binary values for RGB
3v3 for the oscillator
NOTE: Place the two inductors, L3 and L4 at right angles to each other
MDBUSx goes back to MEGA-II
STRETCH:
Every 65 clocks, A2 must have 1 longer clock period. For each Horizontal line.
All clocks derived from 28.636360 MHz Xtal
Unclear who generates 14M: MEGA-II or VGC
PH0 and PH2 not entirely clear
J3 Aux Data Bus
J8 RAM Addr Bus
J12 Main Data Bus
J2 Video Binary B
J5 Video Binary G
J7 Video Binary R
J9 Composite Sync
J14 Mono-Color
J15 VBL
J16 RTC
J4 DAC RGB
J6 COMP_RGB
J10 VID_OUT
J13 COMSYNC
J11 RCA MONO OUT
J17 RCA Shunt Select
J18 RCA
J19 CREF
J20 RGB In
J21 CLK_14M
J24 CLK_PH0
J22 Memory Control
VGC Extra J23
J25 Bald-Ext-1
UF2 VGC
U2 28.636360 MHz
U1 MIC5504-3.3YM5
UE7-1E 74LS02
UE7-1B 74LS02
UB2 MC1377
Sheet: /
Title:
Size: A3
Date:
Rev:
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