

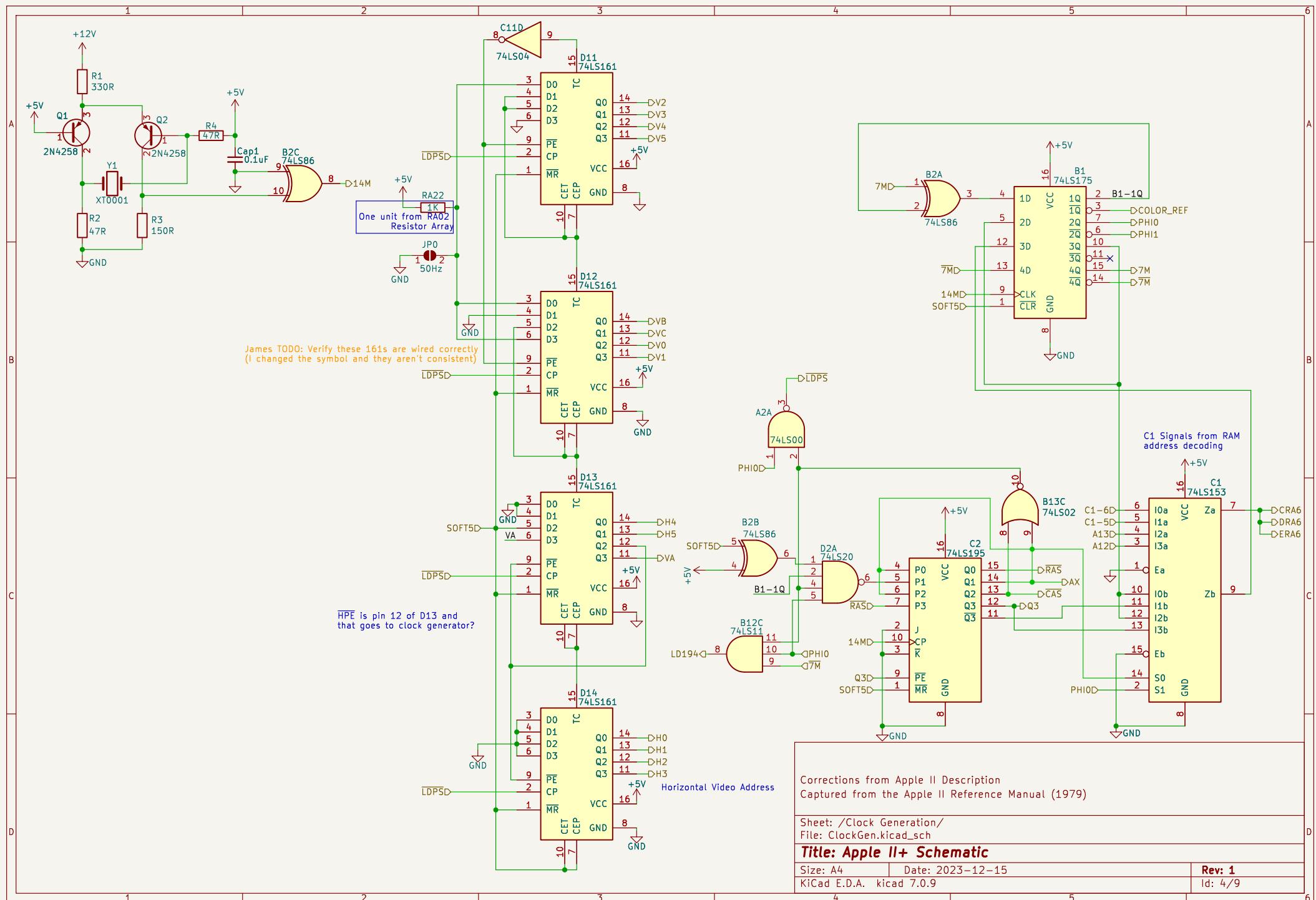
Corrections from Apple II Description
Captured from the Apple II Reference Manual (1979)

Sheet: /RAM Address Multiplexing/
File: RAMAddressMux.kicad_sch

Title: Apple II+ Schematic

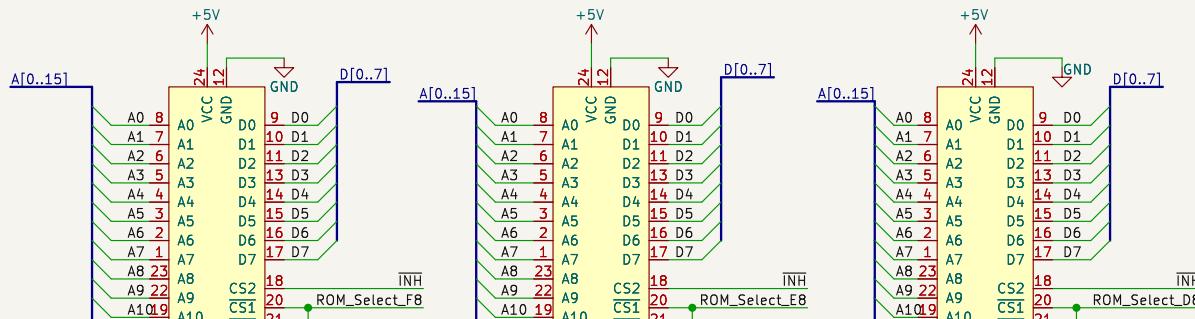
Size: A3 Date: 2023-12-15
KiCad E.D.A. kicad 7.0.9

Rev: 1 Id: 3/9



1 2 3 4 5 6

A

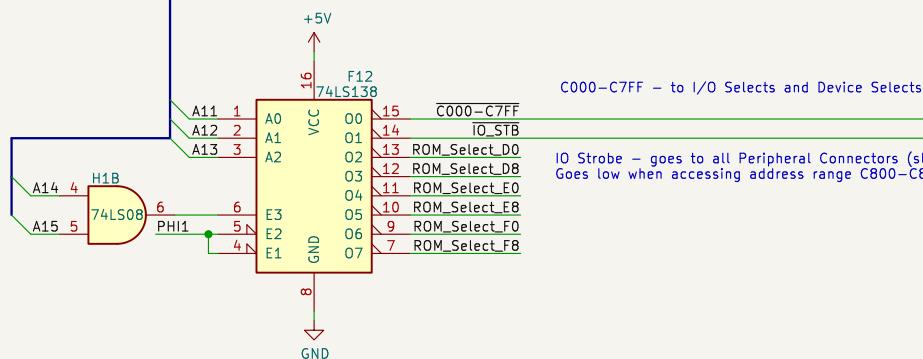


A[0..15] → A[0..15]

INH → INH

PHI1 → PHI1

C



C000-C7FF – to I/O Selects and Device Selects

IO Strobe – goes to all Peripheral Connectors (slots)
Goes low when accessing address range C800-C8FF

Corrections from Apple II Description
Captured from the Apple II Reference Manual (1979)

Sheet: /ROM/
File: ROM.kicad_sch

Title: Apple II+ Schematic

Size: A4 Date: 2023-12-15

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D[0..7] ◊ D[0..7]

A

D

1 2 3 4 5 6

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