SPECIFICATION
FOR
APPROVAL

( ) **Preliminary Specification**
(●) **Final Specification**

TITLE	27.0" UHD/FHD TFT LCD

BUYER	
MODEL	

SUPPLIER	LG Display Co., Ltd.
MODEL	LM270WRA
SUFFIX	SSA1

*When you obtain standard approval,
please use the above model name without suffix

APPROVED BY	SIGNATURE / DATE
/	
/	
/	

Please return 1 copy for your confirmation with your signature and comments.

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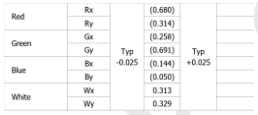
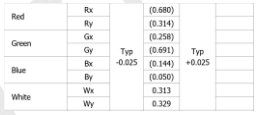
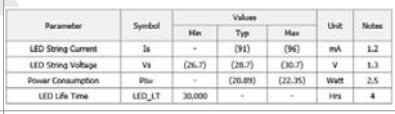
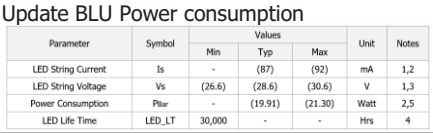
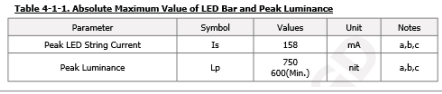
Contents

Record of Revisions

Revision No	Revision Date	Page	Before	After	Application Date
0.0	Mar, 14. 2024	-	-	First Draft(Preliminary)	
0.1	Apr, 26. 2024	27,28	-	Update 2D Drawing File(Front/Rear view)	
0.2	May, 17. 2024	16	-	Update Timing table for VRR (Based on WISTRON request)	
0.3	Jun, 04. 2024	21	-	Update Color Coordinate for P1 	
0.4	Jul, 08. 2024	27, 28	-	Update Label Position Update CoverShield CNT position and size area Update Guide Hole shape changing	
		36, 37	-	Update Thermal data @ HDR600	
0.5	Jul, 24. 2024	4	-	Update LCM Weight information	
		27	-	Update Notes #3. Comment (Screw Torque)	
0.6	Sep, 05. 2024	27	-	Update Drawing file (Cover Shield Protrusion)	
		36, 37	TBD	Update Appendix. Page (for operation HDR function)	
		21		Update Color Coordinate 	
1.0	Oct, 16 2024	4, 8		Update LED String Current(91mA → 87mA) Update BLU Power consumption 	
		22	-	Update Peak LED String Current (162mA → 158mA) 	
		27	-	COF Protrusion Specification : 0.4 → 0.35	

1. General Description

LM270WRA is a color active matrix liquid crystal display with a Light Emitting Diode(LED) backlight assembly without LED driver. The matrix employs a-Si thin film transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 27.0 inch diagonally measured active display area with UHD resolution(3840 horizontal by 2160 vertical pixel array). Each pixel is divided into red, green and blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 10-bit gray scale signal for each dot, thus, presenting a palette of more than 1.07 Billion colors with A-FRC(Advanced Frame Rate Control). It has been designed to apply eDP(HBR2, 5.4Gbps) interface. It is intended to support displays where high brightness, super wide viewing angle, high color saturation, and high color are important.

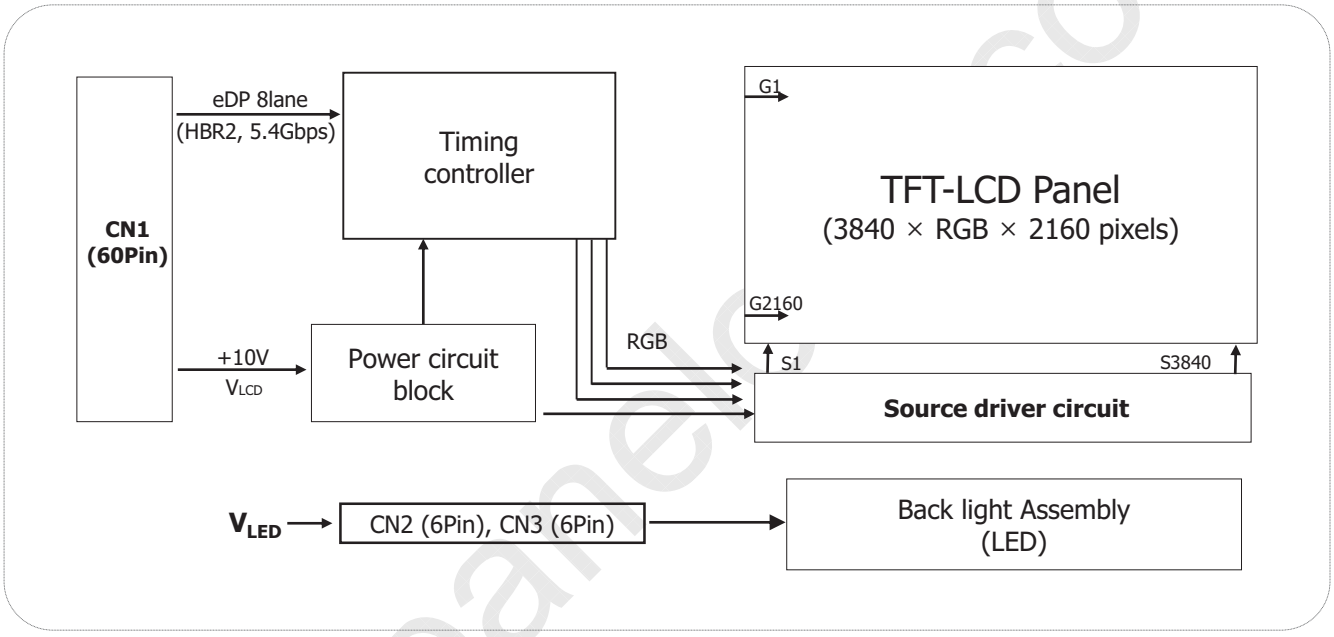


FIG.1 Block Diagram

General Features

Active Screen Size	27.0 inches(68.47cm) (Aspect ratio 16:9)
Outline Dimension	608.80 (H) x 349.40 (V) x 14.10 (D) mm(Typ.)
Pixel Pitch	0.1554(H)mm x 0.1554(V)mm
Pixel Format	3840(H) x 2160(V) Pixels. RGB stripes arrangement.
Color Depth	1.07 Billion colors, 10 Bit(8Bit + A-FRC)
Luminance, White	450cd/m²(Center 1Point, Typ.), Peak 750cd/m²(Center 1Point, Typ.)
Viewing Angle(CR>10)	R/L 178° (Typ.), U/D 178° (Typ.)
Color Gamut (CIE 1976)	DCI 99% (Typ.)
Contrast Ratio	3000:1 (Typ.)
Response Time (Gray to Gray)	14ms (Typ.)
Interface	eDP, 8Lane (HBR2, 5.4Gbps)
Power Consumption	Total 25.11Watt (Typ.)(5.2Watt@ Mosaic_V_{LCD}, 60Hz, 19.91Watt@ Is = 87mA
ES8.0/ErP Lot5/CEL Power Consumption	Total @200nit : 14.91W, EE : 4.70W @ES pattern, 60Hz, BLU : 10.21W (for ES8.0) Total @150nit : 12.14W, EE : 4.70W @ES pattern, 60Hz, BLU : 7.44W (for ErP Lot5) Logic Power : 4.70W @CEL Pattern, 60Hz(for CEL)
Frame rate	60Hz (Typ.), 120Hz (Max.)
Weight	2490g (Typ.)
Display Operating Mode	Transmissive mode, Normally black
Panel type	Reverse type
Surface Treatment	Anti-Glare treatment of the front polarizer(Haze35%, 3H)
Compliance	RoHS(Cd, Pb, Hg, Cr6⁺ free), ES 8.0
Low Blue Light Panel	The ratio of light in the range from 415nm - 455nm compared to 400nm - 500nm shall be less than 35%

※ In case of Compliance, this module supports ES related on LCM.

2. Absolute Maximum Ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 2-1. Absolute Maximum Ratings

Parameter	Symbol	Values		Units	Notes
		Min	Max		
Power Supply Input Voltage	V_{LCD}	-0.3	+12.0	V_{DC}	At 25°C
Operating Temperature	T_{OP}	0	50	°C	1,2,3
Storage Temperature	T_{ST}	-20	60	°C	
Operating Ambient Humidity	H_{OP}	10	90	%RH	
Storage Humidity	H_{ST}	10	90	%RH	
LCM Surface Temperature(Operation)	$T_{surface}$	0	65	°C	1

Notes:
1) Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39°C Max, and no condensation of water.
2) Storage condition is guaranteed under packing condition.
3) LCM surface temperature should be measured under the condition of V_{LCD} = Typ, f_V = 120Hz, T_a = 25°C, no humidity and typical LED string current.
※ Surface temperature of the Component on PCB should be controlled under Tj 125°C (D-IC : Ts 110°C).
If not, problems such as IC damage or decrease of lifetime could occur.

* f_V = Frame frequency
* T_a = Ambient temperature

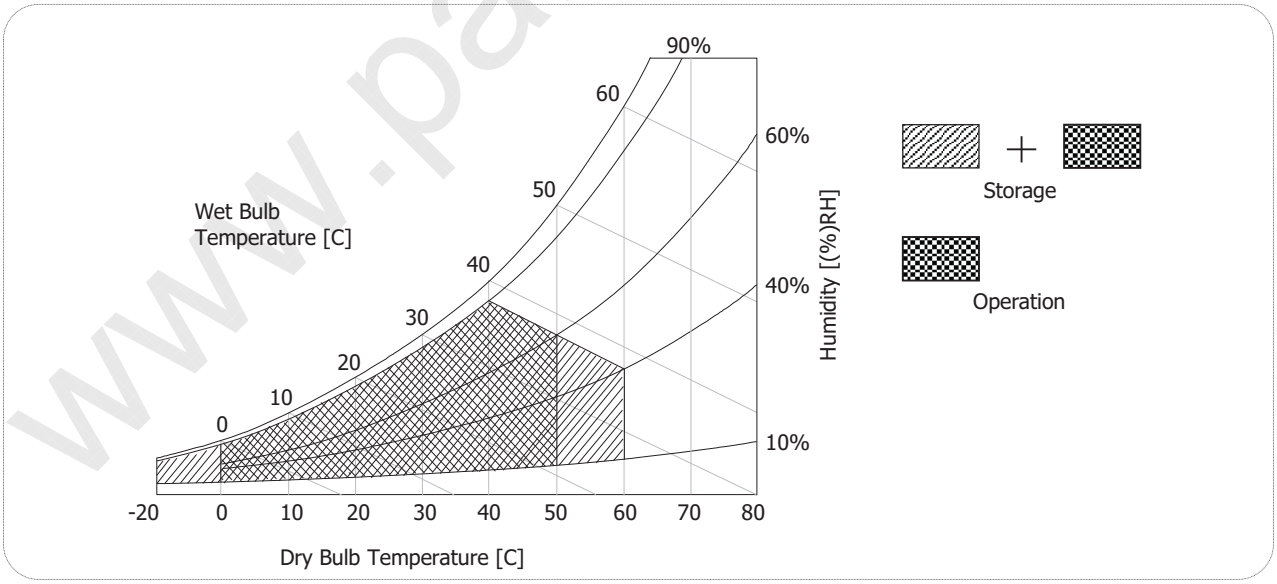


FIG.2 Temperature And Relative Humidity

3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The other input power for the LED/Backlight, is typically generated by a LED Driver. The LED Driver is an external unit to the LCDs.

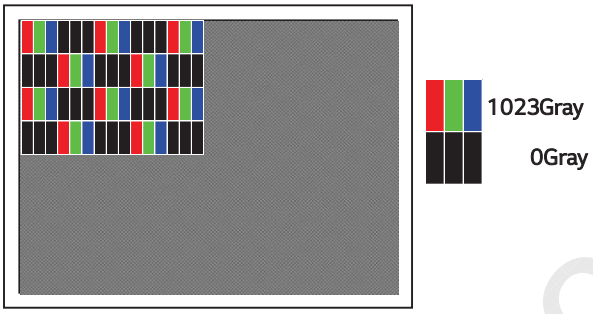
Table 3-1. Electrical Characteristics

Parameter	Symbol	Values			Unit	Notes
		Min	Typ	Max		
Module:						
Power Supply Input voltage	V_{LCD}	9.5	10.0	10.5	Vdc	4
Permissive Power Input Ripple	V_{RIPPLE}	-		400	mVp-p	1
Power Supply Input Current	I_{LCD} Typ.	-	520	650	mA	2
	I_{LCD} Max.	-	1350	1680	mA	
	I_{LCD} White.	-	520	650	mA	
Power Consumption	P_{LCD} Typ.	-	5.20	6.50	Watt	
	P_{LCD} Max.	-	13.50	16.80	Watt	
	P_{LCD} White.	-	5.20	6.50	Watt	
	P_{LCD} Max.	-	23.0	28.7	Watt	2 (fv = *120Hz)
Rush Current	I_{RUSH}	-	-	4.0	A	3

Notes:
1) Permissive power ripple should be measured under the condition of V_{LCD} = Typ, 25±2°C, f_V = Max. Refer to page 7 for the pattern and more information.
2) The specified current and power consumption can be measured under the V_{LCD} = Typ, 25±2°C, f_V = 60Hz and the pattern should be changed according to the typical or maximum power condition. The max. current can be measured only with the maximum power pattern. See the page 7 for details.
3) Maximum condition of inrush current: The duration of rush current is about 5ms and rising time of power input is 500us ±20%.(Min).
4) V_{LCD} level must be measured between two points on PCB of LCM V_{LCD}(test point) ~ LCM Ground. (Test condition: Maximum power pattern, 25°C, f_V = 60Hz)

- f_V = Frame frequency

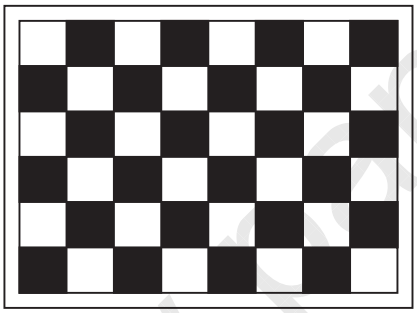
• **Permissive Power Input Ripple**(V_{LCD} = Typ, 25°C, f_V(frame frequency) = Max condition)



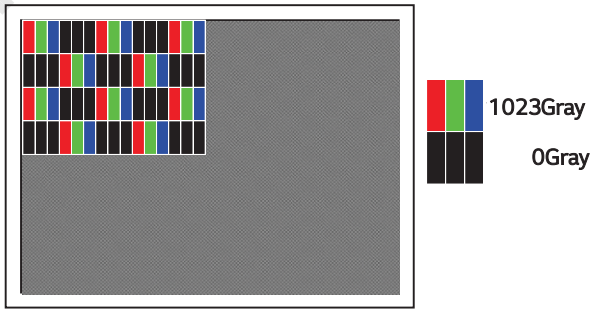
Maximum power pattern

For the exact ripple measurement, the condition of Max 20MHz is recommended in the bandwidth configuration of oscilloscope.

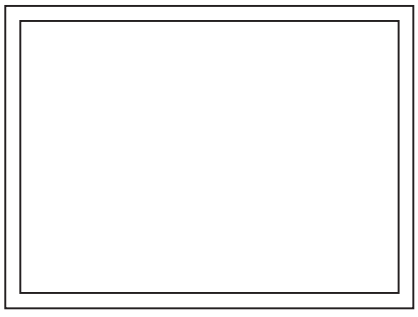
• **Power Consumption**(V_{LCD} = Typ, 25°C, f_V(frame frequency) = 60Hz condition)



Typical power pattern



Maximum power pattern



White Power Pattern

FIG.3-1 Pattern For Power Consumption Measurement

Table 3-2. LED Bar Electrical Characteristics

Parameter	Symbol	Values			Unit	Notes
		Min	Typ	Max		
LED String Current	Is	-	87	92	mA	1,2
LED String Voltage	Vs	26.6	28.6	30.6	V	1,3
Power Consumption	PBar	-	19.91	21.30	Watt	2,5
LED Life Time	LED_LT	30,000	-	-	Hrs	4

Note: The LED consists of 80 LED packages, 8 strings(parallel) x 10 packages(serial)

Notes:
1) The specified values are for single LED bar.
2) The specified current is defined as the input current for single LED string with 100% duty cycle.
3) The specified voltage is the input LED string voltage at typical current 100% duty cycle.
4) The LED life time is defined as the when brightness of LED itself reach to the 50% of initial value under the conditions at T_a = 25±2℃ and typical LED string current.
5) The power consumption shown above does not include the loss of external LED driver.
The typical power consumption is calculated as P_{bar} = Vs(Typ.) x Is(Typ.) x No. of strings.
The maximum power consumption is calculated as P_{Bar} = Vs(Max.) x Is(Typ.) x No. of strings.

3-2. Interface Connections

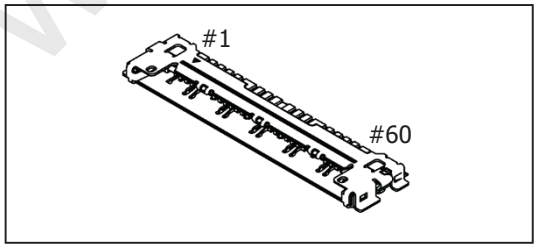
3-2-1. LCD Module

- LCD Connector(Receptacle): 20525-060E-01(Manufactured by I-PEX)
- Mating Connector(Plug): 20523-060T(Manufactured by I-PEX)

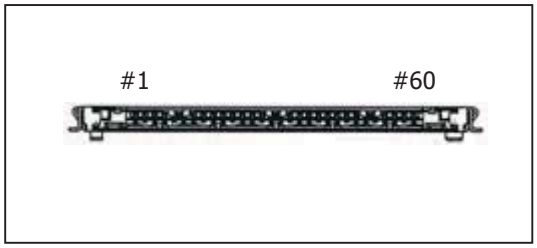
Table 3-3-1. Module Connector(CN1) Pin Configuration

No	Symbol	Description	No	Symbol	Description
1	GND	Ground	31	DP0_L1_N	Master Component Signal for Main Link 1
2	V_{LCD}	Power Supply +10.0V	32	GND	Ground
3	V_{LCD}	Power Supply +10.0V	33	DP0_L2_P	Master True Signal for Main Link 2
4	V_{LCD}	Power Supply +10.0V	34	DP0_L2_N	Master Component Signal for Main Link 2
5	V_{LCD}	Power Supply +10.0V	35	GND	Ground
6	V_{LCD}	Power Supply +10.0V	36	DP0_L3_P	Master True Signal for Main Link 3
7	V_{LCD}	Power Supply +10.0V	37	DP0_L3_N	Master Component Signal for Main Link 3
8	V_{LCD}	Power Supply +10.0V	38	GND	Ground
9	V_{LCD}	Power Supply +10.0V	39	DP1_L0_P	Slave True Signal for Main Link 0
10	GND	Ground	40	DP1_L0_N	Slave Component Signal for Main Link 0
11	GND	Ground	41	GND	Ground
12	GND	Ground	42	DP1_L1_P	Slave True Signal for Main Link 1
13	GND	Ground	43	DP1_L1_N	Slave Component Signal for Main Link 1
14	GND	Ground	44	GND	Ground
15	GND	Ground	45	DP1_L2_P	Slave True Signal for Main Link 2
16	GND	Ground	46	DP1_L2_N	Slave Component Signal for Main Link 2
17	BIST	L(GND): Black, H(3.3V): Rotational Pattern	47	GND	Ground
18	GND	Ground	48	DP1_L3_P	Slave True Signal for Main Link 3
19	NC	No Connection(I2C serial interface for LCM)	49	DP1_L3_N	Slave Component Signal for Main Link 3
20	NC	No Connection(I2C serial interface for LCM)	50	GND	Ground
21	DP0_HPD	Master Hot Plug Detect Signal	51	DP1_AUX_P	Slave True Signal for Auxiliary Channel
22	DP1_HPD	Slave Hot Plug Detect Signal	52	DP1_AUX_N	Slave Component Signal for Auxiliary Channel
23	GND	Ground	53	GND	Ground
24	DP0_AUX_P	Master True Signal for Auxiliary Channel	54	NC	No Connection(I2C serial interface for LCM)
25	DP0_AUX_N	Master Component Signal for Auxiliary Channel	55	NC	No Connection(I2C serial interface for LCM)
26	GND	Ground	56	NC	No Connection
27	DP0_L0_P	Master True Signal for Main Link 0	57	GND	Ground
28	DP0_L0_N	Master Component Signal for Main Link 0	58	NC	No Connection
29	GND	Ground	59	GND	Ground
30	DP0_L1_P	Master True Signal for Main Link 1	60	NC	No Connection

Notes:
1) All GND(ground) pins should be connected together to the LCD module's metal frame.
2) All V_{LCD}(power input) pins should be connected together.
3) BIST(Build In Self Test): If BIST pin is tied to "High(3.3V)", T-con generates rotational pattern. Time to stay at every pattern is about 2sec.



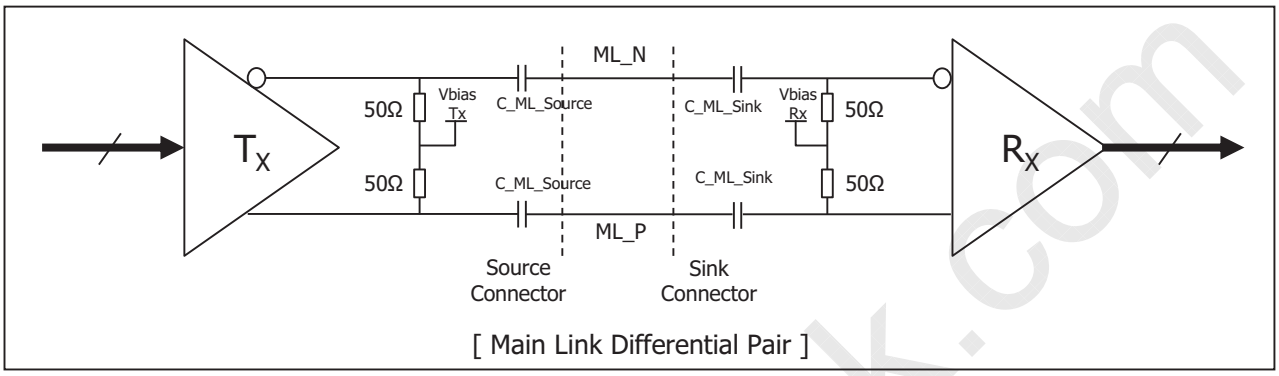
20525-060E-01(I-PEX)



Rear view of LCM

3-2-2. eDP Signal Specifications

1. eDP Main Link Signal

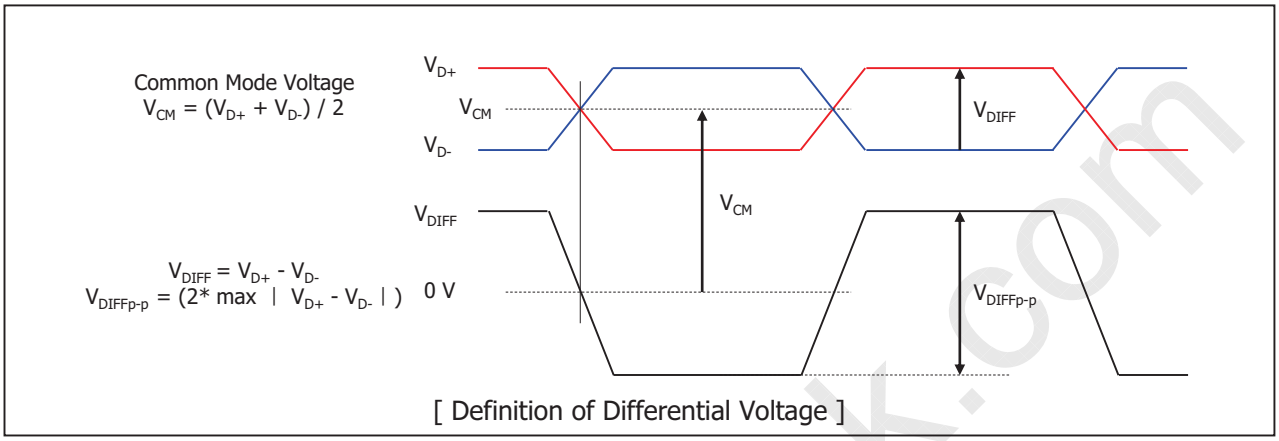


[Main Link Differential Pair]

Parameter	Symbol	Min	Typ	Max	Unit	Notes
Unit Interval for High Bit Rate (5.4Gbps / Lane)	UI_HBR2	-	185	-	ps	
Link Clock Down Spreading	Amplitude	0	-	0.5	%	
	Frequency	30	-	33	kHz	
Maximum Output Voltage Level at Source Side Connector	$V_{TX-DIFFp-p-Max}$	-	-	1.38	V	6
Differential Peak to peak Voltage at Sink Side Connector	$V_{RX-DIFFp-p}$	0.09	-	-	V	7
EYE width at Sink Side Connector	$T_{RX-EYE-CONN}$	0.38	-	-	UI	6,7
Lane Intra-pair Skew	$L_{Rx-SKEW-INTRA\_PAIR}$	-	-	50	ps	
Line skew(Port-to-Port)	Tx-to-Tx_skew	-	-	± 240	clk	8
AC Coupling Capacitor	$C_{SOURCE-ML}$	75	-	200	nF	Source side

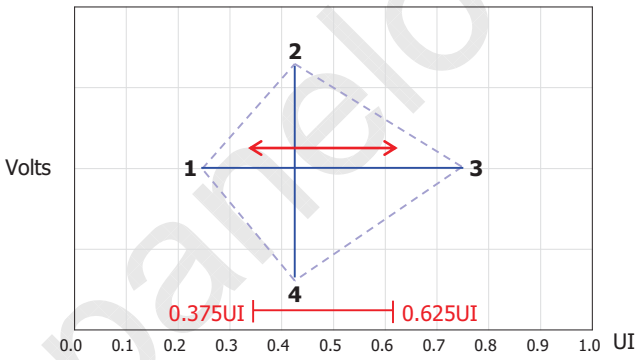
Notes:
1) In cabled embedded system, it is recommended the system designer ensure that EYE width and voltage are met at the sink side connector pins.
2) Mismatched common mode voltage will occur abnormal display.
3) All eDP electrical spec is measured at sink connector side.

Note 6) Definition of Differential Voltage



[Definition of Differential Voltage]

Note 7) Main Link EYE Diagram



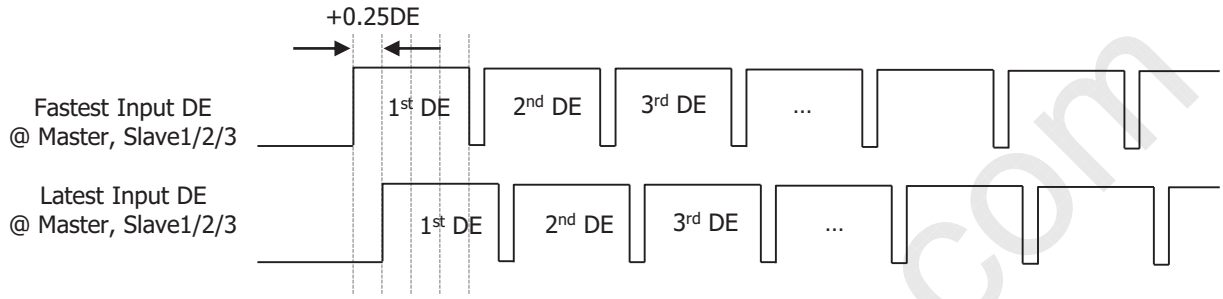
[EYE Mask at Sink Connector Pins]

Point	High Bit Rate 2 @ TP3 EQ	
	Time(UI)	Voltage(V)
1	Any UI location (x) where the eye width is open from x to x + 0.38UI	0.000
2	Any passing UI location between 0.375UI - 0.625UI	0.045
3	Point 1 + 0.38UI	0.000
4	Same as Point 2	-0.045

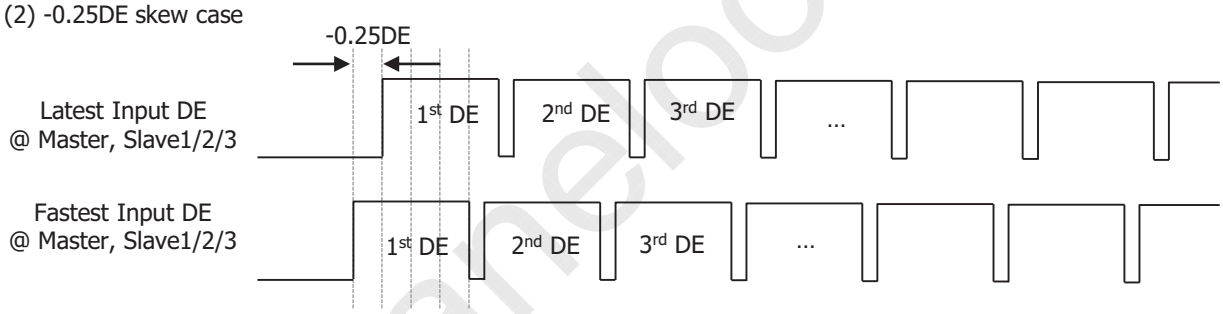
[EYE Mask Vertices at embedded DP Sink Connector Pins]

Note 8) Master Tx to Slave Tx Skew Margin Case

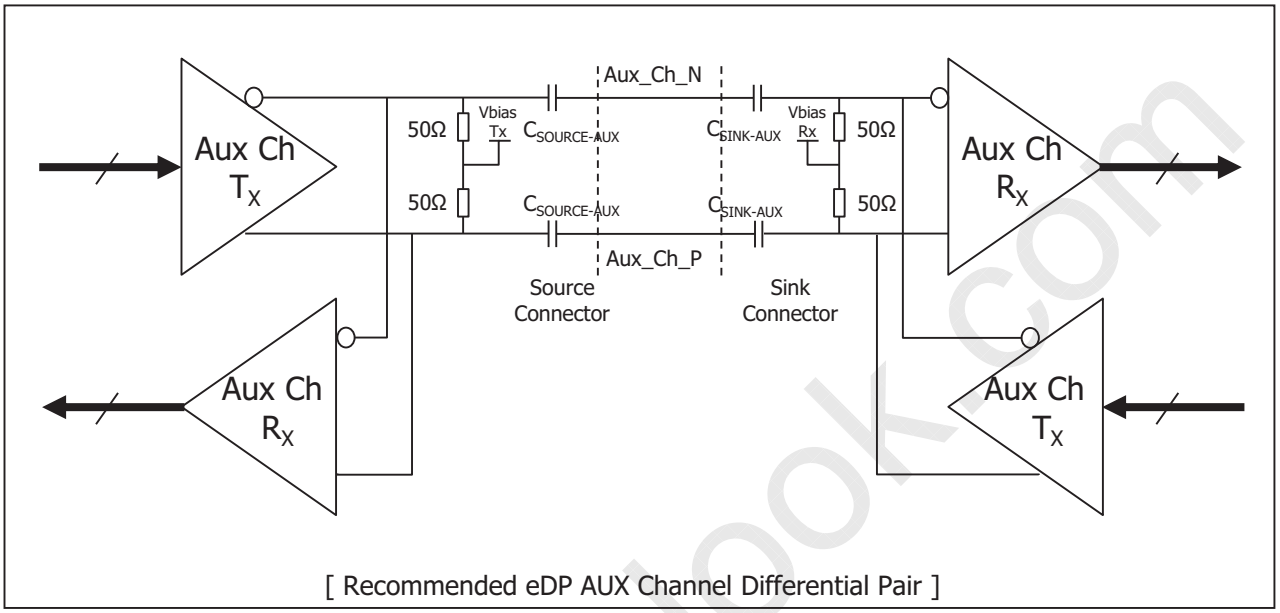
(1) +0.25DE skew case



(2) -0.25DE skew case



2. eDP AUX Channel Signal

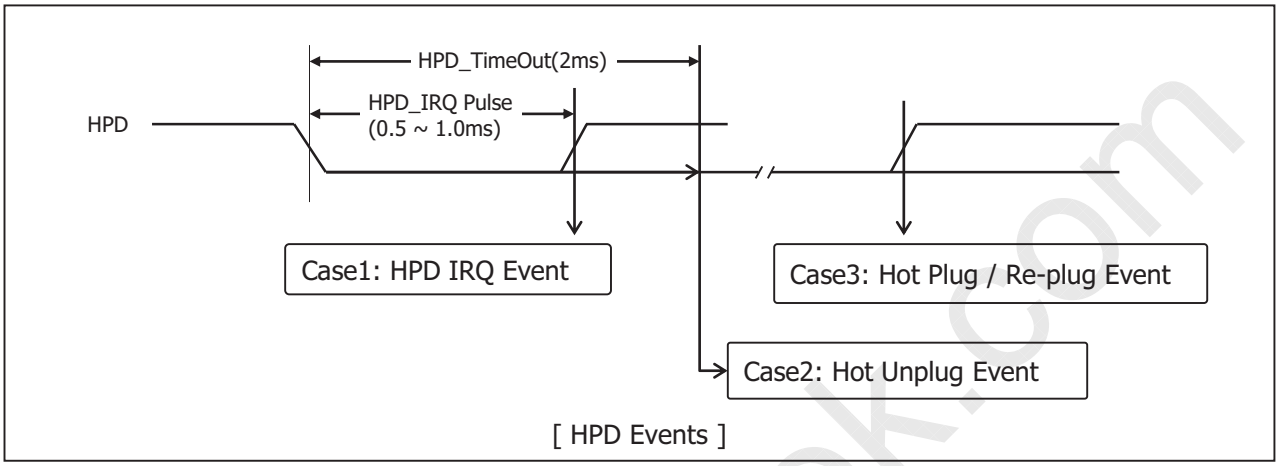


[Recommended eDP AUX Channel Differential Pair]

Parameter	Symbol	Min	Typ	Max	Unit	Notes
AUX Unit Interval	UI	0.4	-	0.6	us	
AUX Jitter at Rx IC Package Pins	T_{jitter}	-	-	0.05	UI	Equal to 30ns
AUX Peak-to-peak Voltage at Connector Pins of Receiving	$V_{AUX-DIFFp-p}$	0.32	-	1.36	V	
AUX Peak-to-peak Voltage at Connector Pins of Transmitting		0.39	-	1.38	V	
AUX EYE width at Connector Pins of Tx and Rx		0.98	-	-	UI	
AUX AC Coupling Capacitor	$C_{SOURCE-AUX}$	75	-	200	nF	Source side

Notes:
1) $V_{AUX-DIFFp-p} = 2 * | V_{AUXP} - V_{AUXN} |$
2) Termination resistor should be 50ohm ± 5% at source side to AUX level.
3) Mismatched common mode voltage will occur abnormal display.

3. eDP HDP Signal



[HPD Events]

Parameter	Symbol	Min	Typ	Max	Unit	Notes
HPD Voltage	HPD	2.25	-	3.6	V	Sink side Driving
Hot Plug Detection Threshold		2.0	-	-	V	Source side Detecting
Hot Unplug Detection Threshold		-	-	0.8	V	
HPD_IRQ Pulse Width	HPD_IRQ	0.5	-	1.0	ms	
HPD_TimeOut		2.0	-	-	ms	HPD Unplug Event

Notes:
1) HPD IRQ: Sink device wants to notify the Source device that Sink's status has changed so it toggles HPD line, forcing the Source device to read its Link / Sink Receiver DPCD field via the AUX-CH.
2) HPD Unplug: The Sink device is no longer attached to the Source device and the Source device may then disable its Main Link as a power saving mode.
3) Plug / Re-plug: The Sink device is now attached to the Source device, forcing the Source device to read its Receiver capabilities and Link / Sink status Receiver DPCD fields via the AUX-CH.

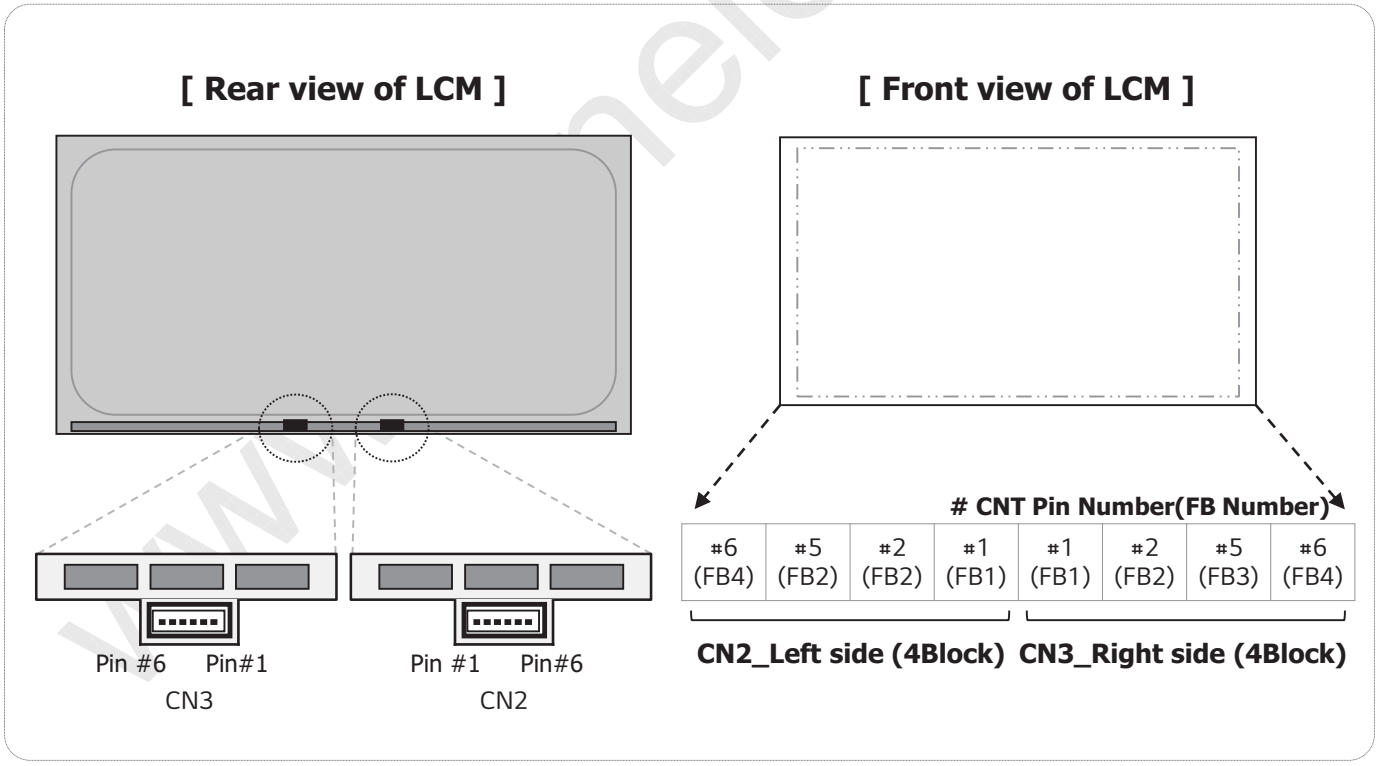
3-2-3. Backlight Connector Pin Configuration

The LED interface connector is 10035WS-H06D(HF)_Manufactured by YEONHO.
The mating connector is a 10035HS-H06G(HF) or SHJP-06V-S(HF).
The pin configuration for the connector is shown in the table below.

Table 3-4. LED Connector Pin Configuration

Pin	Symbol	Pin-description (CN2)	Remark
#1	FB1	Channel 1 current feedback	Left side in front view
#2	FB2	Channel 2 current feedback	
#3	V LED	LED power supply (common anode)	
#4			
#5	FB3	Channel 3 current feedback	
#6	FB4	Channel 4 current feedback	

Pin	Symbol	Pin-description (CN3)	Remark
#1	FB1	Channel 1 current feedback	Right side in front view
#2	FB2	Channel 2 current feedback	
#3	V LED	LED power supply (common anode)	
#4			
#5	FB3	Channel 3 current feedback	
#6	FB4	Channel 4 current feedback	



3-3. Signal Timing Specifications

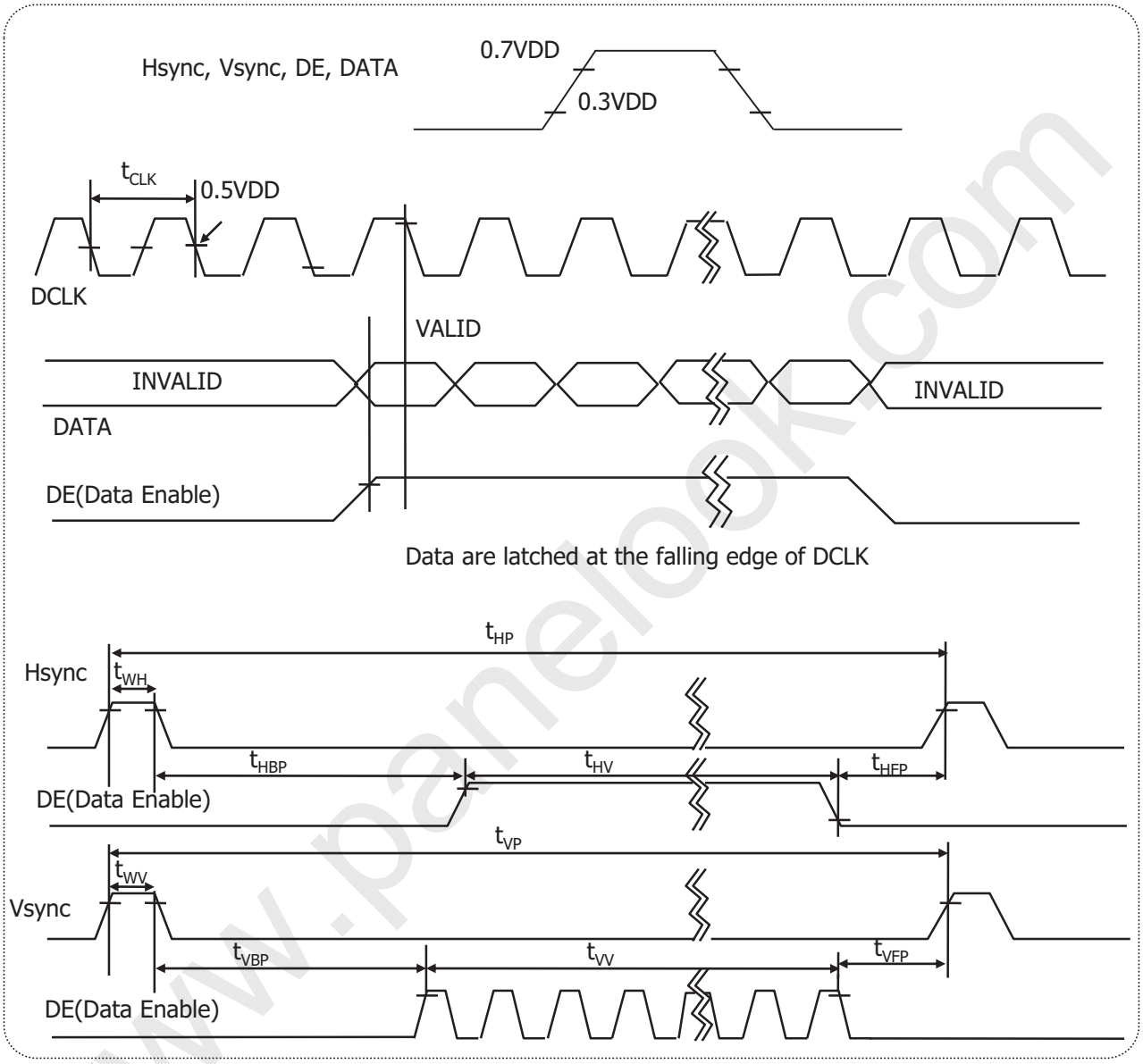
This is the signal timing requirement from the signal transmitter. All of the interface signal timing should be satisfied with the following specifications for its proper operation.

Table 3-5. Timing Table

Item	Symbol	Symbol	Min	Typ	Max	Unit	Notes
DCLK	Period	tCLK	1.86	3.75	4.79	ns	Pixel frequency (Typ. 532.80 MHz)
	Frequency	fCLK	208.68	266.40	547.00	MHz	
Hsync	Period	tHP	2000	2000	2400	tCLK	1,3,4
	Horizontal Valid	tHV	1920	1920	1920	tCLK	
	Horizontal Blank	tHB	80	80	480	tCLK	
	Frequency	fH	106.56	133.20	273.50	kHz	
Vsync	Period	tVP	2220	2220	5825	tHP	2,4
	Vertical Valid	tVV	2160	2160	2160	tHP	
	Vertical Blank	tVB	60	60	3665	tHP	
	Frequency	fV	47	60	121	Hz	

Notes:
1) The value of Hsync Period, Hsync Width and Hsync valid should be even number times of tCLK. If the value is odd number times of tCLK, it can make asynchronous signal timing and cause abnormal display.
2) The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
3) The value of Hsync Period, Hsync Width, and Horizontal Back Porch should be divided by 4 without a remainder.
4) The polarity of Hsync, Vsync is not restricted.

3-4. Signal Timing Waveforms



3-5. Color Data Reference

The Brightness of each primary color(Red,Green,Blue) is based on the 10-bit gray scale data input for the color; the higher the binary input, the brighter the color.
The table below provides a reference for color versus data input.

Table 3-6. Color Data Reference

Color		Input Color Data																											
		RED										GREEN										BLUE							
		MSB									LSB	MSB									LSB	MSB							LSB
		R9	R8	R7	R6	R5	R4	R3	R2	R1	R0	G9	G8	G7	G6	G5	G4	G3	G2	G1	G0	B9	B8	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	Blue (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (1)	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	...					...										...										...					
	RED (1022)	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (1023)	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
	...					...										...										...					
	GREEN (1022)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
	GREEN (1023)	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
BLUE	BLUE (0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	...					...										...										...					
	BLUE (1022)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0
	BLUE (1023)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1

3-6. Power Sequence

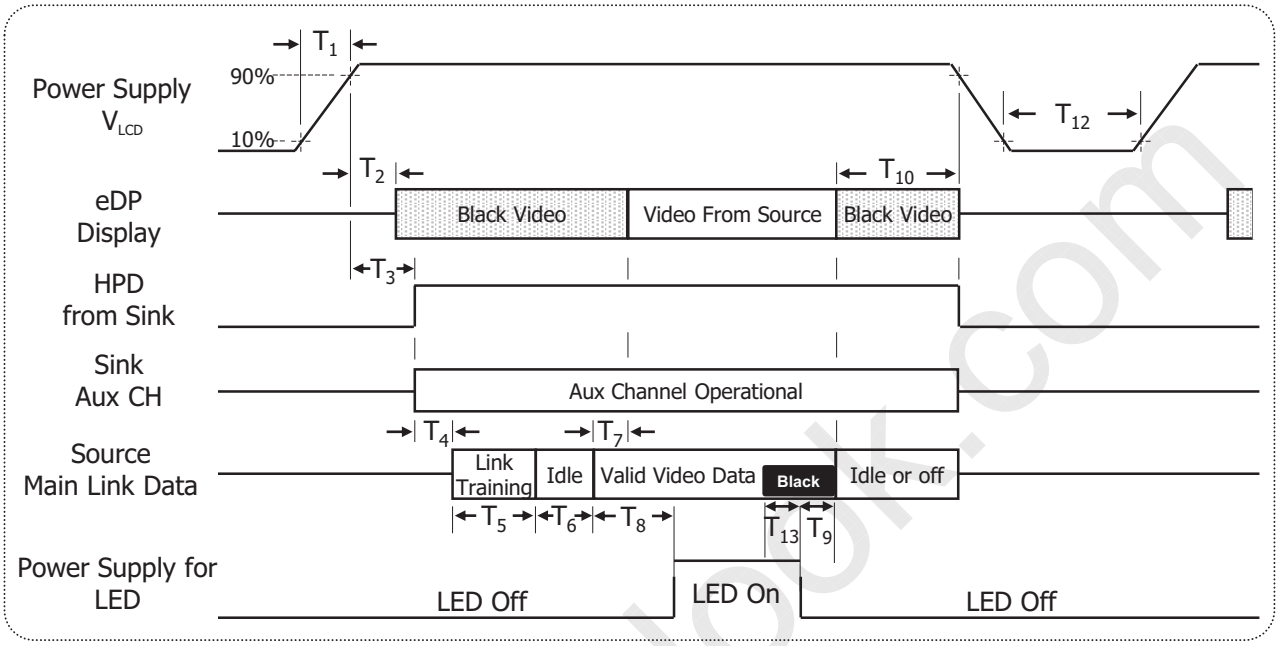


Table 3-7. Power Sequence

Timing	Required By	Limits		Units	Notes
		Min	Max		
T_1	Source	0.5	10	ms	
T_2	Sink	10	200	ms	
T_3	Sink	15	200	ms	
T_4	Source	-	-	ms	5
T_5	Source	-	-	ms	5
T_6	Source	-	100	ms	6
T_8	Source	350	-	ms	
T_9	Source	200	-	ms	4

Timing	Required By	Limits		Units	Notes
		Min	Max		
T_{10}	Source	0	500	ms	
T_{12}	Source	1000	-	ms	
T_{13}	Source	20	-	ms	

Notes:
1) Power sequence should be kept all the time including below cases for normal operation.
   - AC/DC Power On/Off
   - Mode change (resolution, frequency, timing, sleep mode, color depth change, etc.)
   The violation of power sequence can cause a significant trouble in display and reliability.
2) Please avoid floating state of interface signal during signal invalid period.
3) When the interface signal is invalid, be sure to pull down the V_{LCD}.(0V)
4) Please turn off the power supply for LED when the level of V_{LCD} changes to prevent noise issue.
5) Link training duration is dependent on the customer's system.
6) It includes Source Frame Synchronization time.
   Source Frame Synchronization: Time to prepare before Tx(Source) sends valid data(Invalid period).

3-7. Power Dip Condition

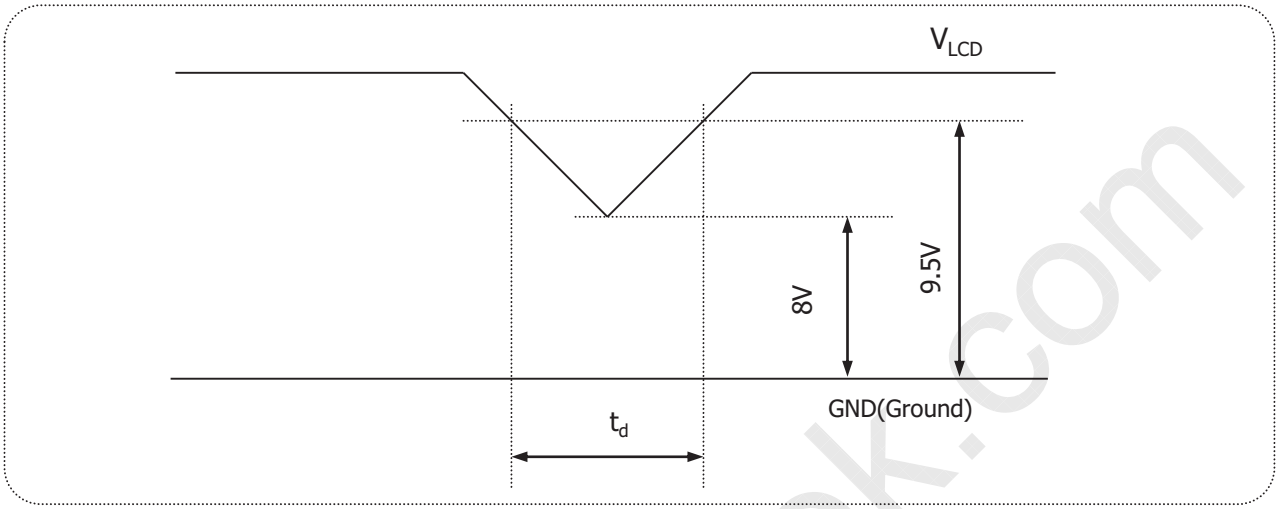


FIG.3-3 Power Dip Condition

For proper operation, stable power supply of V_{LCD} is necessary and power dip is allowed only in below condition. Except this condition, power on/off should follow power sequence specification exactly.

1) Dip Condition

$8V \leq V_{LCD} < 9.5V$, $t_d \leq 20ms$

4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' for approximately 30 minutes in a dark environment at 25±2°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0° and aperture 1 degree. FIG.4-1 presents additional information concerning the measurement equipment and method.

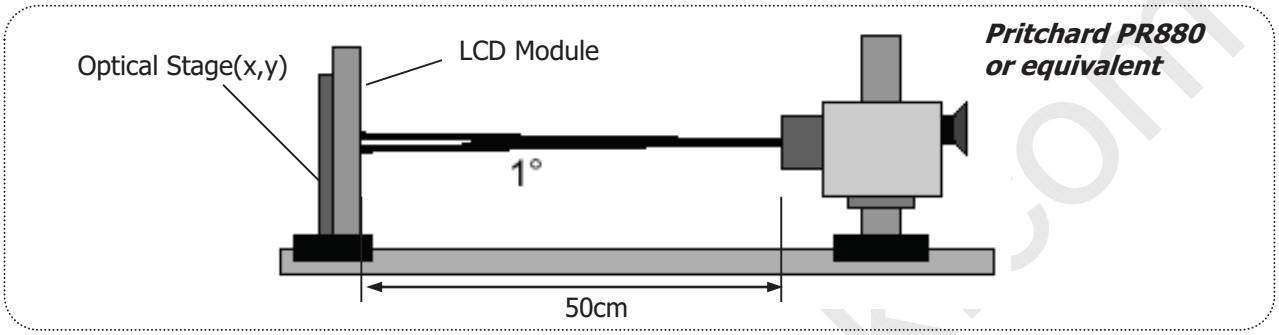


FIG.4-1 Optical Characteristic Measurement Equipment And Method

Table 4-1. Optical Characteristics

(T_a=25 °C, V_{LCD}=Typ, f_V=60Hz, DCLK=Typ, I_S=Typ.)

Parameter		Symbol	Values			Units	Notes
			Min.	Typ.	Max.		
Contrast Ratio		CR	2400	3000	-		1
Surface Luminance, white		L_{WH}	360	450	-	cd/m^2	2
Luminance Variation		δ_{WHITE}	75	80	-	%	3
Response Time	Gray to Gray	T_{GTG_AVR}	-	14	25	ms	4
Color Gamut (CIE 1931)		sRGB	96	100	-	%	5
Color Gamut (CIE 1976)		DCI	95	99	-	%	
Color Coordinates [CIE 1931] ***(By PR650)***	Red	Rx	Typ -0.025	0.682	Typ +0.025		
		Ry		0.312			
	Green	Gx		0.248			
		Gy		0.699			
	Blue	Bx		0.144			
		By		0.047			
	White	Wx		0.313			
		Wy		0.329			
Color Temperature		-	-	6500	-	K	
Viewing Angle (CR>10, General)	Horizontal	θ_H	170	178	-	Degree	6
	Vertical	θ_V	170	178	-		
Gray Scale		-	Typ -0.2	2.2	Typ +0.2		7

4-1. Characteristics of Peak Luminance

Table 4-1-1. Absolute Maximum Value of LED Bar and Peak Luminance

Parameter	Symbol	Values	Unit	Notes
Peak LED String Current	Is	158	mA	a,b,c
Peak Luminance	Lp	750 600(Min.)	nit	a,b,c

Notes:
a) Peak LED string voltage at peak current with 100% duty cycle is (30.8±2.0) V at T_a = 25±2°C.
b) Table 4-1-1 is reference data only for HDR Function usage, refer to the appendix of LCM temperature at peak current.
c) Peak luminance 750nit is achieved at 158mA, while the specifications for guarantee remains under the normal operating condition specified in Table 3-2. Specifications and condition for evaluation test and mass production shall be applied with conditions specified in Table 3-2.

Notes:

1) **Contrast Ratio(CR)** is defined mathematically as: ***(By PR880)***
It is measured at center point(1)

$$\text{Contrast ratio} = \frac{\text{Surface luminance with all white pixels}}{\text{Surface luminance with all black pixels}}$$

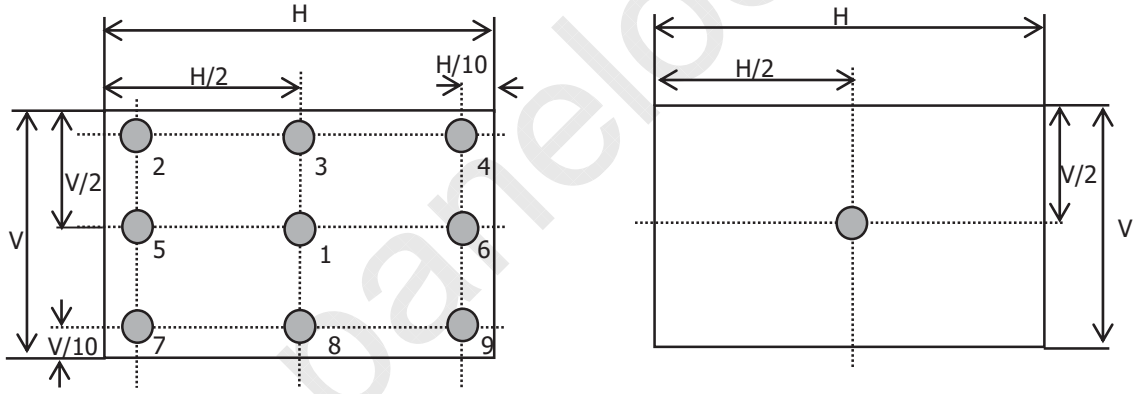
2) **Surface Luminance(L_{WH})** is the luminance value at center 1 point(1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG.4-1. ***(By PR880)***

3) The **Variation in Surface Luminance** , δ_{WHITE} is defined as: ***(By PR880)***

$$\delta_{WHITE} = \frac{\text{Minimum}(L_{P1}, L_{P2}, \ldots, L_{P9})}{\text{Maximum}(L_{P1}, L_{P2}, \ldots, L_{P9})} \times 100(\%)$$

Where L1 to L9 are the luminance with all pixels displaying white at 9 locations.
For more information see FIG.4-2.

<Measuring Point For Luminance Variation> **<Measuring Point For Surface Luminance>**



@ H,V: Active Area

FIG.4-2 Measure Point for Luminance

Notes:

4) The **Gray To Gray Response Time** is defined as the following figure and shall be measured by switching the input signal for "Gray To Gray ". ***(By RD80S)***
- Gray step: 5 Step
- T_{GTG_AVR} is the total average time at rising time and falling time for "Gray To Gray ".
- For the GTG measurement, the sampling rate of oscilloscope is 500k/s.

Table 4-2. GTG Gray

Gray to Gray		Rising Time				
		G1023	G767	G511	G255	G0
Falling Time	G1023					
	G767					
	G511					
	G255					
	G0					

Response Time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

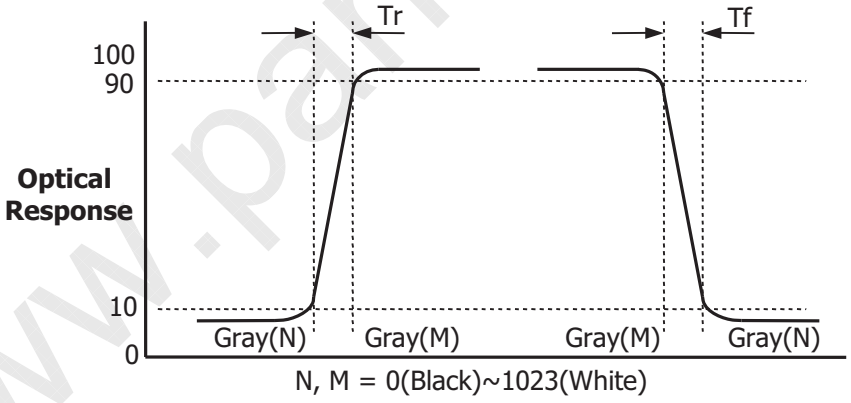


FIG.4-3 Response Time

5) **sRGB color gamut**, which is more than or equal to typ. 99.5%, is stated as 100%

Notes:

6) **Viewing Angle** is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG.4-4. ***(By PR880)***

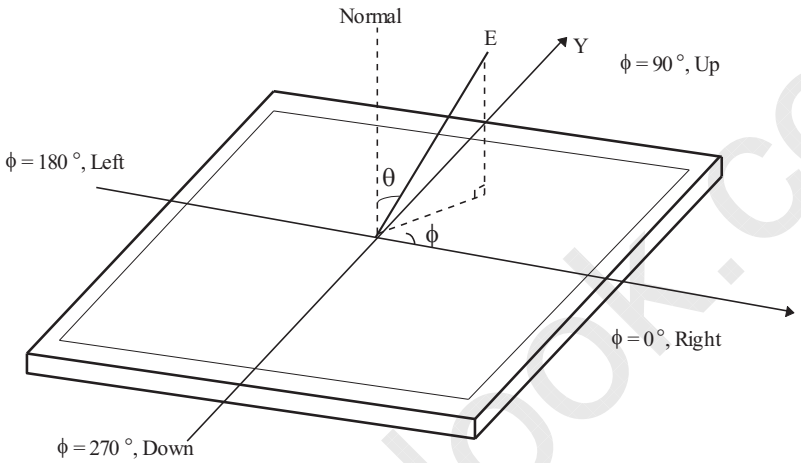


FIG.4-4 Viewing Angle

7) **Gamma Value** is approximately 2.2. For more information see below table.

Table 4-3. Gray Scale Specification

Gray Level	Relative Luminance [%](Typ)
0	0.03
63	0.25
127	1.08
191	2.50
255	4.72
319	7.70
383	11.49
447	16.20
511	21.66
575	28.20
639	35.45
703	43.80
767	53.00
831	63.30
895	74.48
959	86.80
1023	100

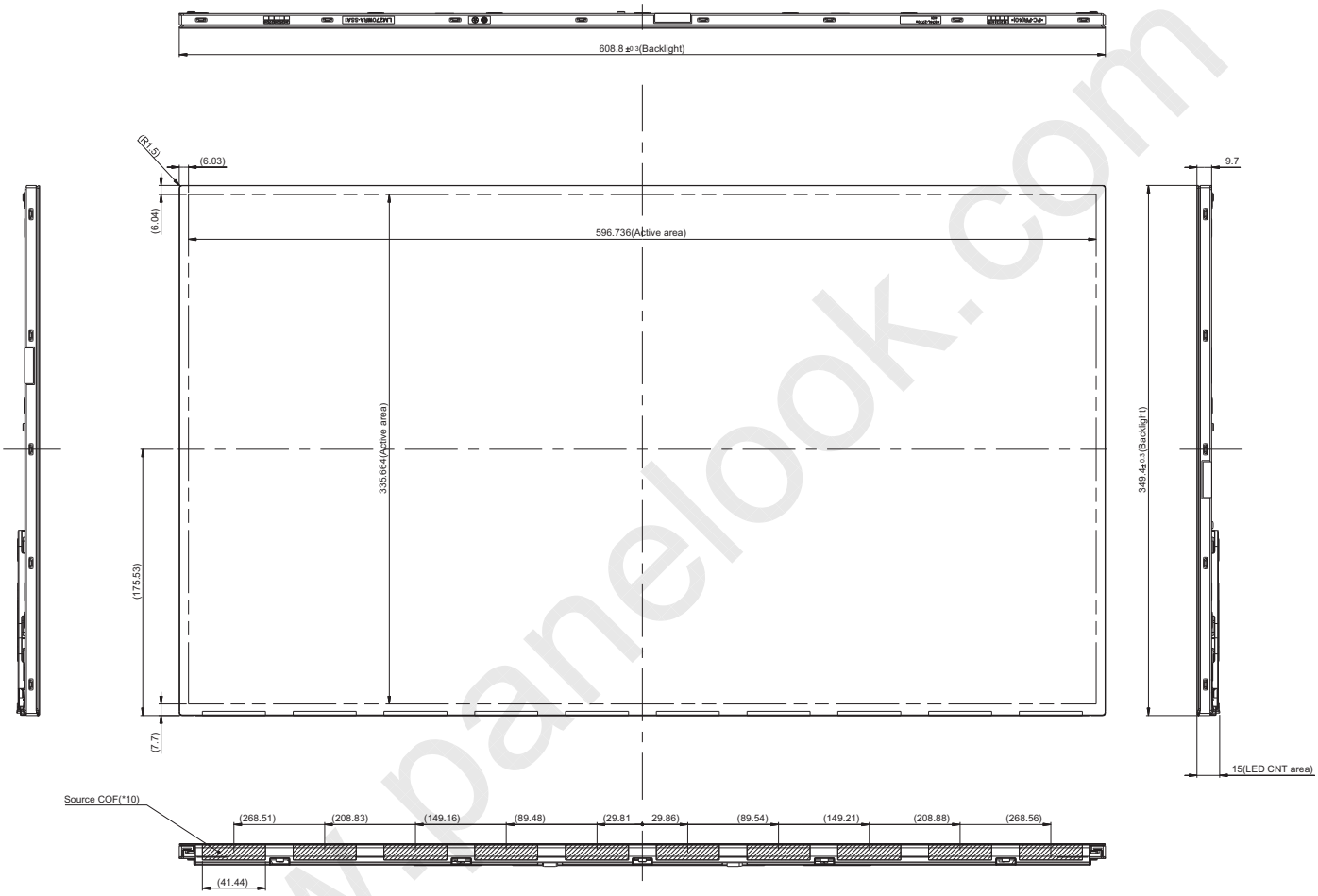
5. Mechanical Characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

Outline Dimension	Horizontal	608.80mm
	Vertical	349.40mm
	Thickness(top/bottom)	9.70mm / 14.10mm
Bezel size (A/A ~ Module outline)	Up / Down / Left / Right	6.04mm / 7.7mm / 6.03mm / 6.03mm
Active Display Area	Horizontal	596.736 mm
	Vertical	335.664 mm
Weight	Typ: 2490g , Max: 2610g	
Surface Treatment	Anti-Glare treatment of the front polarizer(Haze35%, 3H)	
BLU type (LED bar quantity/position)	Horizontal 1bar(Down)	

Note: Please refer to a mechanical drawing in terms of tolerance at the next page.
- Outline dimensions (horizontal, vertical and outside depth) are measured by using vernier calipers.
- The inside depth dimensions are measured by using height gauge, when LCM is put face down onto a flat surface.

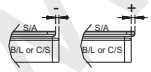
<Front View>



Notes

1. I/F connector specification : i-PEX / 20525-060E-01 or Equivalent
2. LED connector specification: YEONHO, 10035HS-H06C or Equivalent
3. Torque of M3 user hole : 3kgf.cm ~ 6kgf.cm
   (Screw angle : 90˚, Screwdriver angle : 90˚±10˚)

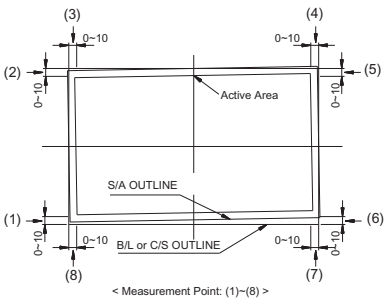
4-1. Tilt Value definition



4-2. Tilt Specifications
- I Max - Min I ≤ 0.3 (@ each side)

Position	Typ.	Tolerance	Range
(1),(2)_Left	-0.1	±0.3	-0.4 ~ +0.2
(3),(4)_Up	-0.1	±0.3	-0.4 ~ +0.2
(5),(6)_Right	-0.1	±0.3	-0.4 ~ +0.2
(7),(8)_Down	-0.1	±0.3	-0.4 ~ +0.2

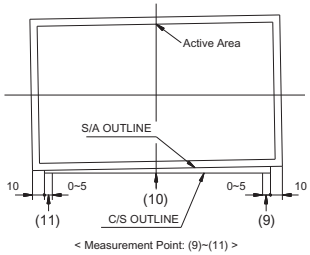
5. Unspecified tolerances to be ± 0.5
6. COF Protrusion Specification : under 0.35
7. The LCM warp(warpage) is less than 1.0 on the surface plate
8. The COF area is weak & sensitive, so please don't press the COF area
9. Undefined height should follow the 3D modeling data
10. Protect Pad should not be removed unless system assembling



< Measurement Point: (1)~(8) >

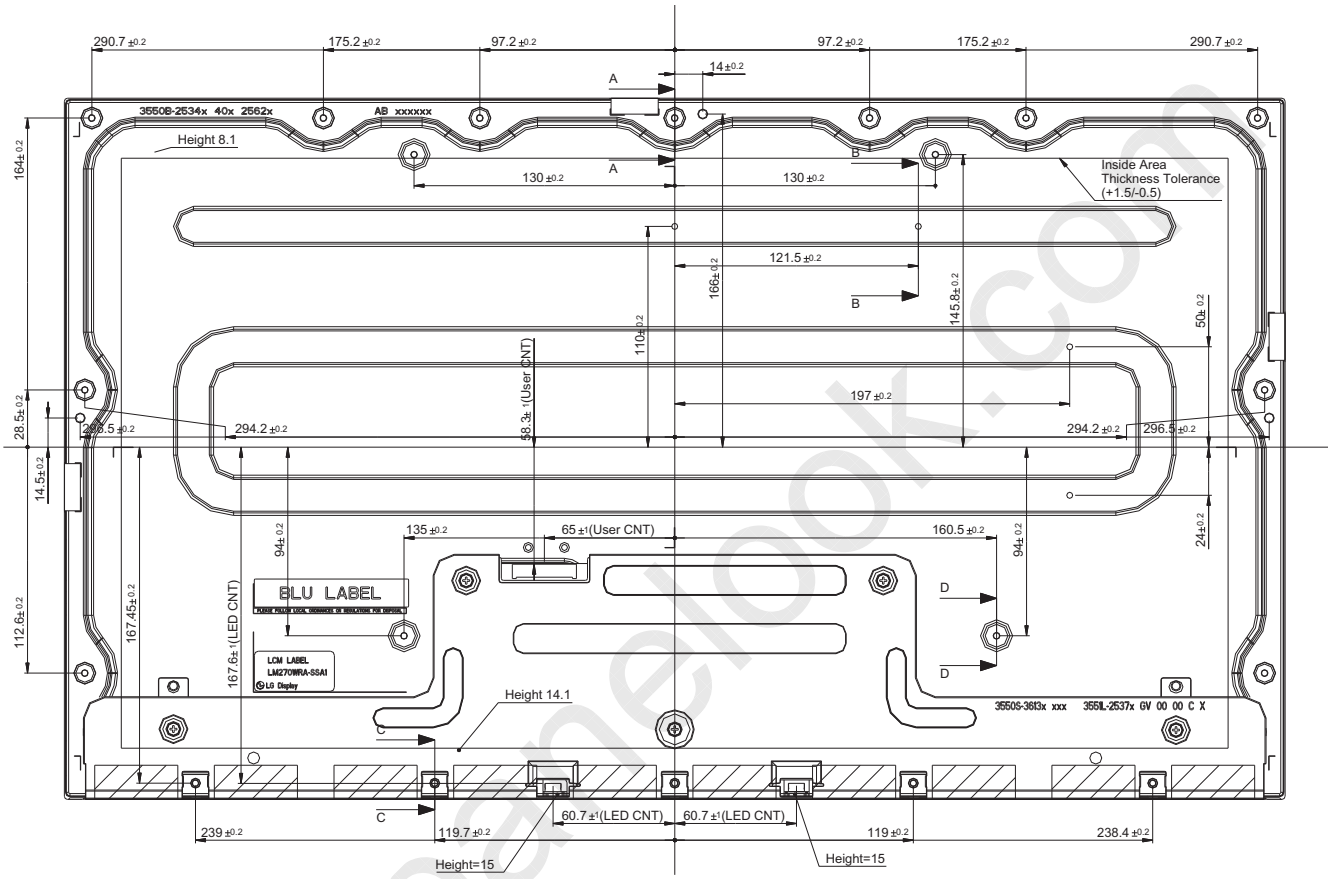
5-1. Cover Shield Protrusion Value definition

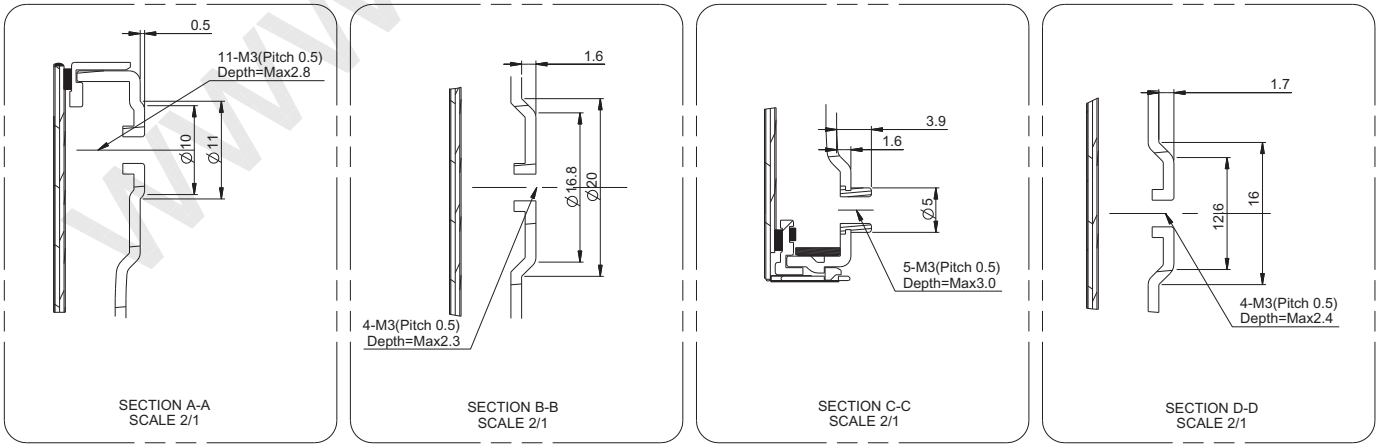
5-2. Cover Shield Protrusion Specifications : < 0.45



< Measurement Point: (9)~(11) >

<Rear View>





6. Reliability

Environment test condition

No	Test Item	Condition	Notes
1	High temperature storage test	T_a = 60°C, 240h	1
2	Low temperature storage test	T_a = -20°C, 240h	1
3	Humidity condition storage	T_a = 40°C, 90%RH	1
4	High temperature operation test	T_a = 50°C, 50%RH, 240h	1
5	Low temperature operation test	T_a = 0°C, 240h	1
6	Humidity condition operation	T_a = 40°C, 90%RH	1
7	Altitude Operating Storage / Shipment	 0 - 10,000 feet (3,048m) 0 - 40,000 feet (12,192m)	
8	Maximum storage humidity for 4 corner light leakage Mura	Max 70%RH, T_a = 40°C	
9	Power On/ Off test	On(5Sec.)/ Off (5Sec.), 30,000 Cycle	
10	Panel Push test	No panel crack under 5kgf	2
11	Vibration test (non-operating)	Waveform : Random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, 10min One time each direction	
12	Shock test (non-operating)	Shock level : 100G Waveform : Half sine wave, 2ms Direction : ±X, ±Y, ±Z One time each direction	
13	Thermal shock test	T_a = -20°C/30min~60°C/30min, 100cycle (Cooling time at least 4h)	1
14	ESD (Electro Static Discharge)	Contact Discharge : ± 8kV,150pF(330Ω), 1sec Air Discharge : ± 15kV, 150pF(330Ω), 1sec	3

Note 1) Result Evaluation Criteria:
TFT-LCD panels test should take place after cooling enough at room temperature.
In the standard condition, there should be no particular problems that may affect the display function.
Storage condition is guaranteed under packing condition

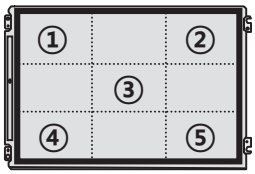
* T_a= Ambient Temperature
* Guarantee 30Khrs on static office circumstances (Room Temp. & Room Humidity) only for the Panel peel off at tilt (-35°~+80°) and pivot (-180°~+180°) usage.

6. Reliability

Note2) Measurement Criteria of Panel Push Test

① Environment : Room Condition, Non-Operating
② Test Criteria
1) Push Gauge : Contact Area Size - Φ 10mm
2) Push Holding Time : 5sec
3) Test Speed : 20mm/min
4) Range 1~10kgf
5) Test Point : Front 5points

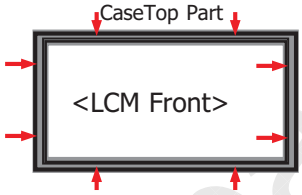
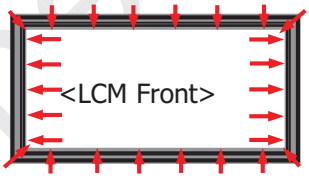
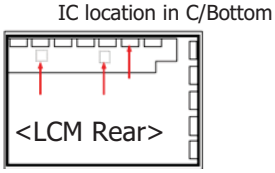
■ Front Point (5 Point)



Note 3) 1. ESD Class B
- Certain performance degradation allowed No data lost/ Self-recoverable/ No hardware failures.

2. Operation Test (Discharge location / Test time)
1) Contact (Top Case) : 5times for each point
- Top Case (LCM front view : Up/Down/Left/ Right 2points of Top Case)
2) Air (Panel) : 5times for each point
- LCM front view: Up/Down (6Points), Left/Right (5Points), Corner (4Points)
- Top case (Top case adjacent part), No Top case (LCM Up/Down/Left/ Right edge)

3. Non-operation Test (Discharge location / Test time):5times for each point
- C/Bottom : IC Location (ASIC, P-IC, OP Amp, Source & Gate IC)

	Operation		Non Operation
	Contact (Top Case)	Air (Panel)	Contact (C/Bottom)
Test Point			

7. International Standards

7-1. Safety

a) IEC 62368-1, The International Electro-technical Commission(IEC).
Audio/video, Information and Communication Technology Equipment - Safety - Safety Requirements.
b) EN 62368-1, European Committee for Electro-technical Standardization (CENELEC)
Audio/video, Information and Communication Technology Equipment - Safety Requirements
c) UL 62368-1, UL LLC.
Audio/video, Information and Communication Technology Equipment - Safety Requirements
d) CAN/CSA C22.2 No.62368-1, Canadian Standards Association (CSA).
Audio/video, Information and Communication Technology Equipment - Safety Requirements
e) IEC 60950-1, The International Electro technical Commission (IEC).
Information Technology Equipment - Safety - Part 1 : General Requirements

7-2. Environment

a) RoHS, Commission Delegated Directive (EU) 2015/863 of 31 March 2015 amending Annex II to Directive 2011/65/EU of the European Parliament and of the Council

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

A	B	C	D	E	F	G	H	I	J	K	L	M

A,B,C: Size(Inch)
E: Month
D: Year
F ~ M: Serial No.

Notes:

1) Year

Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Mark	K	L	M	N	P	R	S	T	U	V

2) Month

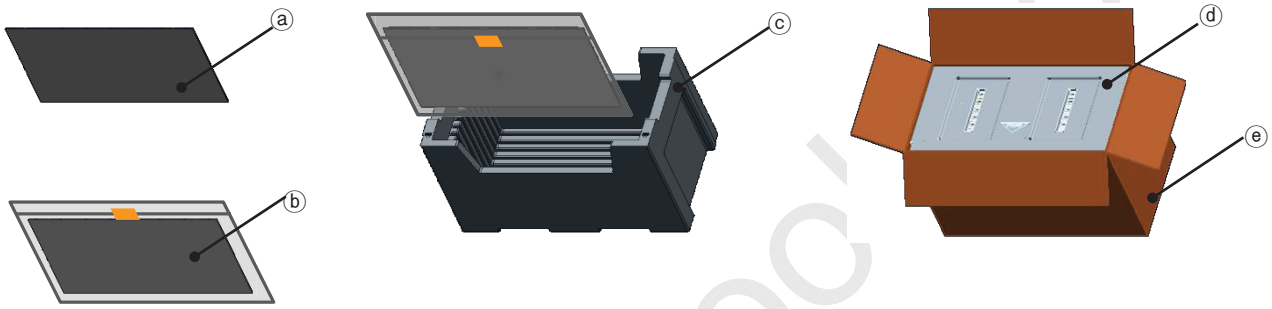
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	A	B	C

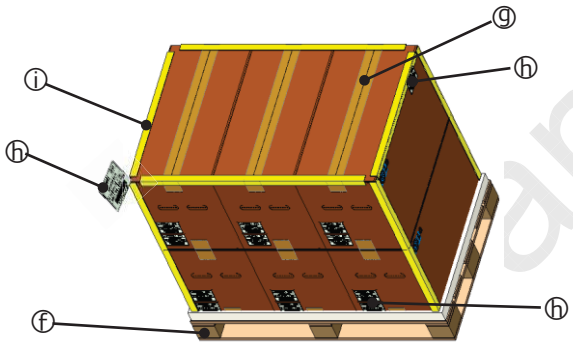
b) Location of Lot Mark
Serial No. is printed on the label. The label is attached to the backside of the LCD module.
This is subject to change without prior notice.

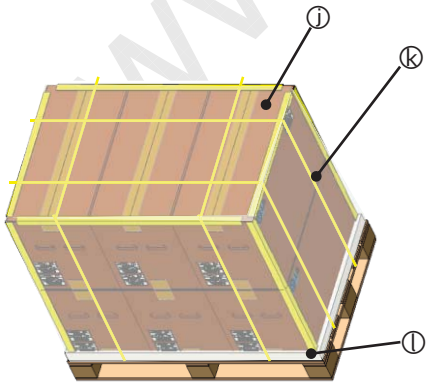
8-2. Packing Form

ITEM	Quantity	Dimension	Weight
Packing (BOX)	10ea	710(L)*365(W)*448(H)	27Kg
Pallet after Packing	60ea	1140(L)*740(W)*1069(H)	178kg
Box Per Pallet	6ea	-	-
Box stack layer in Pallet	3*1*1 Pattern	-	-

* LCM Direction(insert to Bottom Packing) : COF UP







No.	Description	Material
ⓐ	LCM	-
ⓑ	AL-Bag	AL
ⓒ	Packing,Bottom	EPS
ⓓ	Packing,Top	EPS
ⓔ	Box	Paper(SW)
ⓕ	Pallet	Plywood
ⓖ	Tape	OPP
ⓗ	LABEL	YUPO PAPER
ⓘ	Paper Angle	Paper
ⓙ	Wrap	-
ⓚ	BAND	PP
ⓛ	Cushion	PE

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

1) You must mount a module using holes arranged in rear side.
2) You should consider the mounting structure so that uneven force(ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
4) You should adopt radiation structure to satisfy the temperature specification.
5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.
(Some cosmetics are detrimental to the polarizer.)
7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
9) Do not open the case because inside circuits do not have sufficient strength.
10) System frame should not have an interference with panel which can cause LC Leakage/Panel Crack due to the contraction of system frame at low temperature condition or panel damage by any other circumstances.

9-2. Operating Precautions

1) Response time depends on the temperature.(In lower temperature, it becomes longer.)
2) Brightness depends on the temperature.(In higher temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
3) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
6) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
7) A screw which is fastened up the steels should be a machine screw.(if not, it causes metallic foreign material and deal LCM a fatal blow)
8) Please do not set LCD on its edge.
9) When LCMs are used for public display, defects such as Yogore & image sticking can not be guaranteed.
10) LCMs cannot support "Interlaced Scan Method"
11) When this reverse model is used as a forward-type model (PCB on top side) or a Portrait-type mode at storage and operation, LGD can not guarantee any defects of LCM.
12) Please conduct image sticking test after 2-hour aging with Rolling Pattern at normal temperature.(25~40°C)

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions For Strong Light and Hazardous Materials Exposure

Strong light exposure causes degradation of polarizer and color filter.
The LCM should be avoided direct contact with hazardous materials such as sulfur, acetic acid, chlorine, etc. These materials may cause chemical reaction such as sulfurization, corrosion, discoloration, etc.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
2) The LCM storage period is 6 months, which is the storage period under the packaging conditions provided.
3) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.

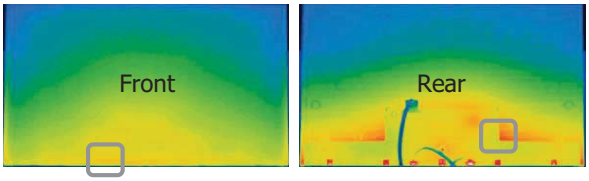
9-6. Handling Precautions For Protection Film

1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

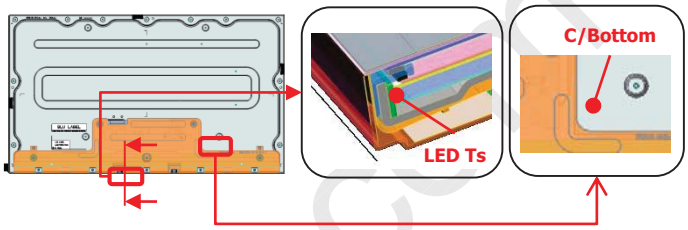
APPENDIX

■ LCM Test Result for Operating HDR Function

1) It is recommended that thermal sensor in system should be placed on the surface of C/Bottom near the LED with high temperature when measured by IR camera.



<Measured by IR Camera>



<Thermal Data Acquisition Position>

2) Measurement of temperature by time.
When the temperature is over 62.4°C on C/Bottom, defects are founded due to thermal effect.

Table 1: Temperature data at typical luminance(87mA), ambient temperature(50°C)

87mA (Typ.)	Measure Point Temp. (Ambi. 50°C)		
Time(min.)	C/Bottom(°C)	LED Ts(°C)	LED Ts - C/Bottom(°C)
30	61.0	67.4	6.4
60	62.1	68.5	6.4
90	62.3	68.7	6.4
120	62.4	68.9	6.5

Table 2: Temperature data at 700nit luminance(150mA), ambient temperature(25°C)

146mA	Measure Point Temp. (Ambi. 25°C)			Remark.	
Time(sec)	C/Bottom(°C)	LED Ts(°C)	LED Ts - C/Bottom(°C)	Interval	LED Current
Base	39.3	45.5	6.2	2hr	87mA(Typ.)
30	39.7	50.3	10.6	0.5min	146mA
60	40.1	51.4	11.3		
90	40.4	52.1	11.7		
120	40.8	52.6	11.8		
150	41.1	52.9	11.8		
180	41.4	53.3	11.9		
240	41.9	53.8	11.9	1min	
300	42.4	54.3	11.9		
360	42.7	54.7	12.0		
420	43.1	55.1	12.0		
480	43.3	55.4	12.1		
540	43.7	55.7	12.0		
600	43.9	56.0	12.1		
900	45.0	57.0	12.0	5min	
1200	45.4	57.7	12.3		
1500	46.0	58.2	12.2		
1800	46.3	58.6	12.3		

APPENDIX

■ LCM Test Result for Operating HDR Function

Table 3: Temperature data at peak luminance(158mA), ambient temperature(25°C)

158mA(Peak.)	Measure Point Temp. (Ambi. 25°C)			Remark.	
Time(sec)	C/Bottom(°C)	LED Ts(°C)	LED Ts - C/Bottom(°C)	Interval	LED Current
Base	39.3	45.5	6.2	2hr	87mA(Typ.)
30	39.7	51.0	11.3	0.5min	158mA (Peak.)
60	40.3	52.3	12		
90	40.7	53.2	12.5		
120	41.2	53.8	12.6		
150	41.4	54.2	12.8		
180	41.8	54.7	12.9		
240	42.4	55.4	13	1min	
300	42.9	55.8	12.9		
360	43.3	56.3	13		
420	43.7	56.8	13.1		
480	44.0	57.2	13.2		
540	44.4	57.5	13.1		
600	44.7	57.8	13.1		
900	45.7	59.0	13.3	5min	
1200	46.4	59.7	13.3		
1500	46.9	60.1	13.2		
1800	47.2	60.6	13.4		

LED Ts

C/Bottom

GENERAL SPECIFICATIONS

Circuit	Custom one-chip of microprocessor LSI circuit.
Channels	T1, T2, T3, T4, T1-T2.
Sensor type	Type K thermocouple probe. Type J/T/E/R/S thermocouple probe. PT 100 ohm probe * Cooperate with an 0.00385 alpha coefficient, meet DIN IEC 751.
Resolution	0.1℃/1℃, 0.1℉/1 ℉.

Type K

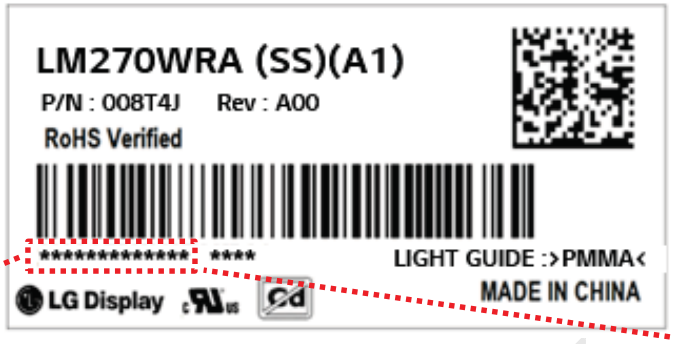
Sensor	Resolution	Range	Accuracy
Type K	0.1 ℃	-50.1 to -100.0 ℃	± (0.4 % + 1 ℃)
		-50.0 to 999.9 ℃	± (0.4 % + 0.5 ℃)
	1 ℃	1000 to 1300 ℃	± (0.4 % + 1 ℃)
	0.1 ℉	-58.1 to -148.0 ℉	± (0.4 % + 1.8 ℉)
		-58.0 to 999.9 ℉	± (0.4 % + 1 ℉)
	1 ℉	1000 to 2372 ℉	± (0.4 % + 2 ℉)

Temperature Meter (Lutron TM-947SD / 4ch)

Luminance(nit)	LED String Current(mA)	LED String Voltage(V) (Tolerance : ± 2.0V)
450	87	28.6
700	146	30.4
750	158	30.8

APPENDIX

■ ID Label of LCM



Digit	1			2	3	4	5						
Code	*	*	*	*	*	*	*	*	*	*	*	*	*

Digit	Description
1	Inch (8.9" = 089, 10.1" = 101, 21.5" = 215)
2	Year (2010=0, 2011=A, 2012=B)
3	Month (Jan.~Sep.:1~9, Oct.=A, Nov.=B, Dec.=C)
4	Module Factory
5	Serial No. (0000001~ZZZZZZZ)

■ Box Label

LM270WRA	SSA1
08T4J	ZB
10 PCS LOT/MM-DD	
MADE IN CHINA	RoHS Verified
********** ***	

■ Pallet Label

LM270WRA	SSA1
08T4J	ZB
60 PCS LOT/MM-DD	
MADE IN CHINA	RoHS Verified
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