Ravi Kant Sharma

Member - Technical Staff

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address #435, 9th Main, HAL II Stage, Bangalore-08, INDIA

EDUCATION

July, 2007 to May, 2010

Birla Institute of Technology, Ranchi

Master of Computer Application

EXPERIENCE

Oct, 2010 to June 2012

Anveshan Telecom

Member - Technical Staff

Freescale MSC8144, SmartCore 3400 series, SmartDSP OS

- Responsible for Design and Development of ITU-T Specifications for Digital Circuit Multiplication Equipment.
- Wrote various Application and Communion Stacks like forward error correction, HDLC, proprietary reliable UDP with packet retransmission, fragmentation and reassembly, TCP/IP Stack maintenance, robust communication protocol for In-Band Management over E1 line, TFTP for Software Upgrade, configuration save and restore, new SNMP v2c features.
- Wrote various Signaling protocols from ITU-T Q Series, Signaling System Region 1, SSR2, SS5 and Call Detail Record (CDR) Generation from SS7 ISUP.

May, 2010 to Oct, 2010

Aricent Technologies

Software Engineer - Development

x86 Intel, ThreadX

- Development of Data link layer (L2) and Network layer (L3) for G.hn next generation wired Home networking protocol.
- Responsible for Development of Medium Access
 Control (MAC) Sub layer, Medium Access Protocol
 (MAP) building and scheduling, Multicast Routing.
- Wrote gTerm, a Command Line Ethernet based Interface (CLI) to debug device (based on G.hn protocol) and Logging Framework.

PROFESSIONAL PROFILE

Embedded Systems
Programmer with strong
technical and analytical skills.
Looking for challenging
projects in embedded systems
where I can utilize my expertise
to develop next gen Network
and Embedded devices in a
highly talented, innovative and
vibrant environment.

SKILLS

Platforms

Freescale MSC8144, Intel x86

Operating Systems

Linux, MS Windows, ThreadX, SmartDSP OS, uC/OS-II

Programming Languages & Tools

C, C++, Assembly, Scheme, Tcl, GCC, GDB, GNU Binutils, Valgrind, lint, MATLAB, Git