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Removing system linearity requirements through reference cancelation in Data Converters

Thesis Planning

Introduction

I divided my ECTS credits roughly equally between the two semesters of my second master year, this is why I will be aiming to put roughly the same amount of time into my this during the first semester and second semester.

Time division and goals

20 % Literature Study

- Reading papers about ADCs to learn how the latest technology deals with non-linearity.

50 % System level simulation

- Creating a high level system and simulating it in matlab.

30 % circuit level simulation

- Realizing the system on circuit level and simulating it.

Estimation of amount of time planned in each month (total time should be 600-720 hours).

Month	Hours	Days	Hours per day
October	63	21	4
November	69	23	4
December	21	7	4
January	0	0	4
February	112	28	4
March	124	31	4
April	120	30	4
May	45	15	4
Total	620	155	

Notes:

1. Some weeks will be occupied by projects of other courses which I have included in the estimation.

2. The period before the exams and during the exams have also been left out of the estimation since I plan to focus on exams than although I might still work on my thesis if I have some time.

Proceedings:

Date	**activity**
20 October	Finish literature review
27 November	Interim presentation
7 December	Finished top level system design
28 February	Finished matlab simulations
15 May	Finished writing thesis text
4 June	Submission deadline

Conclusion

This document is an initial estimate of how the thesis will proceed throughout the year. With this I have clearly stated which goals I have to achieve by when which will help me keep progress.