

Ch5 PCB Design

2018 - 2019 2.(b)

- (b) The output of a driver is connected to the input of a receiver through two parallel signal lines, as shown in Figure 4 on page 4. The signal frequency is 50 MHz; and the differential-mode (DM) and common-mode (CM) signal currents are as indicated.
- Calculate the radiated electric field in $\text{dB}\mu\text{V/m}$ due to DM signal current at a distance 3 m from the circuit. (4 Marks)
 - Repeat part (i) for CM signal current. (4 Marks)
 - Based on calculated radiated electric fields in parts (i) and (ii), what conclusion can you draw? (2 Marks)

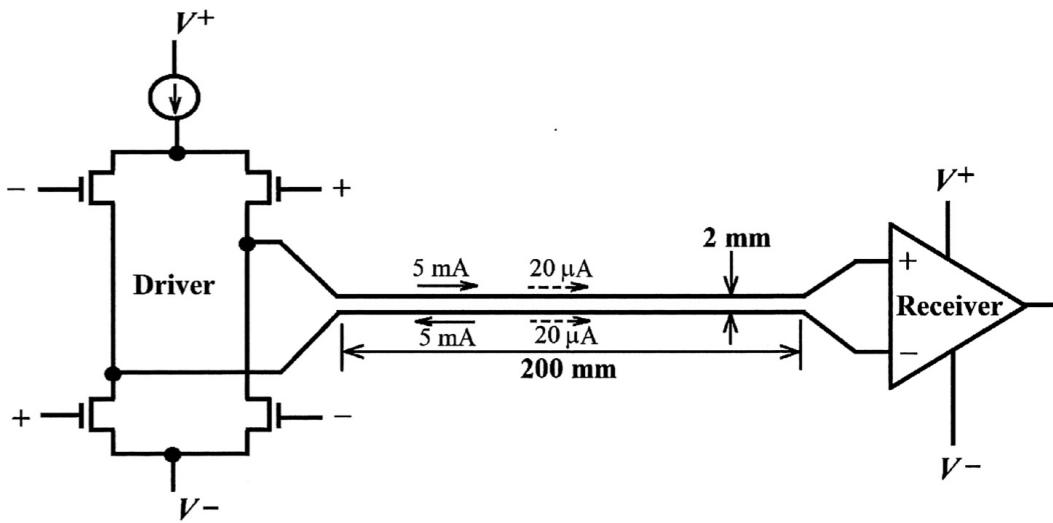


Figure 4

(i) From the figure we can get the loop area is : $A = 0.2 \text{ m} \times 0.002 \text{ m} = 4 \times 10^{-4} \text{ m}^2$
Hence the radiated electric field due to DM current is

$$E_{\text{DM}} = 2.632 \times 10^{-14} \left(\frac{f^2 A I_{\text{DM}}}{r} \right)$$

$$= 2.632 \times 10^{-14} \left(\frac{(50 \times 10^6)^2 \times 4 \times 10^{-4} \times 5 \times 10^{-3}}{3 \text{ m}} \right) = 4.387 \times 10^{-5} \text{ V/m} = 43.87 \mu\text{V/m}$$

$$= 32.84 \text{ dB}\mu\text{V/m}$$

(ii) $E_{\text{CM}} = 1.26 \times 10^{-6} \left(\frac{f I_{\text{CM}} l}{r} \right) = 1.26 \times 10^{-6} \left(\frac{50 \times 10^6 \times 20 \times 10^{-6} \times 20 \times 10^{-3}}{3} \right) = 84 \mu\text{V/m} = 38.49 \text{ dB}\mu\text{V/m}$

(iii) Even common mode current is much lower than DM current, but it produces comparable radiation due to the length of cable

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3. Figure 4 shows the cross-sectional view of a printed circuit board (PCB) where a clock driver (CD) is powered by a voltage regulator (VR) through the power and ground planes. The substrate between the power and ground planes has relative permittivity $\epsilon_r = 4.5$ and relative permeability $\mu_r = 1$.

- (a) The CD draws its supply current from the VR and the current waveform is shown in Figure 5. Plot the radiated emission spectrum up to 1 GHz from the power delivery path and compare it with the FCC Part 15 Class B limit specified in Table 2. Comment on whether the PCB can be sold in USA's market?

(10 Marks)

- (b) If the length and width of the PCB are 15 cm and 10 cm, respectively; what are the expected board resonant frequencies below 1 GHz?

(7 Marks)

- (c) What are the harmonics of the current waveform below 1 GHz that may exhibit significant radiated emission due to the board resonance?

(3 Marks)

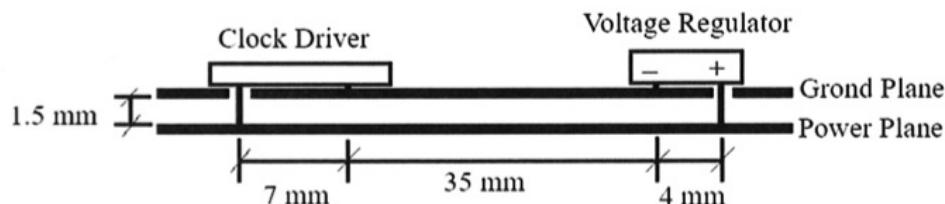


Figure 4

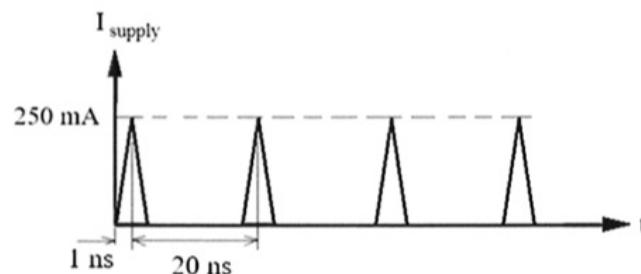


Figure 5

Table 2

Frequency (MHz)	Limit at 3 m (dB μ V/m)
30 – 88	40
88 – 216	43.5
216 – 960	46
>960	54

(a) The fundamental frequency f_1 is : $f_1 = \frac{1}{T} = \frac{1}{20 \times 10^{-9}} = 50 \text{ MHz}$

$$|I_n| = \frac{2I_{tr}}{T} \left[\frac{\sin\left(\frac{n\pi t_r}{T}\right)}{\frac{n\pi t_r}{T}} \right]^2$$

$$= \frac{2 \times 250 \times 10^3 \times 1}{20} \left[\frac{\sin(0.157n)}{0.157n} \right]^2$$

$$\Rightarrow I_1 = 24.795 \text{ mA}$$

The loop area is : $(7+35+4) \times 1.5 = 69 \times 10^{-6} \text{ m}^2$

$$E_{DM} (50 \text{ MHz} @ 3 \text{ m}) = 2.632 \times 10^{-14} \left(\frac{(50 \times 10^6) \times 69 \times 10^{-6} \times 24.795 \times 10^{-3}}{3} \right)$$

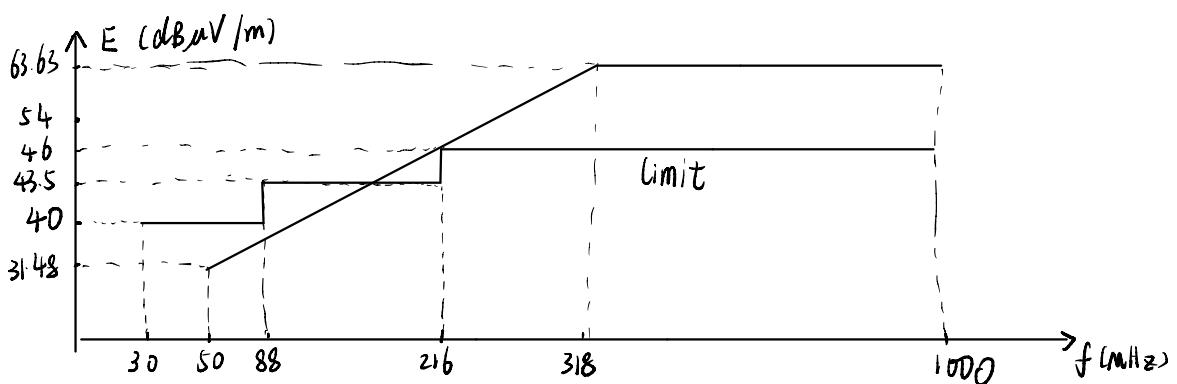
$$= 37.50 \mu\text{V/m}$$

$$= 31.48 \text{ dBuV/m}$$

The corner frequency : $f_c = \frac{1}{\pi t_r} = \frac{1}{10^{-9} \pi} = 318.31 \text{ MHz}$

$$\Delta E = 40 \log \frac{f_c}{f_1} = 32.15 \text{ dB}$$

$$E_{DM} (318.3 \text{ MHz} @ 3 \text{ m}) = 31.48 \text{ dBuV/m} + 32.15 \text{ dB} = 63.63 \text{ dBuV/m}$$



Hence the PCD can't be sold in USA's market. It has exceeded the limit and failed to comply with FCC Part 15 class B.

$$(b) f_{mn} = \frac{150 \sqrt{\left(\frac{m}{0.15}\right)^2 + \left(\frac{n}{0.1}\right)^2}}{\sqrt{\epsilon_r \mu_r}} \text{ MHz}$$

$$= \frac{150 \sqrt{\left(\frac{m}{0.15}\right)^2 + \left(\frac{n}{0.1}\right)^2}}{\sqrt{4.5}}$$

$$= 70.71 \sqrt{\left(\frac{m}{0.15}\right)^2 + \left(\frac{n}{0.1}\right)^2}$$

$m=0, n=1, 2, 3, 4, \dots$

$$f_{01} = 707.1 \text{ MHz}$$

$$f_{02} = 1.41 \text{ GHz} > 1 \text{ GHz}$$

$m=1, n=0, 1, 2, \dots$

$$f_{10} = 471.4 \text{ MHz}$$

$$f_{11} = 849.83 \text{ MHz}$$

$$f_{12} = 1.49 \text{ GHz} > 1 \text{ GHz}$$

$m=2, n=0, 1, 2, \dots$

$$f_{20} = 942.8 \text{ MHz}$$

$$f_{21} = 1.18 \text{ GHz} > 1 \text{ GHz}$$

$m=3, n=0, 1, 2, \dots$

$$f_{30} = 1.41 \text{ GHz} > 1 \text{ GHz}$$

Therefore, the expected board resonant frequencies are:

$$707 \text{ MHz}, 471.4 \text{ MHz}, 849.83 \text{ MHz}$$

(c) The harmonic frequency below 1 GHz :

$$f_1 = 50 \text{ MHz}$$

$$f_2 = 100 \text{ MHz}$$

$$f_3 = 150 \text{ MHz}$$

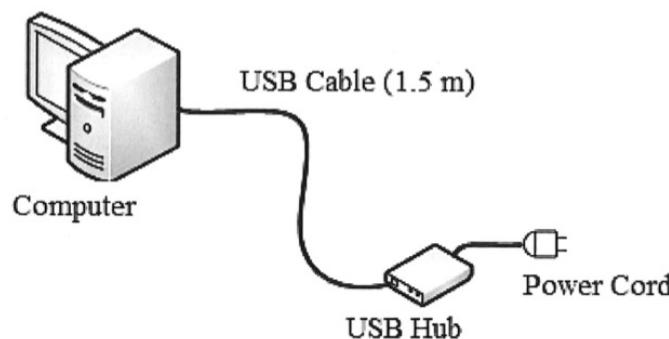
$$\vdots$$

$$f_{19} = 950 \text{ MHz}$$

Because of the board resonance, $f_{14} = 700 \text{ MHz}$, $f_9 = 450 \text{ MHz}$, $f_7 = 850 \text{ MHz}$ may exhibit significant radiated emission.

- (b) A computer is connected to a USB hub through a 1.5 m long USB cable, as shown in Figure 4. A radio frequency (RF) receiver with a tuning range of 30 MHz to 60 MHz, is located at 5 m away from the cable. It has been demonstrated that if any unwanted external field strength at the receiver's antenna is higher than $60 \text{ dB}\mu\text{V/m}$, the receiver will not be able to receive the wanted signal properly. In order to prevent possible interference from the USB cable's common mode radiation, what is the maximum allowable common-mode current on the USB cable (in dB μA) from 30 MHz to 60 MHz? It is assumed that the common-mode current on the USB cable is nearly constant along its length. The common mode radiation from the power cord can be ignored.

(10 Marks)

**Figure 4**

Since $E_{cm} \propto f$, when $f = 60 \text{ MHz}$, assuming that $E_{cm} = 60 \text{ dB}\mu\text{V/m}$
 $= 10^3 \mu\text{V/m}$

$$E_{cm} = 1.26 \times 10^{-6} \left(\frac{f I_{cm}}{r} \right) \text{ V/m}$$

$$= 1.26 \times 10^{-6} \times \frac{60 \times 10^6 I_{cm} \times 1.5 \text{ m}}{5 \text{ m}}$$

$$= 10^{-3} \text{ V/m}$$

$$\Rightarrow I_{cm} = \frac{E_{cm}}{1.26 \times 10^{-6}} \cdot \frac{r}{f l}$$

$$= \frac{10^{-3}}{1.26 \times 10^{-6}} \times \frac{5}{60 \times 10^6 \times 1.5}$$

$$= 44.09 \mu\text{A}$$

$$= 32.89 \text{ dB}\mu\text{A}$$

Therefore, the maximum allowable common-mode current on the USB cable is $32.89 \text{ dB}\mu\text{A}$.

3. Figure 2(a) shows the cross-sectional view of a printed circuit board (PCB) where a memory chip is connected to a voltage regulator through the power and ground planes. The substrate between the power and ground planes has a relative permittivity $\epsilon_r = 4.2$ and relative permeability $\mu_r = 1$.

- (a) When the memory chip is in operation, it draws the current from the voltage regulator and the current waveform is shown in Figure 2(b). Will the PCB comply with FCC Part 15 Class B limit given in Table 3 on page 4? Justify your answer by plotting the radiated emission spectrum up to 1 GHz against the limit line.

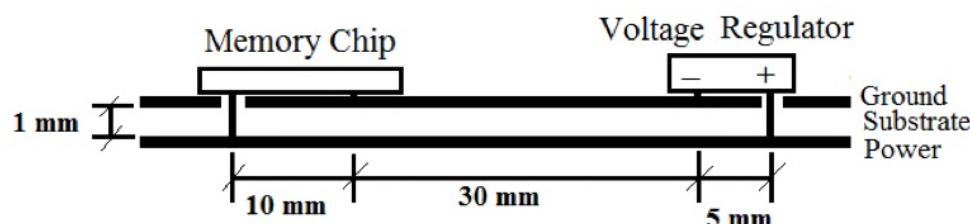
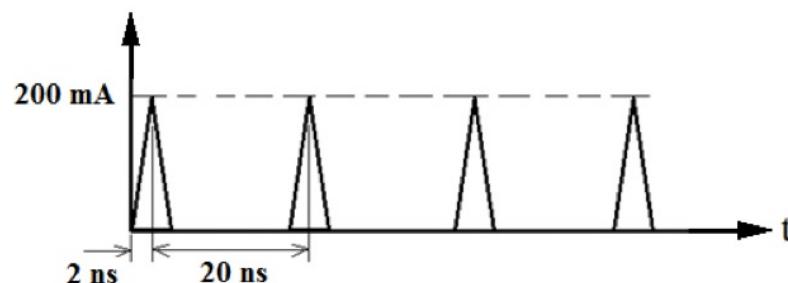
(10 Marks)

- (b) If the length and width of the PCB are 20 cm and 10 cm, respectively; what are the expected board resonant frequencies below 1 GHz for the PCB?

(7 Marks)

- (c) Which harmonics of the current waveform shown in Figure 2(b) below 1 GHz are expected to exhibit significant emission level due to the board resonance?

(3 Marks)

**Figure 2(a)****Figure 2(b)****Table 3**

Frequency (MHz)	Limit at 3 m (dB μ V/m)
30 - 88	40
88 - 216	43.5
216- 960	46
>960	54

(a) From figure 2(a), we know that the loop area is $A = (10+30+5) \times 1 = 45 \times 10^{-6} \text{ m}^2$

The fundamental frequency $f_1 = \frac{1}{T} = \frac{1}{20 \times 10^{-9}} = 50 \text{ MHz}$

$$|I_{nl}| = \frac{2I_{tr}}{T} \left[\frac{\sin(\frac{n\pi t}{T})}{\frac{n\pi t}{T}} \right]^2 A$$

$$\Rightarrow |I_1| = \frac{2 \times 20 \times 10^3 \times 2}{20} \left[\frac{\sin(\frac{2\pi}{20} \times 2)}{\frac{2\pi}{20} \times 2} \right]^2 = 38.70 \text{ mA}$$

$$E_{dm} (50 \text{ MHz } @ 3 \text{ m}) = 2.632 \times 10^{-4} \left(\frac{(50 \times 10)^2 \times 45 \times 10^{-6} \times 38.7 \times 10^3}{3} \right)$$

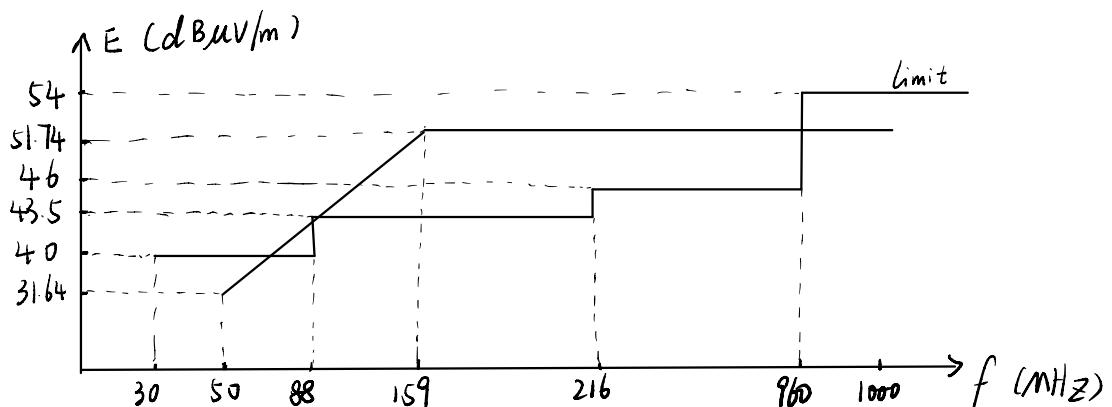
$$= 38.20 \mu\text{V/m}$$

$$= 31.64 \text{ dB}\mu\text{V/m}$$

The corner frequency $f_c = \frac{1}{\pi t_r} = \frac{1}{\pi \times 2 \times 10^{-9}} = 159 \text{ MHz}$

$$\Delta E = 40 \log \frac{f_c}{f_1} = 20.10 \text{ dB}$$

$$E_{dm} (159 \text{ MHz } @ 3 \text{ m}) = 31.64 \text{ dB}\mu\text{V/m} + 20.10 \text{ dB} = 51.74 \text{ dB}\mu\text{V/m}$$



It's shown clearly above that this PCB failed to comply with FCC Part 15 class B.

(b) The board resonance frequency is

$$f_{mn} = \frac{150 \sqrt{\left(\frac{m}{0.2}\right)^2 + \left(\frac{n}{0.1}\right)^2}}{\sqrt{\mu_r \epsilon_r}} \text{ MHz}$$

for this PCB $f_{mn} = \frac{150}{\sqrt{1 \times 4.2}} \sqrt{\left(\frac{m}{0.2}\right)^2 + \left(\frac{n}{0.1}\right)^2} \text{ MHz}$
 $= 73.19 \sqrt{\left(\frac{m}{0.2}\right)^2 + \left(\frac{n}{0.1}\right)^2} \text{ MHz}$

$m=0, n=1, 2, 3 \dots$

$$f_{01} = 731.9 \text{ MHz}$$

$$f_{02} = 1.46 \text{ GHz} > 1 \text{ GHz}$$

$m=1, n=0, 1, 2 \dots$

$$f_{10} = 365.95 \text{ MHz}$$

$$f_{11} = 818.29 \text{ MHz}$$

$$f_{12} = 1.51 \text{ GHz} > 1 \text{ GHz}$$

$m=2, n=0, 1, 2 \dots$

$$f_{20} = 731.9 \text{ MHz}$$

$$f_{21} = 1.04 \text{ GHz} > 1 \text{ GHz}$$

$m=3, n=0, 1, 2 \dots$

$$f_{30} = 1.10 \text{ GHz} > 1 \text{ GHz}$$

Therefore, the expected board resonant frequencies below 1 GHz are:

365.95 MHz, 731.9 MHz, 818.29 MHz

(c) Harmonics of the current wave below 1 GHz are:

$$f_1 = 50 \text{ MHz}$$

$$f_2 = 100 \text{ MHz}$$

:

$$f_{19} = 950 \text{ MHz}$$

The 7th (350 MHz), 15th (750 MHz) and 16th (800 MHz) harmonics are expected to exhibit significant emission level due to the board resonance.

