

Q1 (a) SDT for types and declaration:

interpretation

$\text{int } x = 1 \{ x.val = 1 \}$

Type checking

$\text{int } x = 1 \{ \text{if } x.val \in 1 \in \text{int}$
 $\text{assign } x.val = 1$
 else
 error
 $\}$

(b) SDT for type checking -

interpretation

$E \rightarrow E_1 + E_2 \{ E.val = E_1.val + E_2.val \}$

Type checking

$E \rightarrow E_1 + E_2 \{ \text{if } E_1.type \in E_2.type \in \text{int}$
 $E.type = \text{int};$
 else
 $E.type = \text{float};$
 $\}$

(c) SDT for switch case:-

interpretation

$\text{switch } (s)$

$\{ \text{case } (1): s = 10;$
 $\text{case } (2): s = 20;$
 $\text{default}: s = 30;$
 $\}$

$\{ \text{if } (s.val) \text{ switch}$
 $\text{case } 1: s.val = 10;$
 $\text{case } 2: s.val = 20;$
 $\text{default}: s.val = 30;$
 $\}$

Type checking
switch (s)

```
{ case 1: s.val = 10;
  case 2: s = 20;
  default: s = 30;
}
```

```
{ if s.val = 1
  s.val = 10;
  elif s.val = 2
    s.val = 20;
  else
    s.val = 30;
}
```

q.2: a) $P \rightarrow S$

b) $S \rightarrow \text{switch stmt } S \mid " := " \mid \epsilon$

c) $\text{switch stmt} \rightarrow \text{switch}(\text{id}) \{ \text{case blocks} \}$

d) $\text{case block} \rightarrow \text{case : stmt2} \mid \text{case blocks} \mid \text{default: 2}$

e) $\text{switch stmt2} \rightarrow " := " \mid \text{break}$

f) $" := " \rightarrow x = E$

g) $E: \text{Rel Ex} \mid \text{Add Ex} \mid \text{Id}$

a) $P.\text{temp} \rightarrow \text{new temp}()$

$P.\text{code} \rightarrow S.\text{code} \parallel P.\text{temp}$

b) $S.\text{temp} \rightarrow \text{new temp}()$

$S.\text{code} \rightarrow S.\text{temp} \parallel " := " \parallel \epsilon$

c) if $\text{id}.\text{code} = \text{case}$

then execute case block

d) $\text{case block}.\text{code} \Rightarrow \text{stmt2}.\text{code}$

$\text{case block}.\text{val} \Rightarrow \text{case id} \parallel \text{case blocks} \parallel \text{default}$

e) $\text{stmt2}.\text{val} \Rightarrow " := " \parallel \text{break}$

f) $" := " \Rightarrow x.\text{val} = E.\text{val}$

g) $E \Rightarrow "<" \parallel ">" \parallel ">" \parallel "+" \parallel \text{id}.\text{code}$

h) $E \Rightarrow "<" \parallel ">" \parallel ">" \parallel "+" \parallel \text{id}.\text{code}$

Q.3. cgen[cond $p1 \Rightarrow c1$; $p2 \Rightarrow c2$;] — $p1 \Rightarrow en$, done =
 cgen($p1$) // now acc($\$a0$) has a pointer to value of $p1$
 lw $\$a0$ 12($\$a0$) // read the value attr of the bool.
 beq $\$a0$ 0 PRE02 // go to second predicate value is false.
 cgen($c1$) // now acc($\$a0$) has a pointer to value of $c1$
 b DONE // evaluation of condition is complete

Label PRE02:

cgen($p2$) // now acc($\$a0$) has a pointer to value of $p2$
 lw($\$a0$) 12($\$a0$) // read the value attr. of the bool.
 beq $\$a0$ 0 VOID // value is void if all predicates are false
 cgen($c2$) // now acc($\$a0$) has a pointer to value of $c2$
 b DONE // evaluation of cond. is complete

Label VOID:

li $\$a0$ 0 // put void into the accumulator

Label DONE

Q.4: SBT for computing types & their width

$T \rightarrow B, C$ { B type; $w = B.height$ }

$B \rightarrow int$ { $B.type = int$; $B.height = 4$ }

$B \rightarrow float$ { $B.type = float$; $B.height = 8$; }

$C \rightarrow E$ { $C.type = t$; $C.height = w$; }

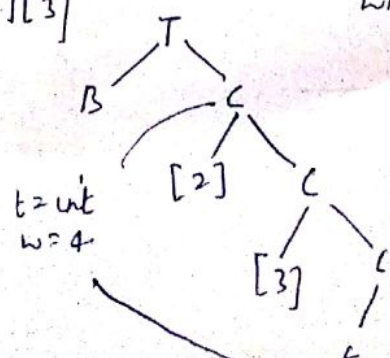
$C \rightarrow [num]C_1$ { array($num, value$, $C_1.type$); }

$C.width = num.value * C_1.height$; }

type = array(2, array(3, int))

width = 24

dependency graph: [2][3]



type = array(2, array(3, int))

width = 24

type = array(3, int)

width = 12

type = int

width = 4

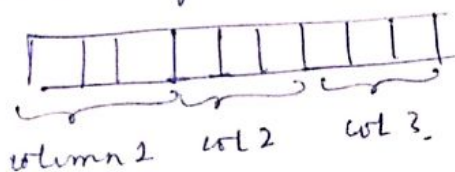
$\Rightarrow P \rightarrow D \{ \text{offset} = 0; \}$

$D \rightarrow T.id; \{ T.op.put(id.lexeme, T.type, offset);$
 $\text{offset} = \text{offset} + T.width; \}$

$D \rightarrow D_1 / E$

\Rightarrow The semantic action $D + T.id; D$ creates a symbol table entry by executing $t.op.put(id.lexeme, T.type, offset)$ here $t.op$ denotes the current symbol table.

Array addressing:-



\Rightarrow Location for 2D array $\rightarrow \underbrace{i \times \text{width}}_{\text{completed at run-time}} + \underbrace{(\text{base}_A + \text{low}_1 \times \text{width})}_{\text{can be done at compile time}}$

\Rightarrow The location of $A[i_1, i_2]$ is

$$\text{base}_A + ((i_1 \times \text{low}_1) \times n_2 + i_2 - \text{low}_2) \times \text{width}.$$

Where

$\text{base}_A =$ location of array

$\text{low}_1 =$ index of 1st col.

$\text{low}_2 =$ " " 1st row.

$n_2 =$ no. of rows.

$\text{width} =$ width of each array element can be written as

$$\underbrace{((i_1 \times n_2) + i_2) \times \text{width}}_{\text{run-time}} + \underbrace{\text{base}_A - ((\text{low}_1 \times n_2) + \text{low}_2) \times \text{width}}_{\text{compile time}}$$

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