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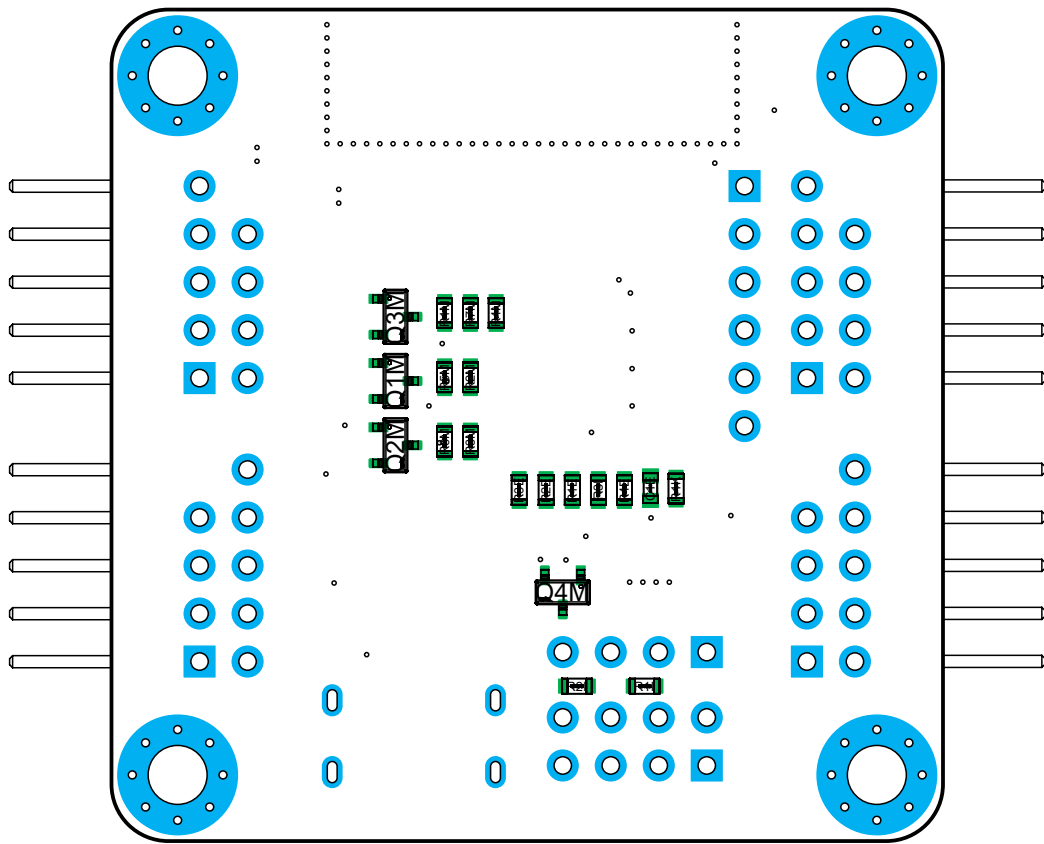
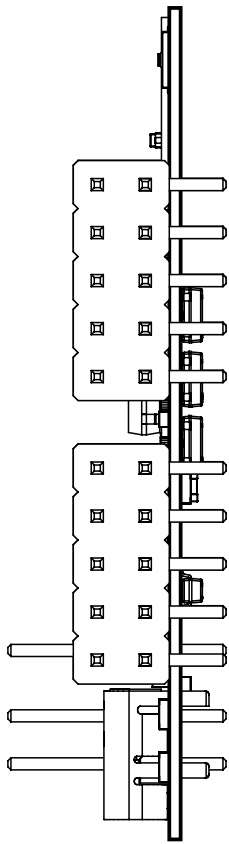
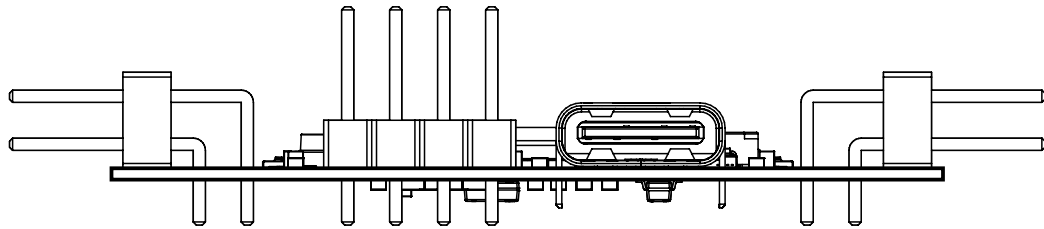
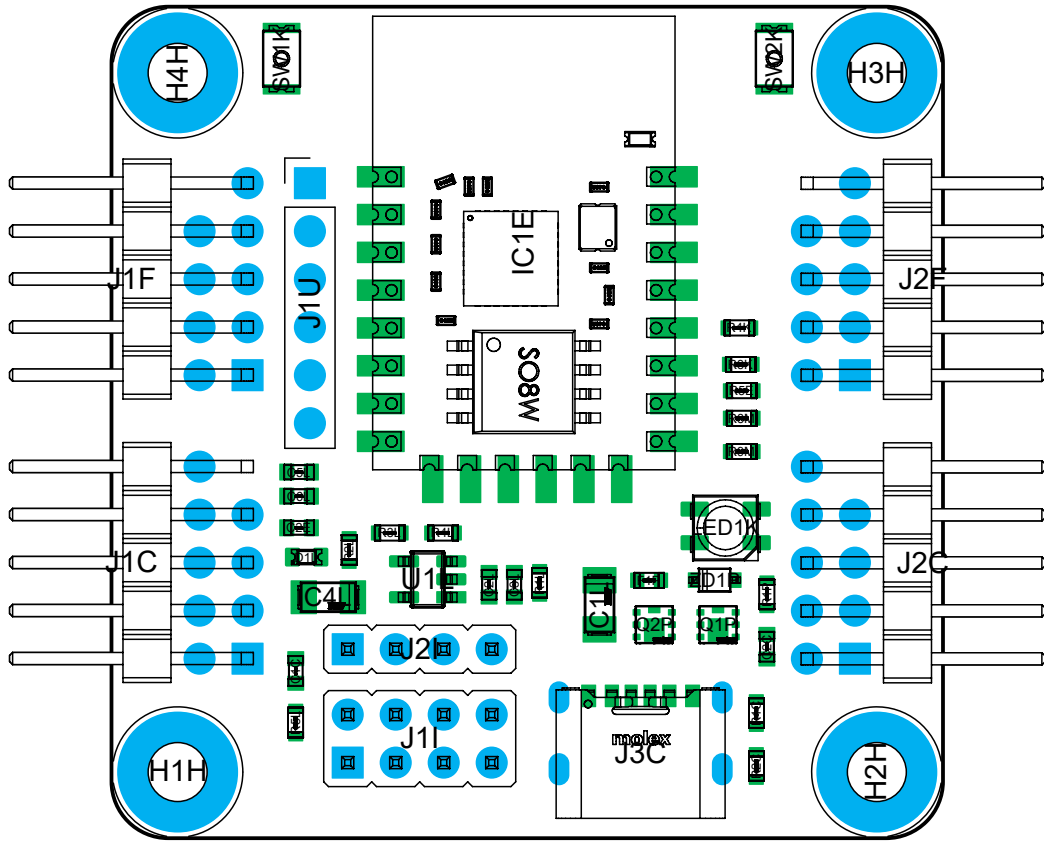
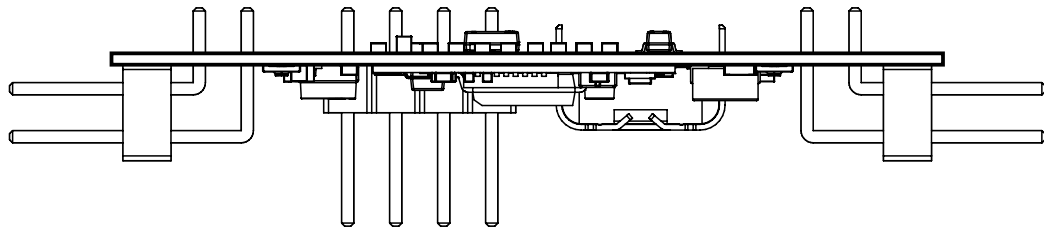
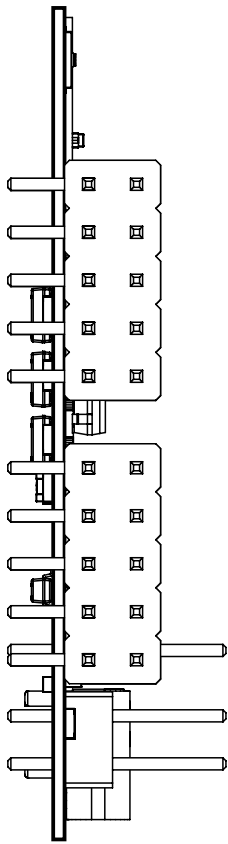
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- Note:
- 1 Text element with square border.
 - 2 Text element with no border
 - 3 Text element with circle border

THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF . ANY REPRODUCTION IN PART OR AS A WHOLE WITHOUT THE WRITTEN PERMISSION OF IS PROPRIETARY AND CONFIDENTIAL

		UNLESS OTHERWISE SPECIFIED:		NAME	DATE			
		DIMENSIONS ARE IN INCHES	DRAWN		26-12-2024	TITLE		
		TOLERANCES:	CHECKED					
		FRACTIONAL ±	ENG APPR.					
		ANGULAR: MACH ± BEND ±	MFG APPR.					
		TWO PLACE DECIMAL ±	Q.A.			COMMENTS:		
		THREE PLACE DECIMAL ±						
		INTERPRET GEOMETRIC TOLERANCING PER:				SIZE	DWG. NO.	
		MATERIAL						
NEXT ASSY	USED ON	FINISH				SCALE:	1:1	WEIGHT:
APPLICATION		DO NOT SCALE DRAWING						SHEET 1 OF 1

PCB MANUFACTURING SPECIFICATIONS

"=ProjectTitle"

SPECIFICATIONS

NOTE #	NOTE
1	ALL SPECIFICATIONS REFERENCED ARE OF THE REVISION SPECIFIED IN THE TITLE BLOCK
2	SUPPLIER SHALL NOT MODIFY THE DESIGN OR APPROVED STACK-UP WITHOUT WRITTEN PERMISSION
3	ALL MATERIALS SHALL BE RoHS COMPLIANT AND FINAL PRODUCT SHALL BE ACCEPTABLE TO USE IN RoHS ASSEMBLY. RoHS LOGO SHALL BE MARKED IN SILKSCREEN INK BY THE SUPPLIER WHERE INDICATED BY THE TEXT "PLACE MARKINGS HERE"
4	COPPER FOIL: REFER TO LAYER STACK LEGEND FOR Cu THICKNESS DETAILS. ALL Cu THICKNESSES ARE FINISHED AND INCLUDE BASE FOIL PLUS Cu PLATING ON PLATED LAYERS
5	ELECTRICAL TEST: ALL PRINTED CIRCUITS SHALL BE 100% ELECTRICALLY TESTED FOR OPENS/SHORTS USING PROVIDED NETLIST. REJECTED PRINTED BOARDS MUST BE CLEARLY MARKED WITH NON-CONDUCTIVE, PERMANENT INK.
6	MARKINGS: VENDOR MARKING AND DATE/LOT CODES SHALL BE LOCATED ON THE BOARD IN THE RESERVED AREA AS SPECIFIED IN THE GERBER LAYER "PCBM_NOTES" BY THE TEXT "PLACE MARKINGS HERE".
7	MARKINGS: THE SIDE ONTO WHICH PLACE THE MARKINGS IS AT THE SUPPLIER DISCRESSION UNLESS OTHERWISE NOTED ONTO THE LAYER "PCBM_NOTES"
8	SUPPLIER SHALL CHECK PCBM_NOTES LAYER BEFORE ASKING FOR CLARIFICATIONS
9	MANUFACTURE TENTED/PLUGGED VIAS AS SPECIFIED IN THE GERBER FILES

SPECIFICATIONS

LENGHT	44.00mm
WIDTH	44.00mm
LAYERS	2
MATERIAL	FR-4
MATERIAL MIN TG	130-140
TRACK WIDTH/CLEARANCE	10 mils / 10 mils
THICKNESS	0.6mm
COPPER THICKNESS	35um (1oz)
SOLDERMASK	YES, TOP AND BOTTOM
SOLDERMASK COLOR	GREEN
SILKSCREEN	YES, TOP AND BOTTOM
SILKSCREEN COLOR	WHITE
SURFACE FINISH	HASL LEAD FREE
GOLD FINGERS	NO
CHAMFERING	YES
IMPEDANCE CONTROL	YES
HALF-CUT/CASTELLATED HOLES	NO
BURIED/BLIND VIAS	NO
VIAS FILLED WITH RESIN	NO
CARBON MASK	NO
COUNTERSINKS/COUNTERBORES	NO
Z-AXIS MILLING	NO
PEELABLE SOLDERMASK	NO

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
Surface Material	Top Solder	0.02mm	Solder Resist	Solder Mask	GTS
Copper	Top Layer	0.04mm		Signal	GTL
Core		0.50mm	FR-4	Dielectric	
Copper	Bottom Layer	0.04mm		Signal	GBL
Surface Material	Bottom Solder	0.02mm	Solder Resist	Solder Mask	GBS
	Bottom Overlay			Legend	GBO

Total thickness: 0.60mm

NON-COPPER LAYER THICKNESS FOR REFERENCE ONLY
LAYERS OF TYPE "INTERNAL PLANE" ARE NEGATIVE

Title: =ProjectTitle		Author:	CONFIDENTIAL My Company Address Line 1 Address Line 2 Address Line 3 Address Line 4	[YOUR LOGO HERE]	6
		Approved:			
Size: A3	Prj: =ProjectTitle	Edited: 26-12-2024			
Unit: mm		Variant: [No Variations]			
Date: 26-12-2024 06:28		SW version: 25.1.2.22			
Git Hash: 27 [No modification]					
File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\ESP8266-Desktop Power\PCB MANUFACTURING					

[YOUR LOGO HERE]

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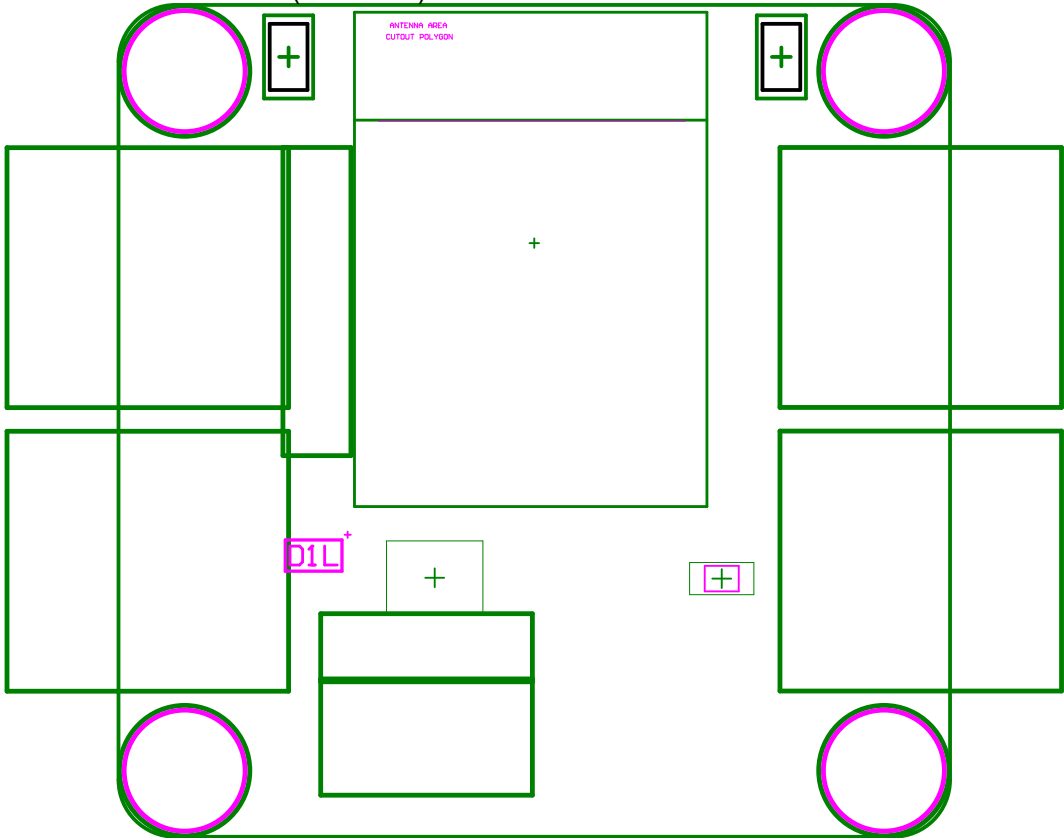
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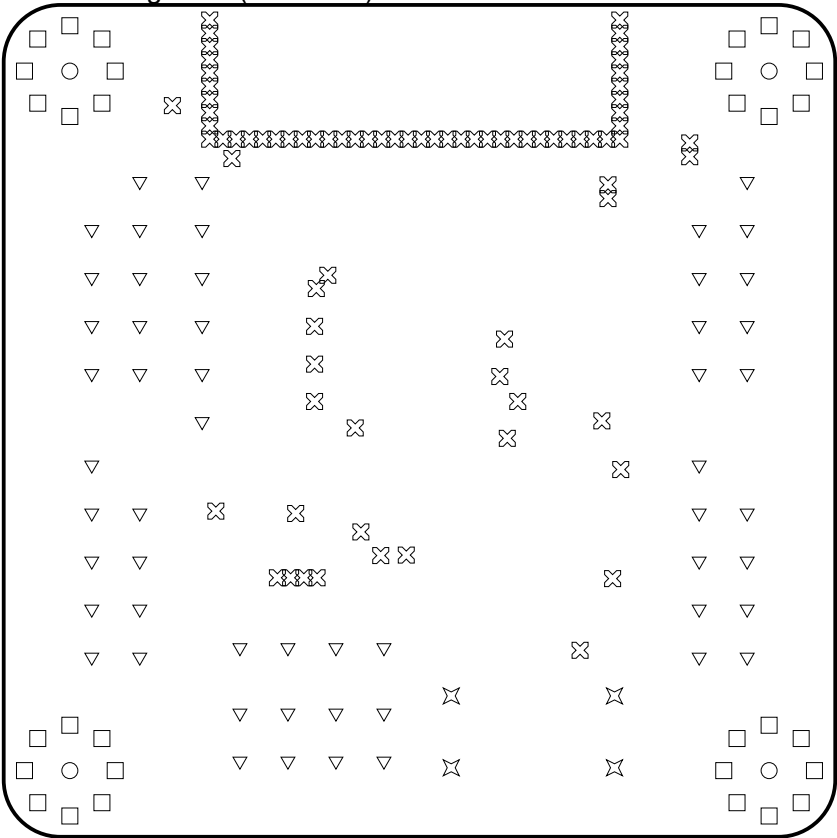
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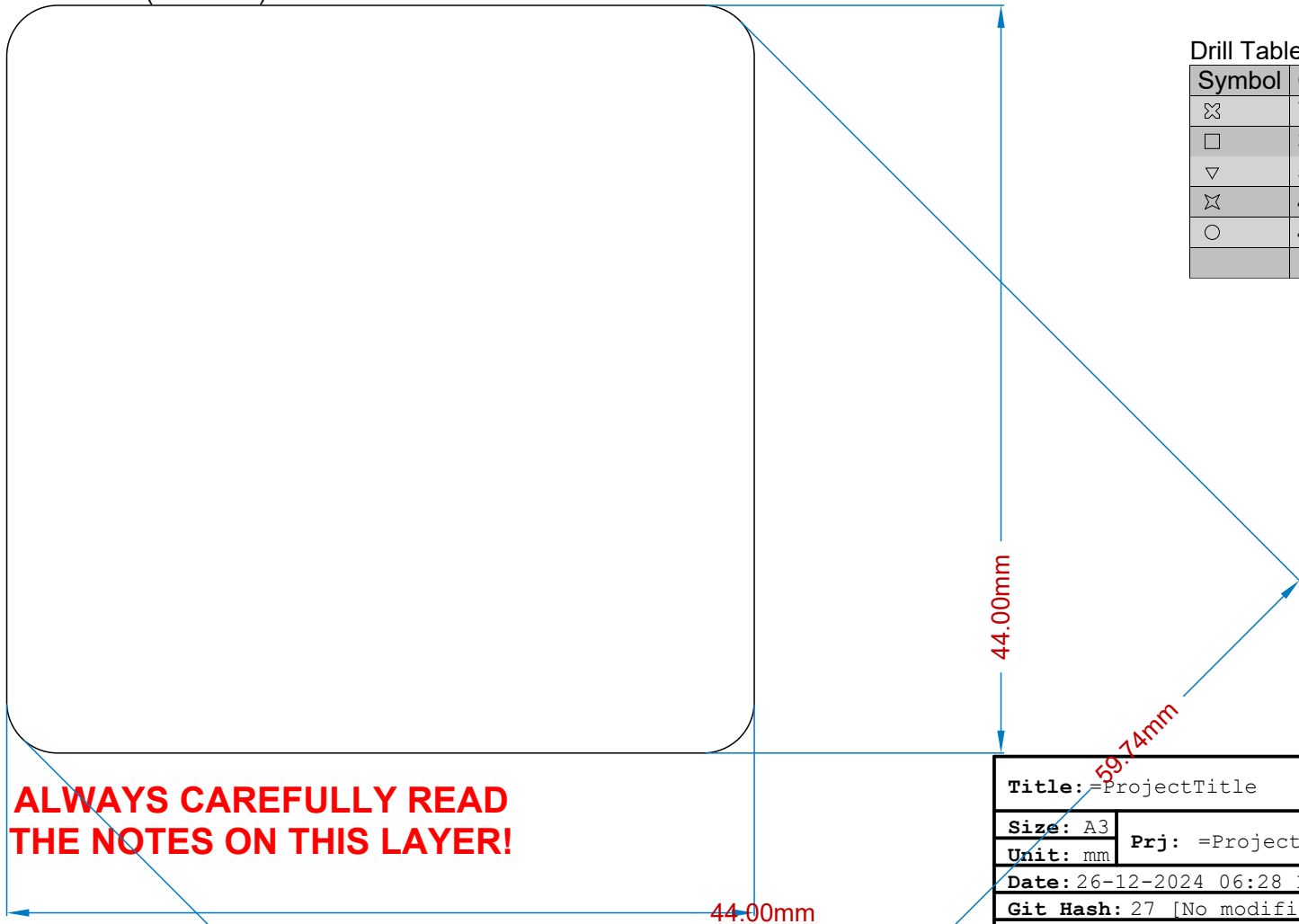
Mechanical 1 (Scale 5:2)



Drill Drawing View (Scale 5:2)



Dimensions (Scale 5:2)

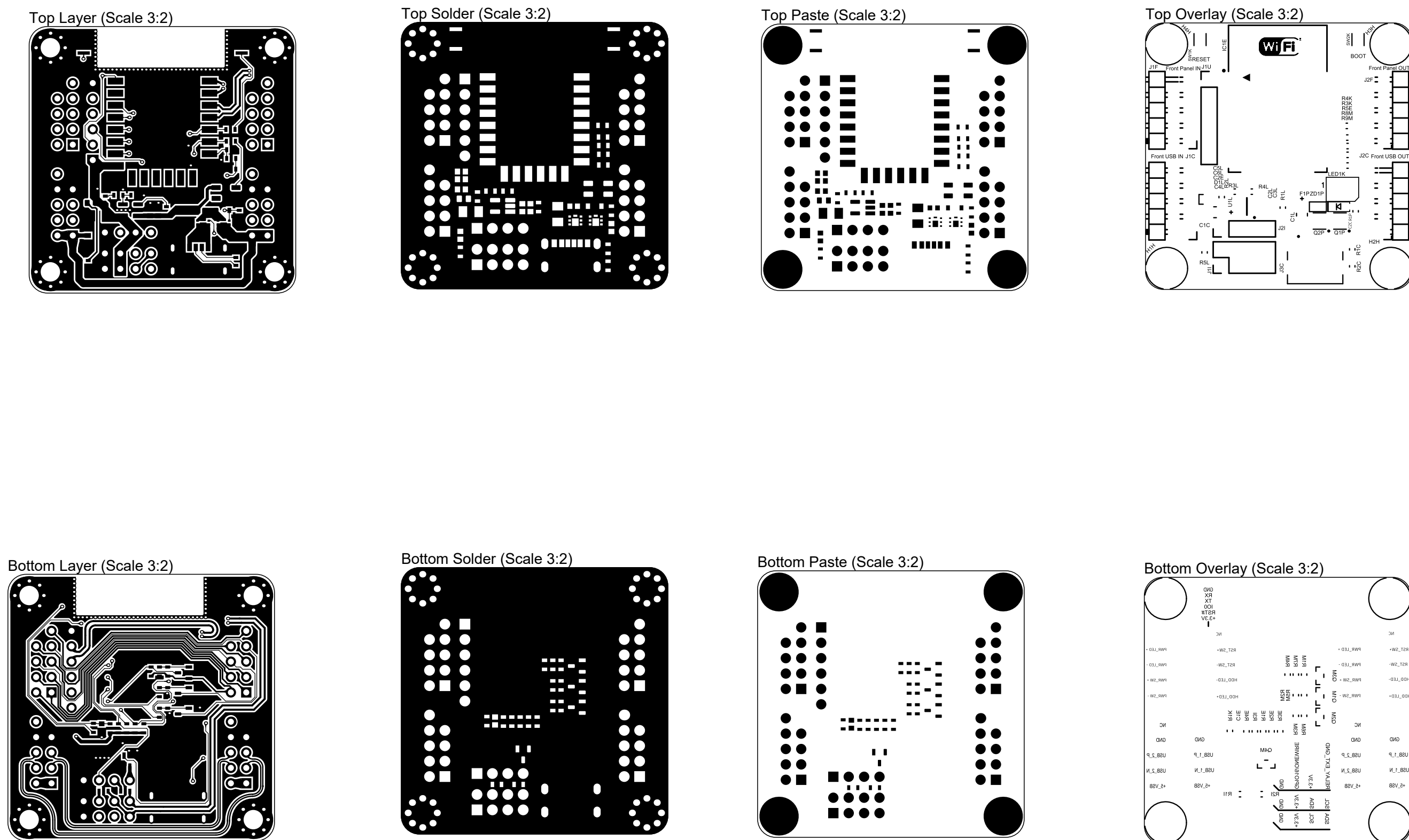


Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
⊗	79	0.30mm	Есть	
□	32	0.50mm	Есть	
▽	54	1.00mm	Есть	
⊠	4	1.20mm	Есть	
○	4	3.20mm	Есть	
	173 Total			

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Size: A3	Prj: =ProjectTitle	Approved:			
Unit: mm		Edited: 26-12-2024			
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[YOUR LOGO HERE]



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Unit: mm			Prj: =ProjectTitle		Edited: 26-12-2024				Address Line 1
					Variant: [No Variations]				Address Line 2
Date: 26-12-2024 06:28			MSheet 3 of 3		SW version: 25.1.2.22				Address Line 3
Git Hash: 27 [No modification]									Address Line 4
File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\ESP8266-Desktop Power\PCB MANUFACTURING									

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PCB ASSEMBLY SPECIFICATIONS

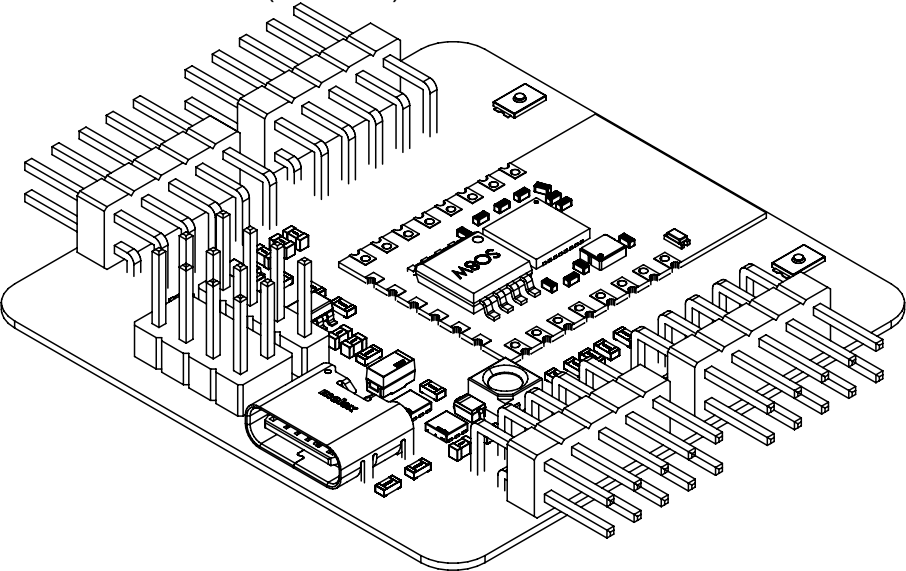
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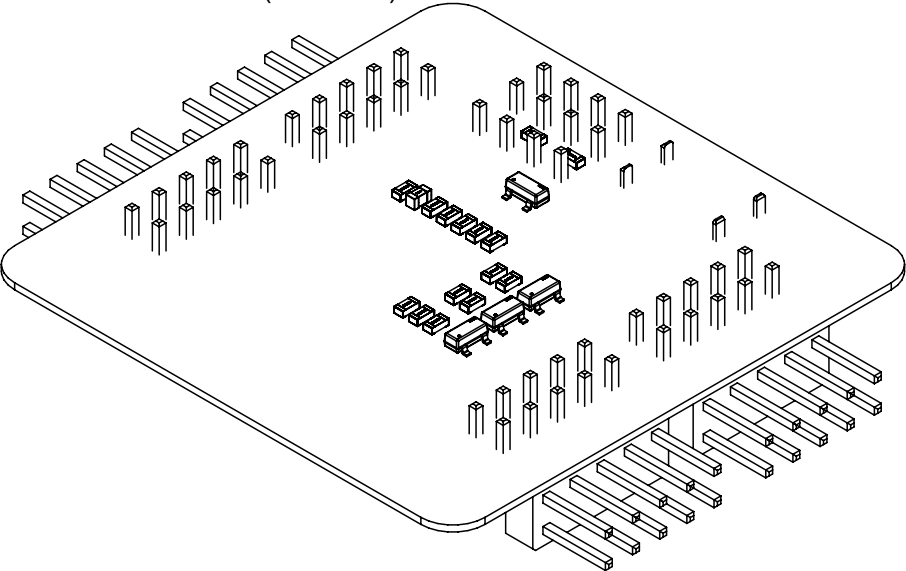
SPECIFICATIONS

NOTE #	NOTE
1	ALL SPECIFICATIONS REFERENCED SHALL BE OF THE LATEST REVISION UNLESS OTHERWISE NOTED
2	SUPPLIER SHALL NOT MODIFY THE DESIGN WITHOUT WRITTEN PERMISSION
3	REFER TO EXCEL BOM FOR UP-TO-DATE INFORMATION
4	THE BOM IN THIS DOCUMENT IS PURELY AN AID TO ASSEMBLY OPERATIONS AND MAY NOT HAVE THE MOST UP-TO-DATE DATA OR ALL APPROVED COMPONENT ALTERNATIVE.

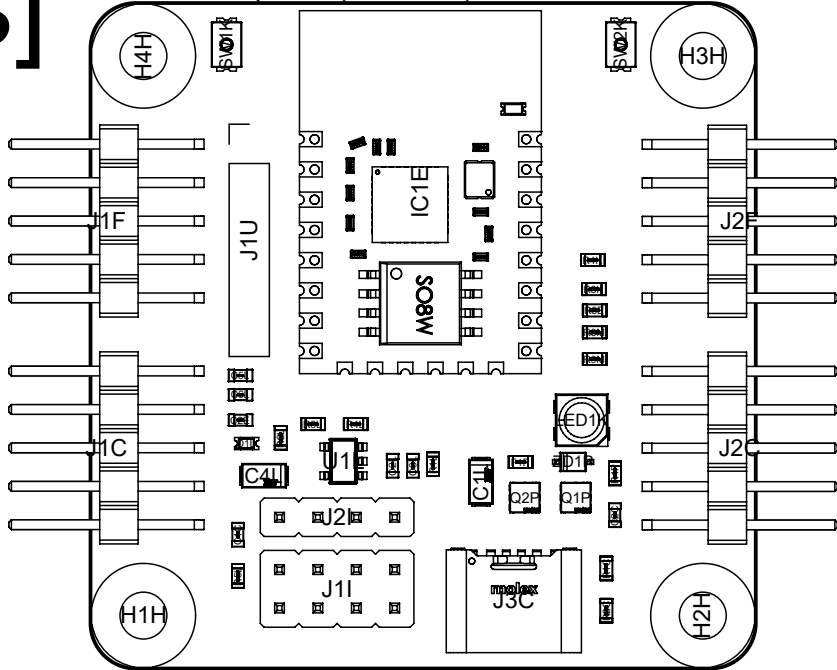
View from Front side (Scale 2:1)



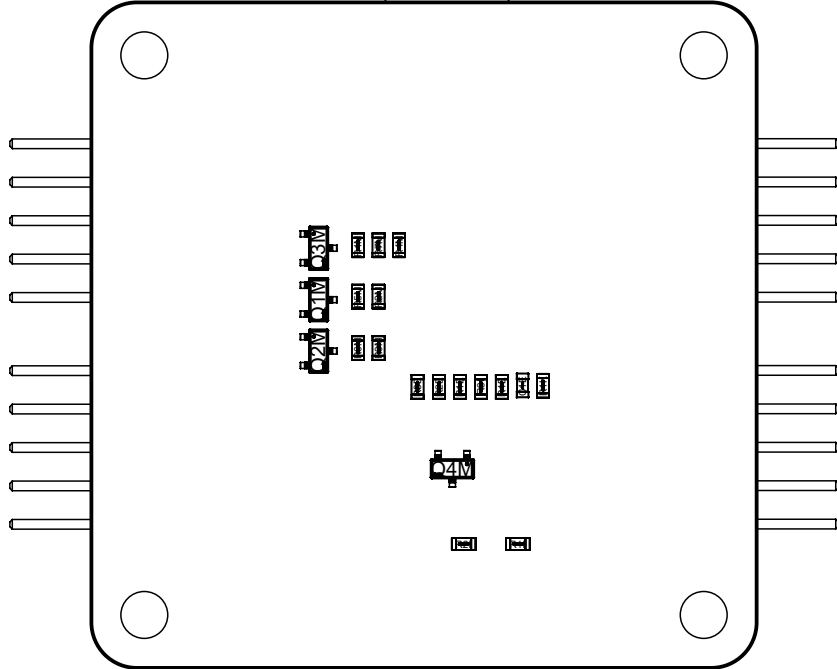
View from Back side (Scale 2:1)



View from Top side (Scale 2:1)

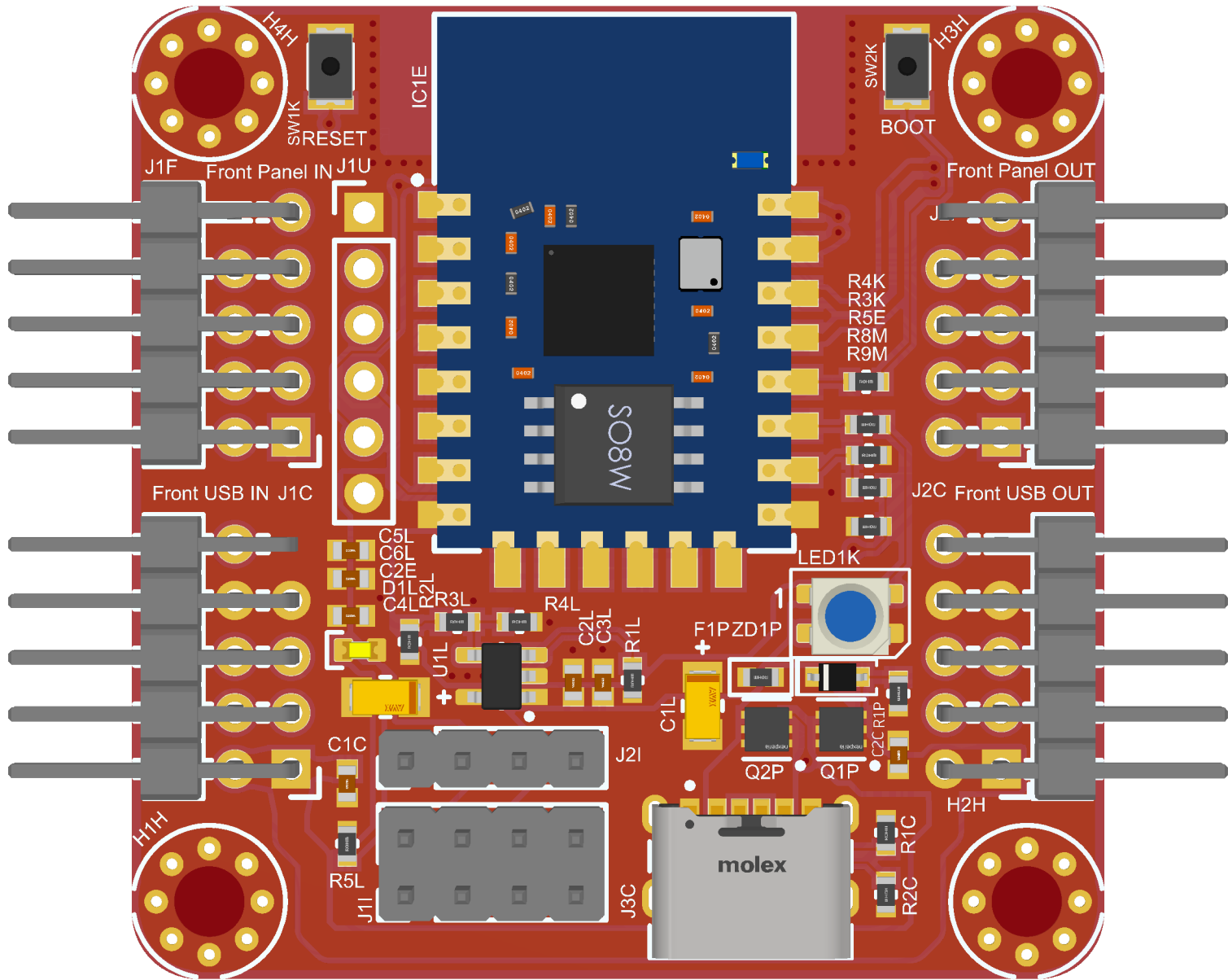


View from Bottom side (Scale 2:1)



Title: =ProjectTitle		Author:	<div>CONFIDENTIAL</div> <div>My Company</div> <div>Address Line 1</div> <div>Address Line 2</div> <div>Address Line 3</div> <div>Address Line 4</div> <div>[YOUR LOGO HERE]</div>	6	
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Unit: mm		Edited: 26-12-2024			
Date: 26-12-2024 06:28		Variant: [No Variations]			
MSheet 1 of 9		SW version: 25.1.2.22			
Git Hash: 27 [No modification]					
File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\ESP8266-Desktop Power\PCB ASSEMBLY ESP8266					

Realistic View TOP



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Unit: mm		Edited: 26-12-2024		
Date: 26-12-2024 06:28		Variant: [No Variations]		
Date: 26-12-2024 06:28		SW version: 25.1.2.22		
Git Hash: 27 [No modification]				
File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\ESP8266-Desktop Power\PCB ASSEMBLY ESP8266				

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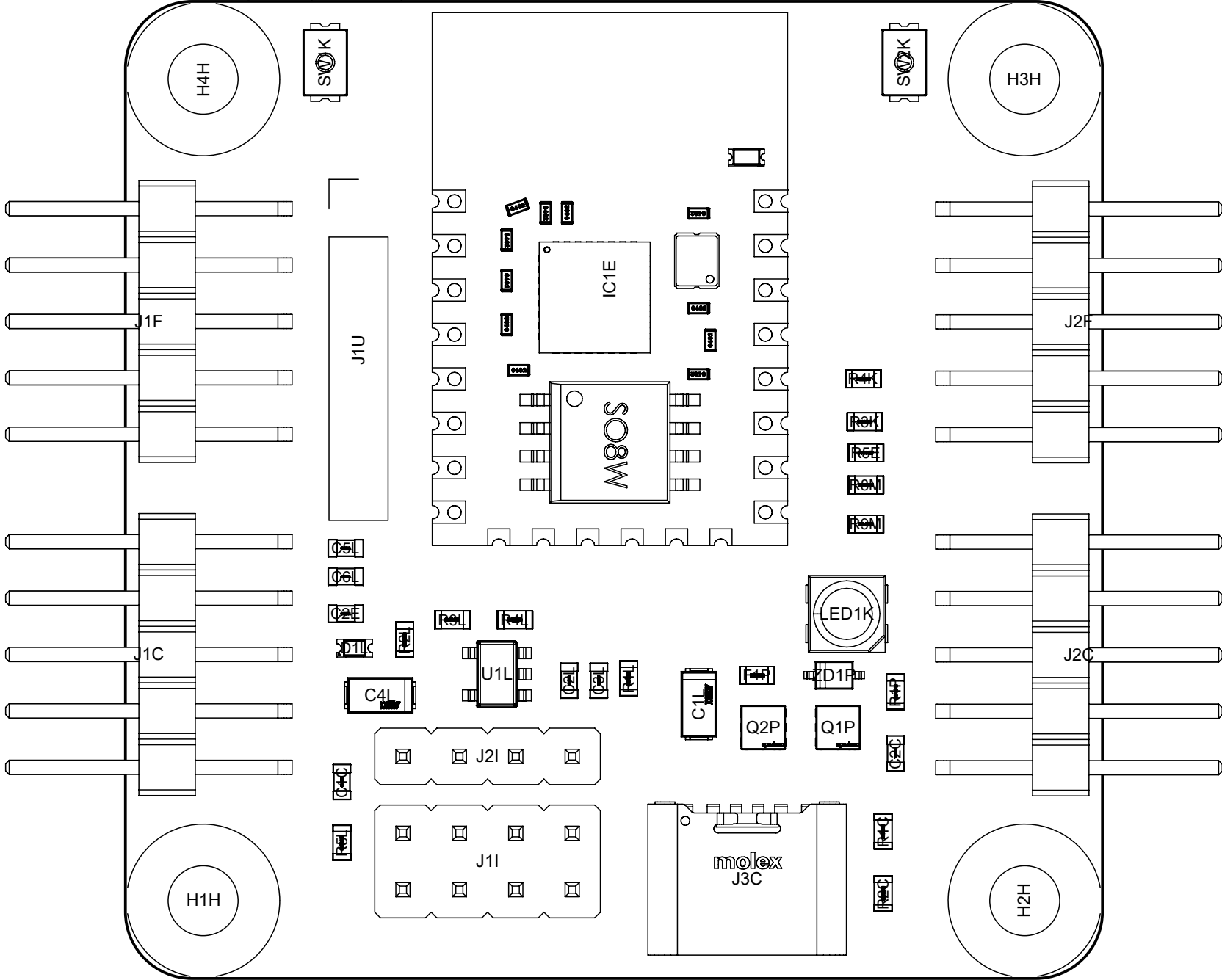
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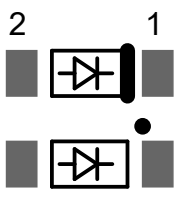
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View from Top side (Scale 4:1)



DIODE ORIENTATION

Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()

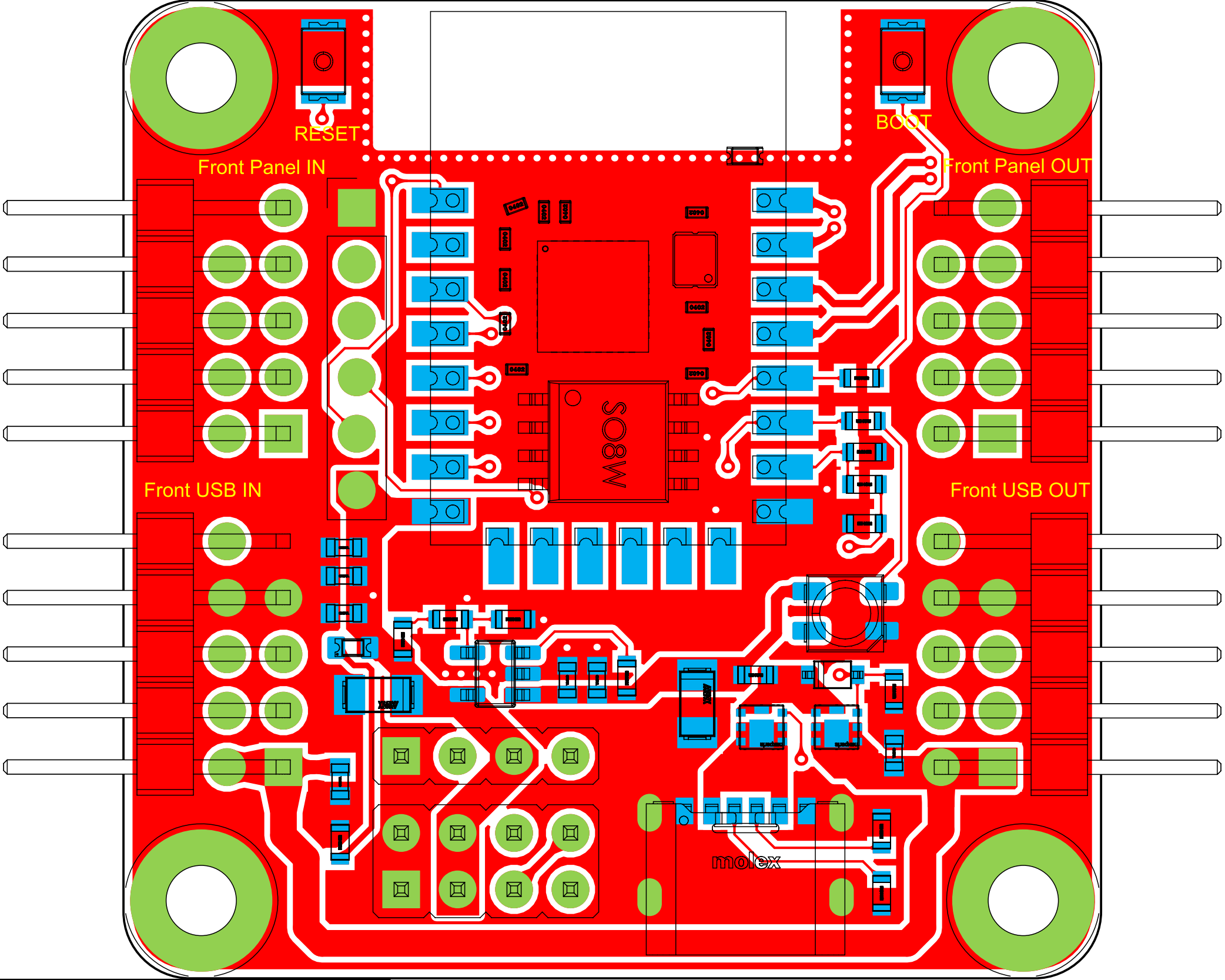


Name	CATHODE
Short name	K
Pin number	1
Silkscreen	thick line / dot
assembly view	dot (•)

Title: =ProjectTitle		Author:	CONFIDENTIAL My Company Address Line 1 Address Line 2 Address Line 3 Address Line 4	[YOUR LOGO HERE]	6
Size: A3	Prj: =ProjectTitle	Approved:			
Unit: mm		Edited: 26-12-2024			
Date: 26-12-2024 06:28		Variant: [No Variations]			
Date: 26-12-2024 06:28		SW version: 25.1.2.22			
Git Hash: 27 [No modification]					
File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\ESP8266-Desktop Power\PCB ASSEMBLY ESP8266					

[YOUR LOGO HERE]

View from Top side (Scale 5:1)

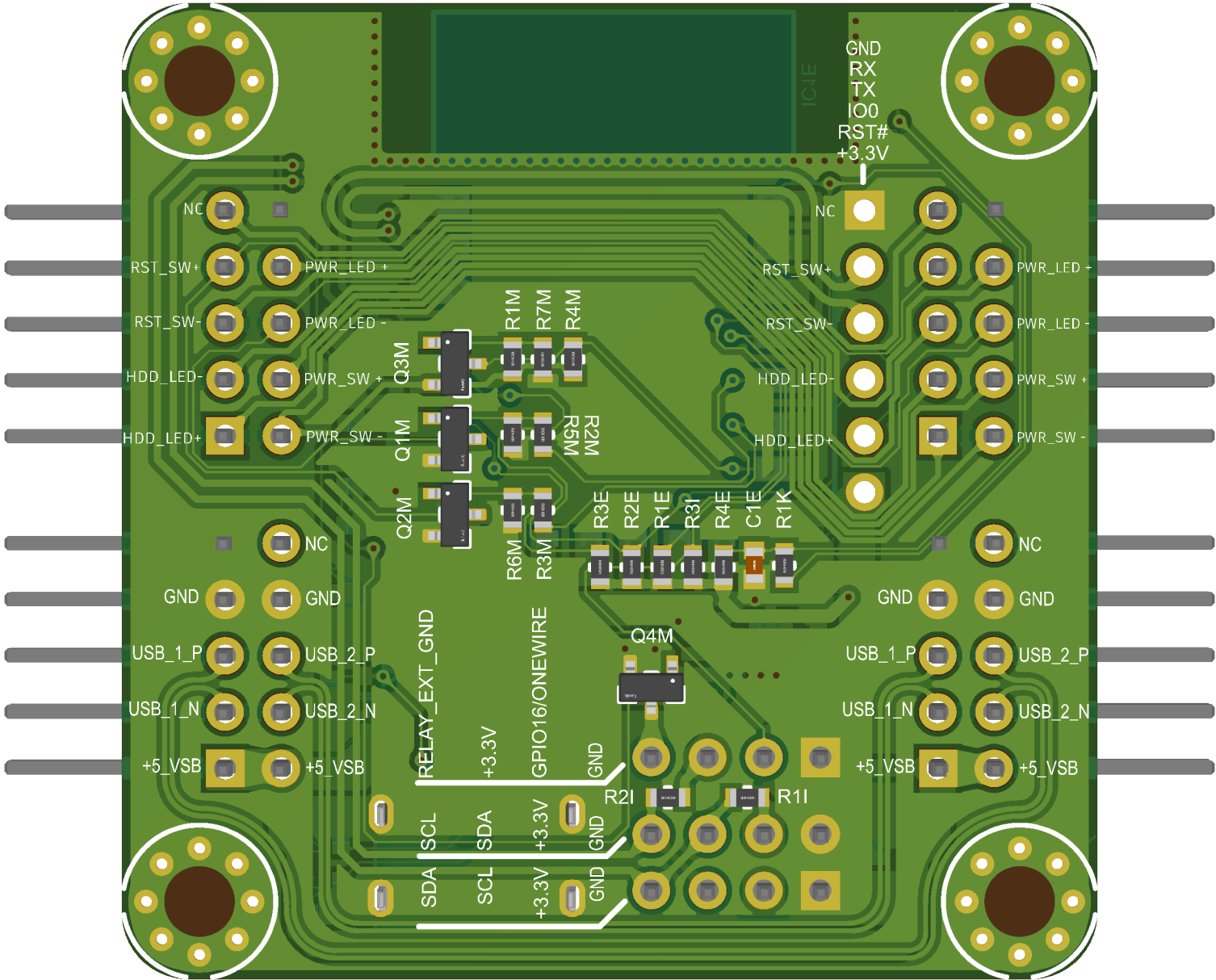


DIODE ORIENTATION

Name	ANODE	2	1	Name	CATHODE
Short name	A			Short name	K
Pin number	2			Pin number	1
Silkscreen	thin line			Silkscreen	thick line / dot
assembly view	no dot ()			assembly view	dot (•)

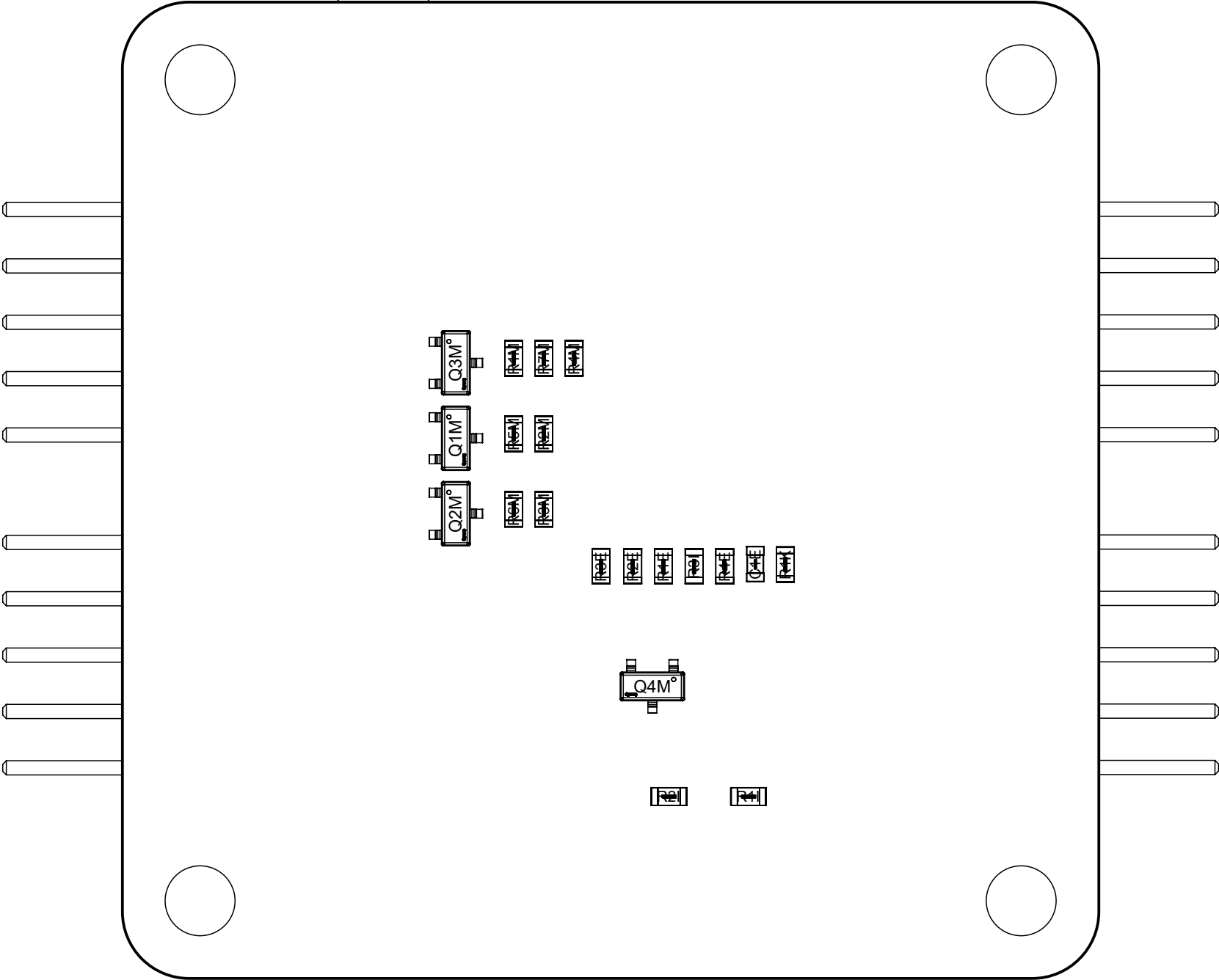
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Git Hash: 27 [No modification]				
SW version: 25.1.2.22				
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Realistic View BOTTOM



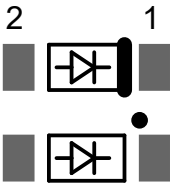
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Date: 26-12-2024 06:28		Variant: [No Variations]				Address Line 2
Date: 26-12-2024 06:28		SW version: 25.1.2.22	Address Line 3			
Git Hash: 27 [No modification]			Address Line 4			
File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\ESP8266-Desktop Power\PCB ASSEMBLY ESP8266						

View from Bottom side (Scale 4:1)



DIODE ORIENTATION

Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()



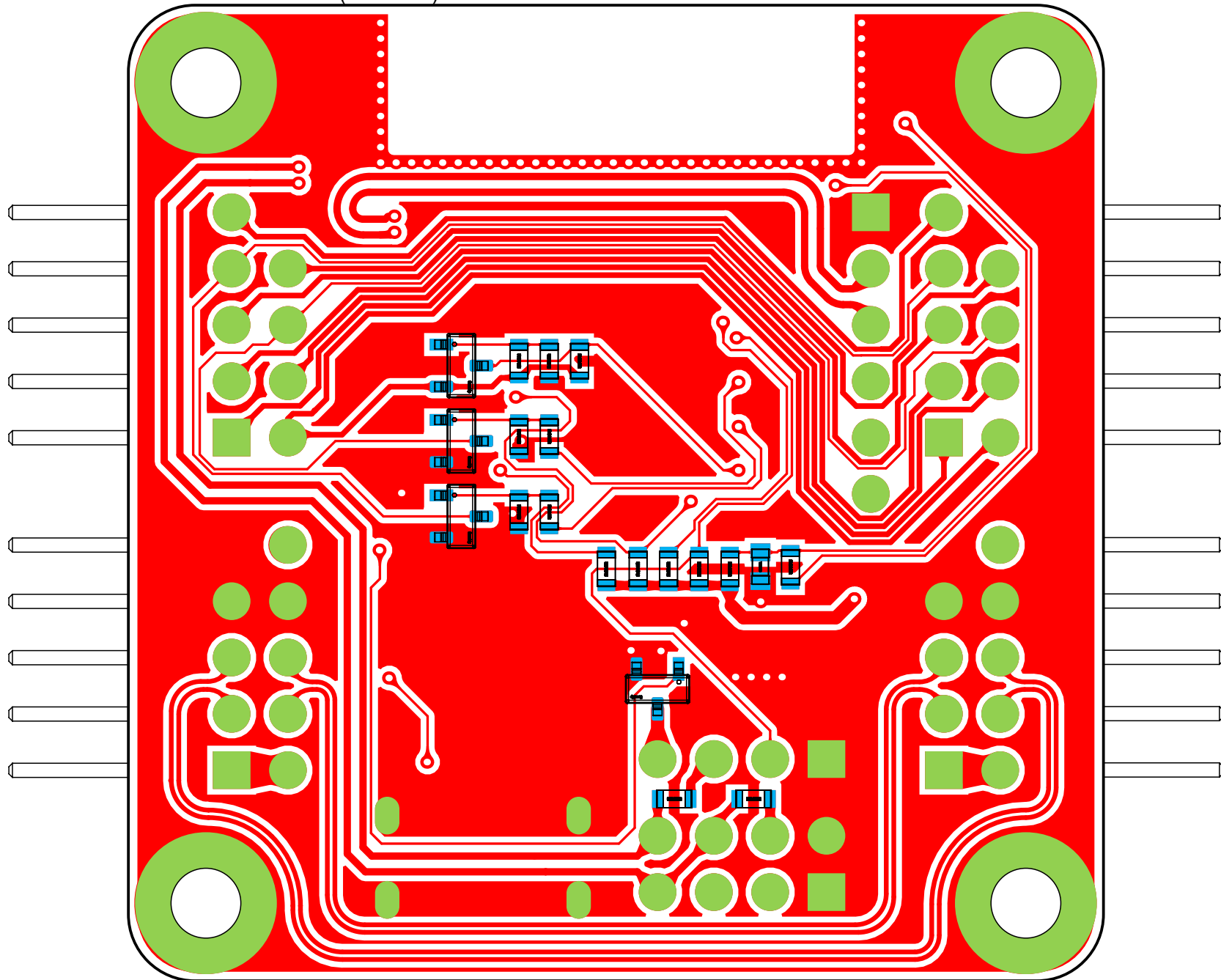
Name	CATHODE
Short name	K
Pin number	1
Silkscreen	thick line / dot
assembly view	dot (•)

Title: =ProjectTitle	
Size: A3	Prj: =ProjectTitle
Unit: mm	
Date: 26-12-2024 06:28 EMSheet 6 of 9	
Git Hash: 27 [No modification]	
File: C:\Users\desktop\Documents\Project Fi	

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Address Line 2
Address Line 3
Address Line 4

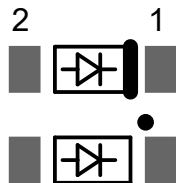
[YOUR LOGO HERE]

View from Bottom side (Scale 4:1)



DIODE ORIENTATION

Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()



Name	CATHODE
Short name	K
Pin number	1
Silkscreen	thick line / dot
assembly view	dot (•)

Title: =ProjectTitle

Size: A3

Unit: mm

Date: 26-12-2024 06:28 PMSheet 7 of 9

Git Hash: 27 [No modification]

File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\ESP8266-Desktop Power\PCB ASSEMBLY ESP8266

Author:

Approved:

Edited: 26-12-2024

Variant: [No Variations]

SW version: 25.1.2.22

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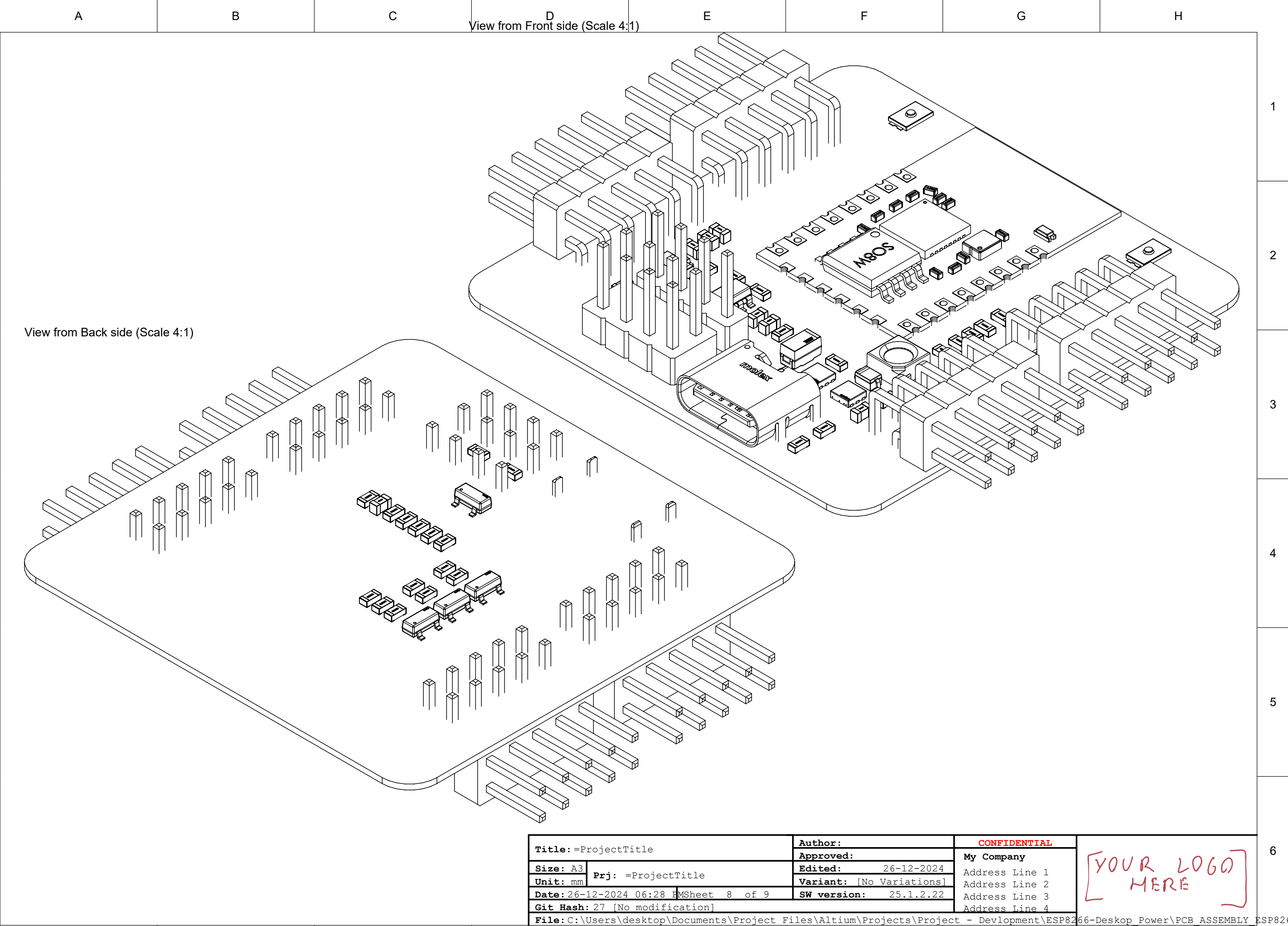
Address Line 1

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Address Line 3

Address Line 4

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1	<div>Bill Of Materials</div> <table><tr><th>Line #</th><th>Description</th><th>Designator</th><th>Quantity</th><th>Manufacturer Part Number 1</th><th>Part Number</th><th>Layer</th></tr><tr><td></td><td>Multilayer Ceramic Capacitor, 2.2 uF, 10 V, ± 10%, X7R, 0603 [1608 Metric]</td><td>C1C</td><td>1</td><td></td><td></td><td></td></tr><tr><td></td><td>Cap Cer 0.1UF 6.3V X7R 0603</td><td>C1E, C2E</td><td>2</td><td>KGM15AR70J104KM</td><td></td><td></td></tr><tr><td></td><td>Surface Mount Tantalum Capacitor, 47 uF, 10 V, ± 20%, -55 °C, 125 °C, 1206 [3216-12 Metric]</td><td>C1L</td><td>1</td><td></td><td></td><td></td></tr><tr><td></td><td>Multilayer Ceramic Capacitor, 4.7 uF, 10 V, ± 10%, X5R, 0603 [1608 Metric]</td><td>C2C</td><td>1</td><td>CL10A475KP8NNNC</td><td></td><td></td></tr><tr><td></td><td>Multilayer Ceramic Capacitor, 22 uF, 10 V, ± 20%, X5R, 0603 [1608 Metric]</td><td>C2L</td><td>1</td><td>GRM188R61A226ME15D</td><td></td><td></td></tr><tr><td></td><td>Multilayer Ceramic Capacitor, 0.1 uF, 10 V, ± 10%, X7R, 0603 [1608 Metric]</td><td>C3L</td><td>1</td><td>C0603C104K8RAC7867</td><td></td><td></td></tr><tr><td></td><td>47uF Solid MnO2 Tantalum Electrolytic Capacitor; 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6.3 V dc +/-20%; T491 Series	C4L	1	T491A476M006AT				Multilayer Ceramic Capacitor, 22 uF, 6.3 V, ± 20%, X5R, 0603 [1608 Metric]	C5L, C6L	2	GRM188R60J226MEA0D				LED 0603 YELLOW SMD	D1L	1					Fuse PPTC SMD 0603	F1P	1						H1H, H2H, H3H, H4H	4					ESP8266 ESP-12-F	IC1E	1						J1C, J2C	2						J1F, J2F	2						J1I	1						J1U	1						J2I	1					Connector USB Type C Female 6Positions 0.5mm Right Angle SMT Embossed T/R - Tape and Reel	J3C	1					4 PIN SMART LED WS2812 BODY 3.5MM X 3.5MM	LED1K	1					Single N-Channel 60 V 3.5 Ohm 1 nC SIPMOS® Small Signal Mosfet - SOT-23	Q1M, Q2M, Q3M, Q4M	4	BSS138N				PMPB14XPZ	Q1P, Q2P	2	PMPB14XPZ				SMD Chip Resistor, 5.1 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose	R1C, R2C	2	CRCW06035K10FKEA				SMD Chip Resistor, 10 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose	R1E, R1L, R2E, R3E, R4E, R4M, R5E, R5M, R6M, R7M, R9M	11	RC0603FR-0710KL				SMD Chip Resistor, 2.2 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose	R1I, R2I	2	RC0603FR-072K2L				SMD Chip Resistor, 1 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose	R1K, R4K	2	RC0603FR-101KL				SMD Chip Resistor, 100 Ohm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose	R1M, R2M, R3K, R3M, R8M	5	AC0603FR-07100RL				SMD Chip Resistor, 100 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose	R1P	1	AC0603FR07100KL				Surface Mount Thick Film Chip Resistor 0603 Case 1.69K Ohms 1% Tolerance 100 PPM	R2L	1	MCR03EZPFX1691				SMD Chip Resistor, 4.7 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose	R3I	1	AC0603FR-074K7L				Res Thick Film 0603 33K Ohm 1% 0.1W(1/10W) ±100ppm/C Pad SMD Automotive T/R	R3L	1	ERJ-3EKF3302V				SMD Chip Resistor, 20 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose	R4L	1	CR0603-FX-2002ELF				Res Thick Film 0603 2.49K Ohm 1% 1/10W ±100ppm/°C Molded SMD SMD Paper T/R	R5L	1					3.0MM X 2.0MM / 3.3N / 0.15MM ST	SW1K, SW2K	2	EVP-AWED4A	EVP-AWED4A			LDO U-Reg Adj 0, 3A SOT23-5	U1L	1					MM3Z12VST1G Zener Diode, 12V 2% 200 mW SMT 2-Pin SOD-323 ON Semiconductor MM3Z12VST1G	ZD1P	1				1
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