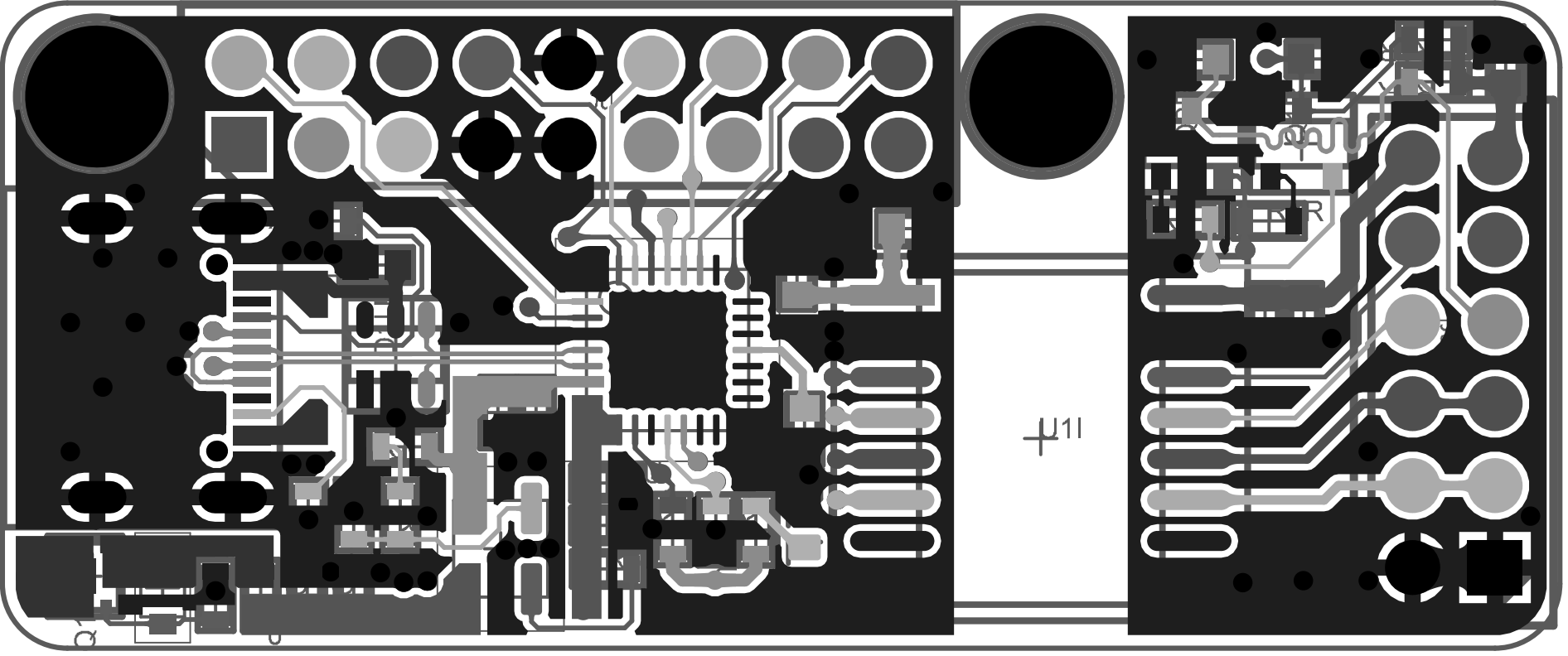
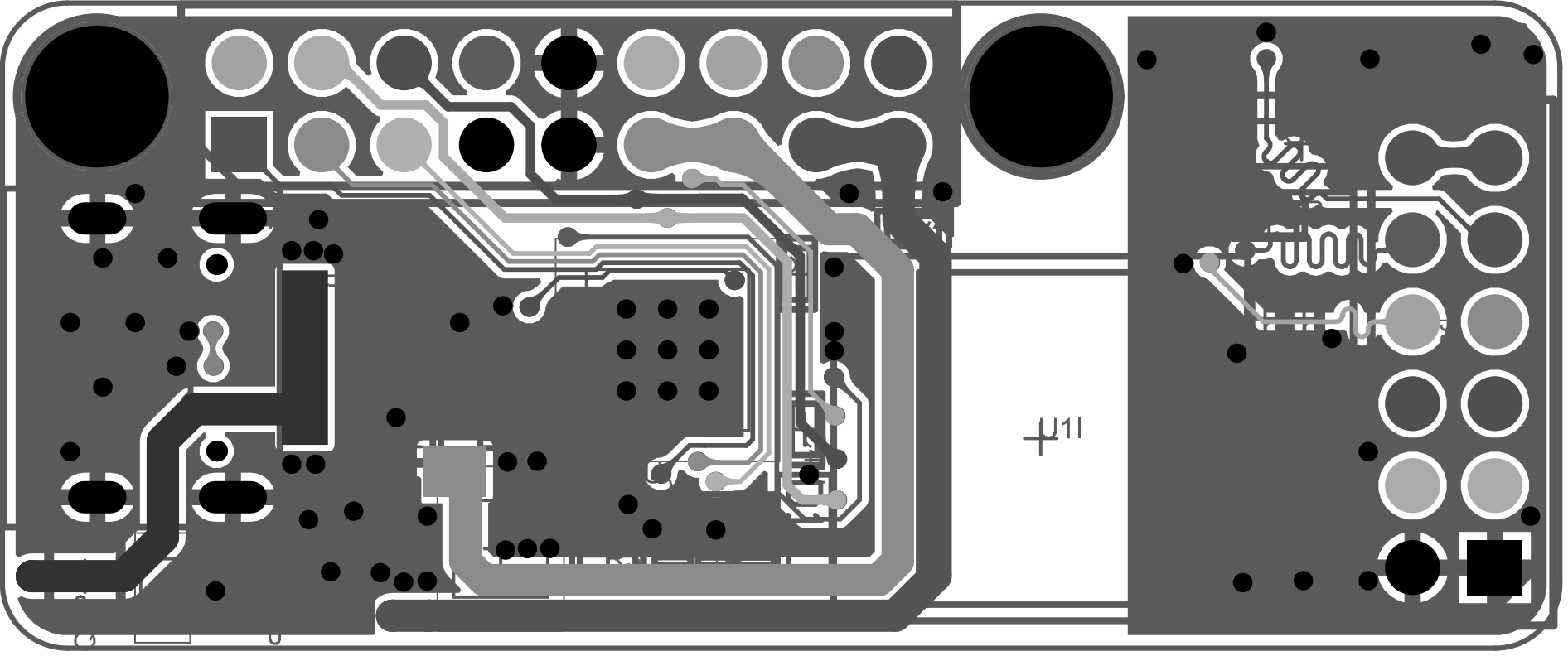
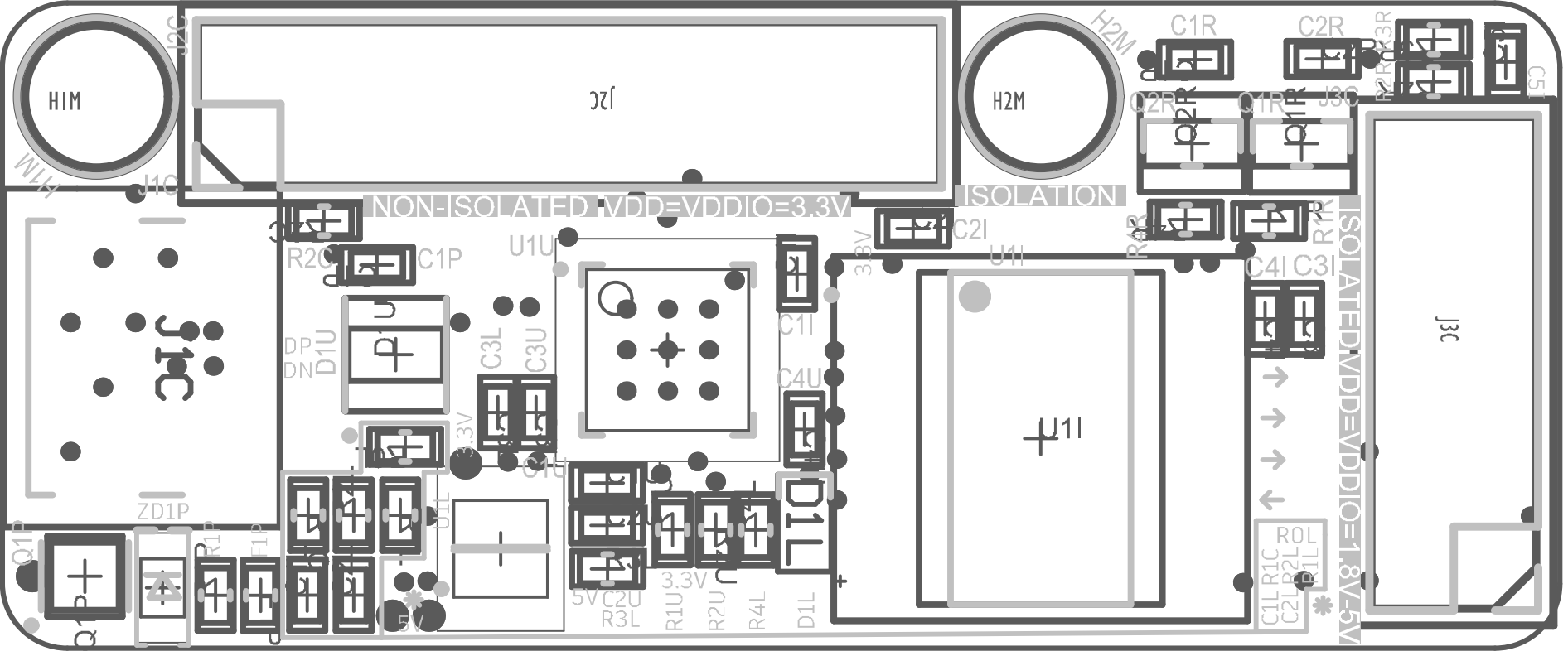



μ11


H1M
H2M
J2C
J3C
J1C
NON-ISOLATED VDD=VDDIO=3.3V
ISOLATION
ISOLATED VDD=VDDIO=1.8V-5V
U1U
U1I
C1R
C2R
C5
Q2R
Q1R
J3C
R2C
C1P
C2I
C1I
C4U
C4I
C3I
DP
DN
D1U
C3L
C3U
3.3V
ZD1P
Q1C
R1P
F1P
C1L
5V
C2U
R3L
R1U
R2U
R4L
D1L
R0L
C1L
R1C
C2L
R2L
R1L

M1H
M2H
WITHOUT AUTO PROG
WITH AUTO PROG
VDD
VDD
ISOLATION
RI# CTS# RTS# DSR# GND DTR# TXD RXD
5V 5V 3.3V 3.3V GND GND SUS# SUS
EN
RST
nRST
DTR#
RST#
IO0
GPIO0
TXD
TXD
RXD
RXD
GND
GND
2 Layers
0.6MM
Copper 1oz
Impedance
Single 50Ω
Single 60Ω
Diff 90Ω
USB TO
UART
ISOLATED
AND
RS-232
ISOLATED PORTS
SCH-Ver-1 & PCB-Ver-1
NON-ISOLATED PORTS
DCD#
RST#
Alcatraz
Register, 1%, 0603
Capacitor, 10%, 0603

J1C
J2C
W1M
W2M
U1
U2
U3
U4
U5
U6
U7
U8
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U40

H1M
J2C
H2M
Q2R
Q1R
J3C
U1
Q1L
J1C

H1M
J2C
H2M
Q2R
Q1R
J3C
U1
Q1L
J1C

H1M
J2C
H2M
Q2R
Q1R
J3C
U1
Q1L
J1C

H1M
J2C
H2M
Q2R
Q1R
J3C
U1
D1L
J1C

H1M
J2C
H2M
Q2R
Q1R
J3C
U1
D1L
J1C

H1M
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H2M
Q2R
Q1R
J3C
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Q1L
J1C

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D1L
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Q1R
J3C
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D1L
J1C

H1M
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Q1R
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Q1
J1C

H1M
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H2M
Q2R
Q1R
J3C
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H1M
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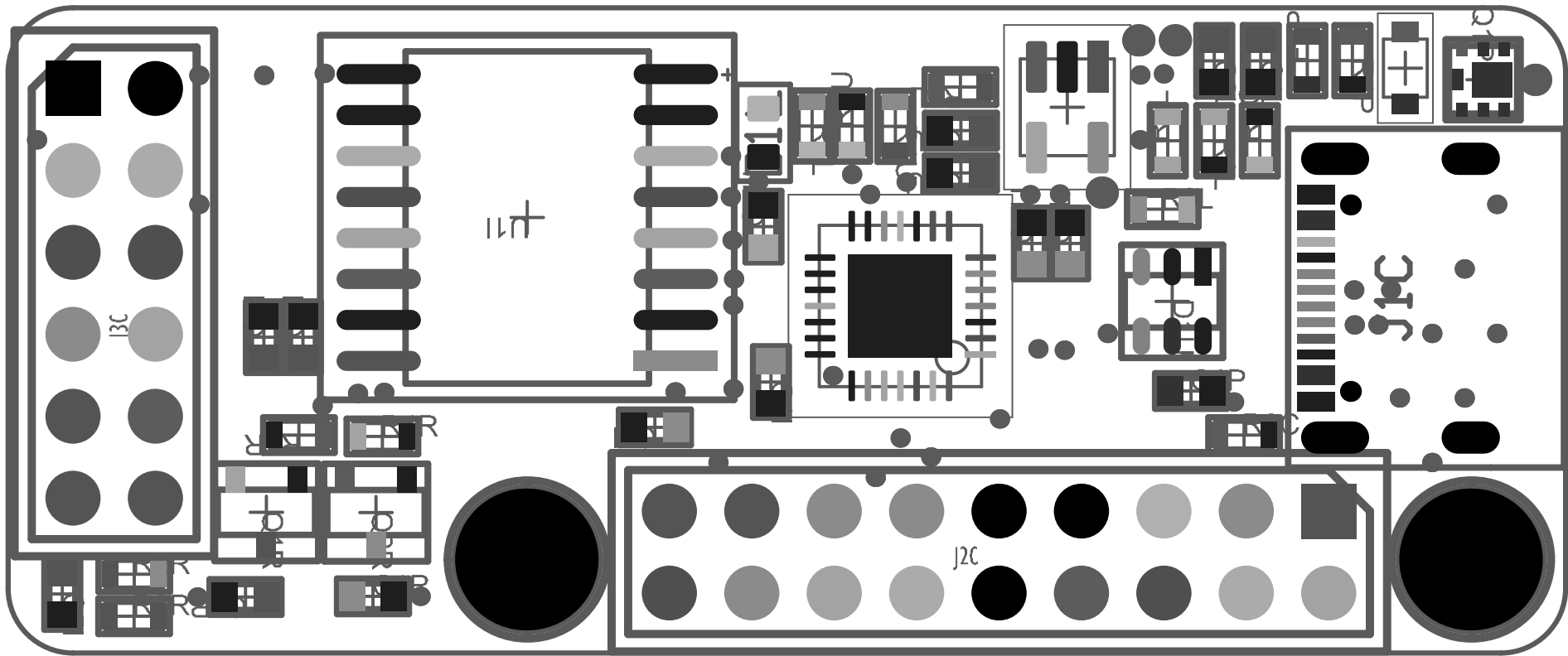
J3C
U11
J1C
J2C

J2C
J1C
U1
Q1
D1
L1
C1
C2
C3
C4
C5
C6
C7
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C40

J2C
J1C
J3C
Q1C
Q2R
Q1R
Q3R
Q4R
Q5R
Q6R
Q7R
Q8R
Q9R
Q10R
Q11R
Q12R
Q13R
Q14R
Q15R
Q16R
Q17R
Q18R
Q19R
Q20R
Q21R
Q22R
Q23R
Q24R
Q25R
Q26R
Q27R
Q28R
Q29R
Q30R
Q31R
Q32R
Q33R
Q34R
Q35R
Q36R
Q37R
Q38R
Q39R
Q40R
Q41R
Q42R

J2C
H1M
H2M
J1C
J3C
U1
U2
U3
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U39
U40

H1M
J2C
H2M
Q2R
Q1R
J3C
U1I
U1D
J1C