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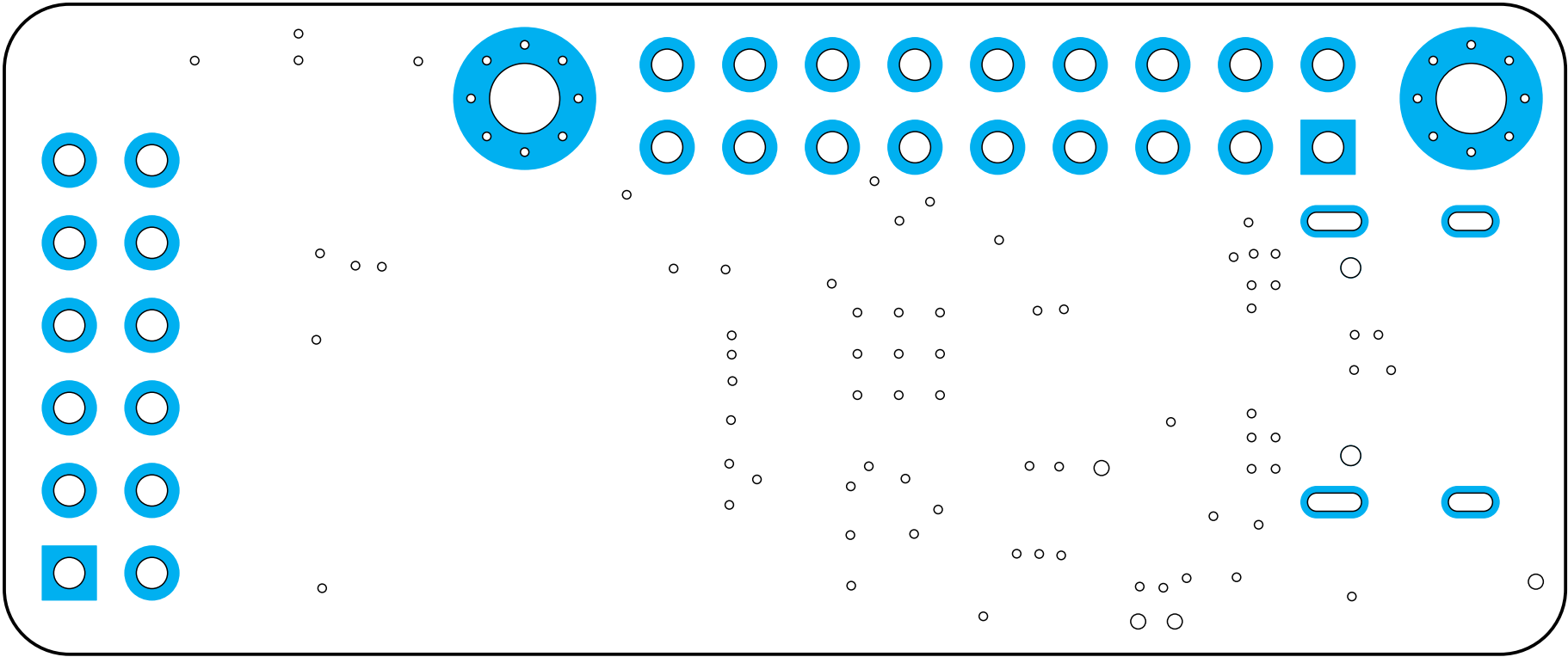
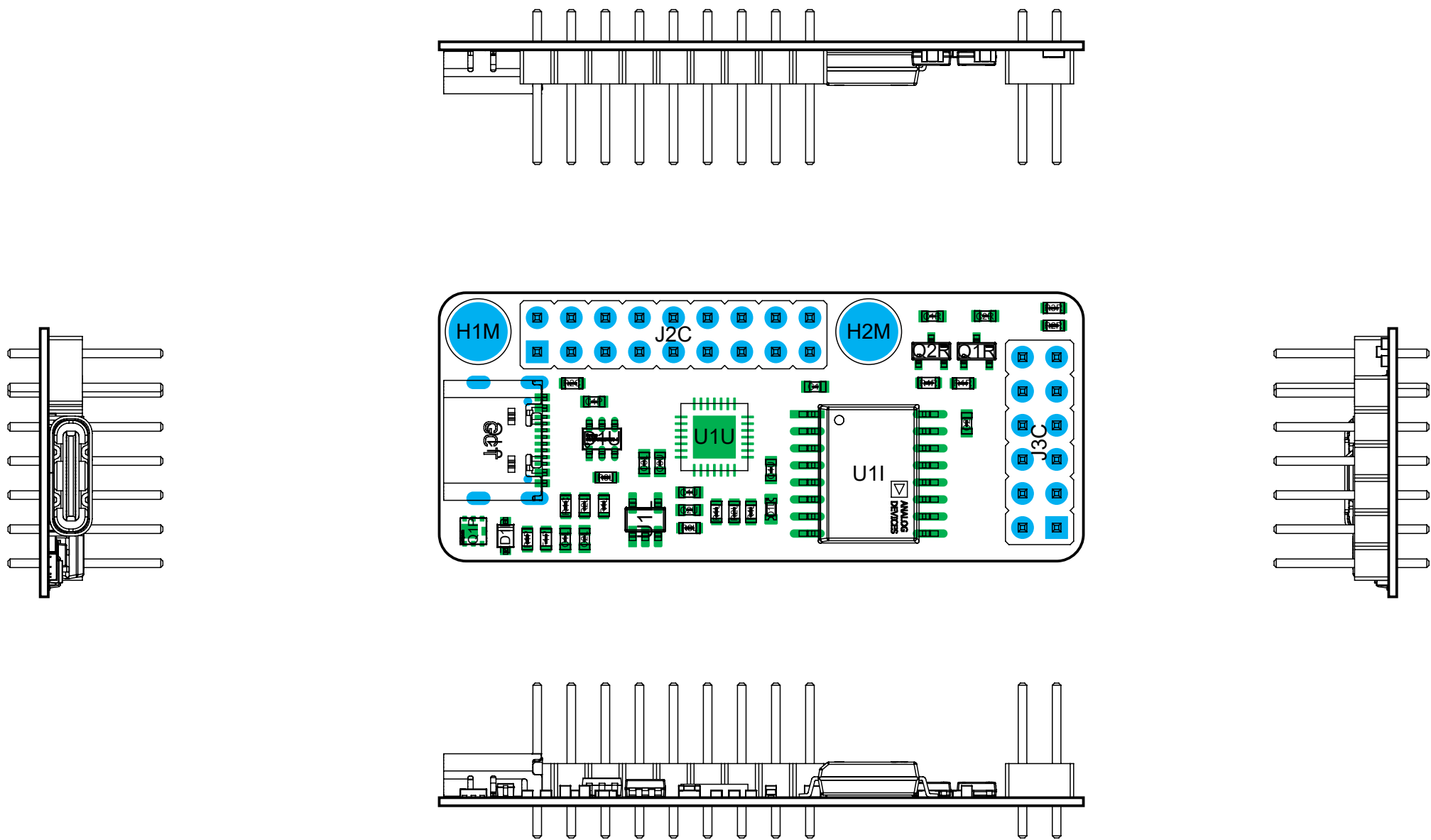
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- Note:
- 1 Text element with square border.
 - 2 Text element with no border
 - 3 Text element with circle border

THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF . ANY REPRODUCTION IN PART OR AS A WHOLE WITHOUT THE WRITTEN PERMISSION OF IS PROPRIETARY AND CONFIDENTIAL			UNLESS OTHERWISE SPECIFIED:		NAME	DATE					
			DIMENSIONS ARE IN INCHES TOLERANCES:	DRAWN		19-11-2024					
			FRACTIONAL ±	CHECKED			TITLE				
			ANGULAR: MACH± BEND ±	ENG APPR.							
			TWO PLACE DECIMAL ±	MFG APPR.							
			THREE PLACE DECIMAL ±	Q.A.							
			INTERPRET GEOMETRIC TOLERANCING PER:	COMMENTS:							
			MATERIAL								
			FINISH								
	NEXT ASSY	USED ON	DO NOT SCALE DRAWING					SIZE	DWG. NO.		
	APPLICATION							SCALE: 1:1	WEIGHT:	SHEET 1 OF 1	

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PCB MANUFACTURING SPECIFICATIONS

"=ProjectTitle"

SPECIFICATIONS

NOTE #	NOTE
1	ALL SPECIFICATIONS REFERENCED ARE OF THE REVISION SPECIFIED IN THE TITLE BLOCK
2	SUPPLIER SHALL NOT MODIFY THE DESIGN OR APPROVED STACK-UP WITHOUT WRITTEN PERMISSION
3	ALL MATERIALS SHALL BE RoHS COMPLIANT AND FINAL PRODUCT SHALL BE ACCEPTABLE TO USE IN RoHS ASSEMBLY. RoHS LOGO SHALL BE MARKED IN SILKSCREEN INK BY THE SUPPLIER WHERE INDICATED BY THE TEXT "PLACE MARKINGS HERE"
4	COPPER FOIL: REFER TO LAYER STACK LEGEND FOR Cu THICKNESS DETAILS. ALL Cu THICKNESSES ARE FINISHED AND INCLUDE BASE FOIL PLUS Cu PLATING ON PLATED LAYERS
5	ELECTRICAL TEST: ALL PRINTED CIRCUITS SHALL BE 100% ELECTRICALLY TESTED FOR OPENS/SHORTS USING PROVIDED NETLIST. REJECTED PRINTED BOARDS MUST BE CLEARLY MARKED WITH NON-CONDUCTIVE, PERMANENT INK.
6	MARKINGS: VENDOR MARKING AND DATE/LOT CODES SHALL BE LOCATED ON THE BOARD IN THE RESERVED AREA AS SPECIFIED IN THE GERBER LAYER "PCBM_NOTES" BY THE TEXT "PLACE MARKINGS HERE".
7	MARKINGS: THE SIDE ONTO WHICH PLACE THE MARKINGS IS AT THE SUPPLIER DISCRESSION UNLESS OTHERWISE NOTED ONTO THE LAYER "PCBM_NOTES"
8	SUPPLIER SHALL CHECK PCBM_NOTES LAYER BEFORE ASKING FOR CLARIFICATIONS
9	MANUFACTURE TENTED/PLUGGED VIAS AS SPECIFIED IN THE GERBER FILES

SPECIFICATIONS

LENGHT	20.00mm
WIDTH	48.00mm
LAYERS	2
MATERIAL	FR-4
MATERIAL MIN TG	130-140
TRACK WIDTH/CLEARANCE	8 mils / 8 mils
THICKNESS	1.6mm
COPPER THICKNESS	35um (1oz)
SOLDERMASK	YES, TOP AND BOTTOM
SOLDERMASK COLOR	RED
SILKSCREEN	YES, TOP AND BOTTOM
SILKSCREEN COLOR	WHITE
SURFACE FINISH	HASL LEAD FREE
GOLD FINGERS	NO
CHAMFERING	NO
IMPEDANCE CONTROL	NO
HALF-CUT/CASTELLATED HOLES	NO
BURIED/BLIND VIAS	NO
VIAS FILLED WITH RESIN	NO
CARBON MASK	NO
COUNTERSINKS/COUNTERBORES	NO
Z-AXIS MILLING	NO
PEELABLE SOLDERMASK	NO

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
Surface Material	Top Solder	0.02mm	Solder Resist	Solder Mask	GTS
Copper	Top Layer	0.04mm		Signal	GTL
Core		0.50mm	FR-4	Dielectric	
Copper	Bottom Layer	0.04mm		Signal	GBL
Surface Material	Bottom Solder	0.02mm	Solder Resist	Solder Mask	GBS
	Bottom Overlay			Legend	GBO

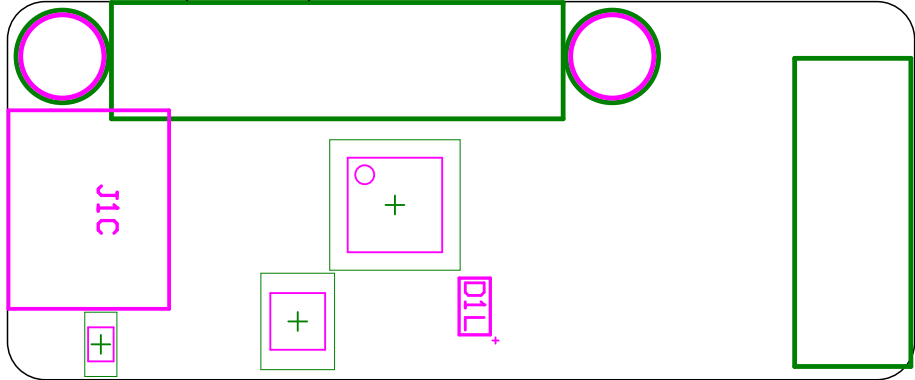
Total thickness: 0.60mm

NON-COPPER LAYER THICKNESS FOR REFERENCE ONLY
LAYERS OF TYPE "INTERNAL PLANE" ARE NEGATIVE

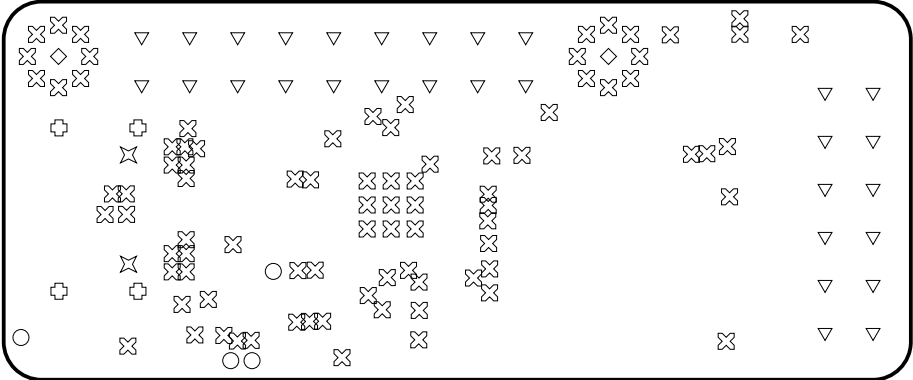
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Unit: mm		Edited: 19-11-2024			
Date: 19-11-2024 09:54 AM		Variant: [No Variations]			
SW version: 24.10.1.45					
Git Hash:					
File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\USB-UART-ISO-CP2102\PCB MANUFACTURING					

[YOUR LOGO HERE]

Mechanical 1 (Scale 5:2)



Drill Drawing View (Scale 5:2)



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
×	88	0.30mm	Есть	
○	4	0.50mm	Есть	
⊕	4	0.60mm	Есть	
✱	2	0.65mm	Нет	
▽	30	1.00mm	Есть	
◇	2	2.20mm	Есть	
	130 Total			

Dimensions (Scale 5:2)

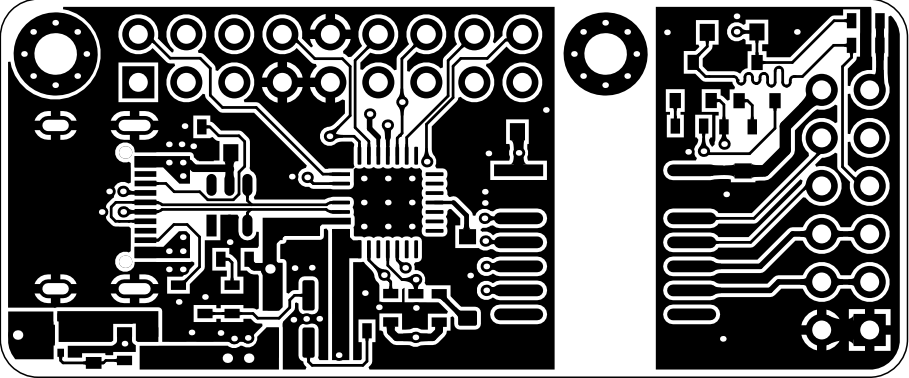


ALWAYS CAREFULLY READ
THE NOTES ON THIS LAYER!

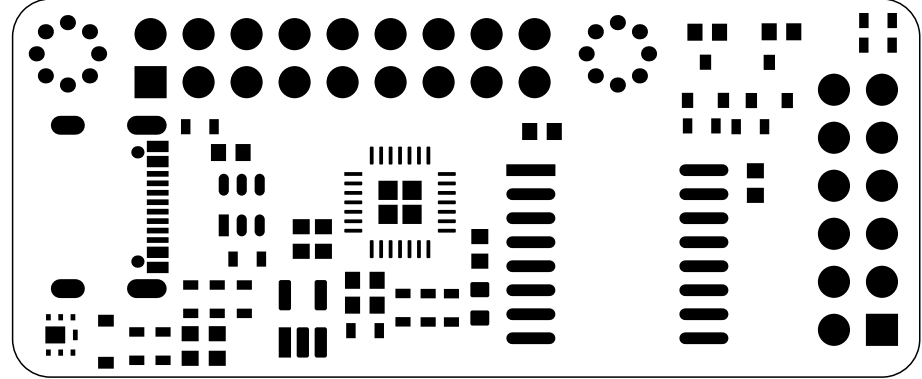
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Unit: mm		Edited: 19-11-2024			
Date: 19-11-2024 09:54 AM		Variant: [No Variations]			
Sheet 2 of 3		SW version: 24.10.1.45			
Git Hash: 404 [No modification]					
File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\USB-UART-ISO-CP2102\PCB MANUFACTURING U					

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HERE]

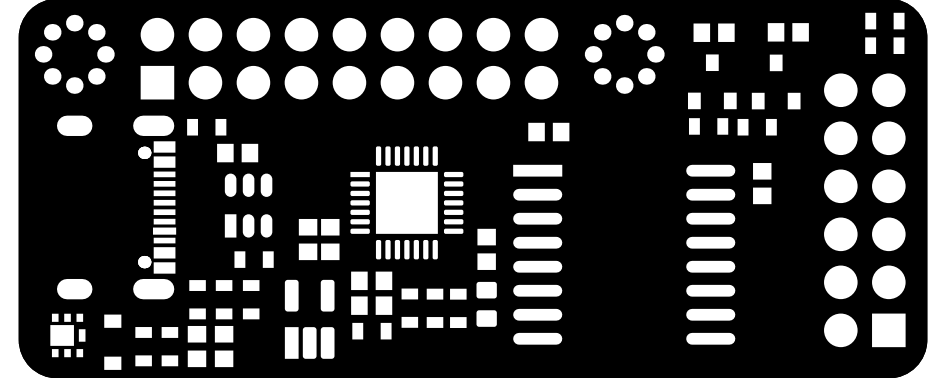
Top Layer (Scale 5:2)



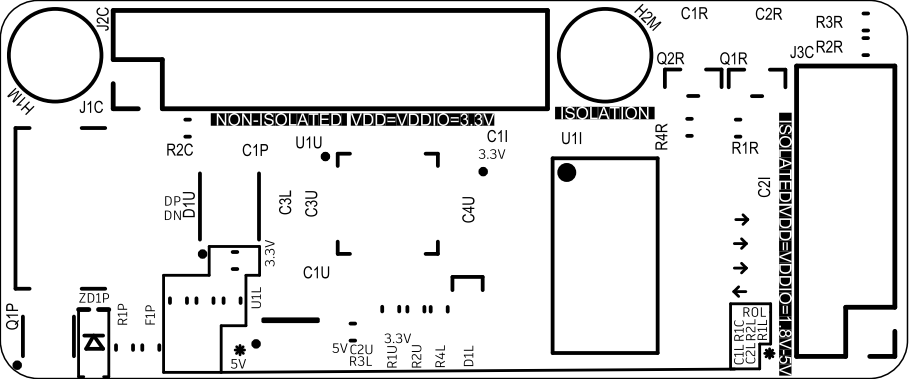
Top Paste (Scale 5:2)



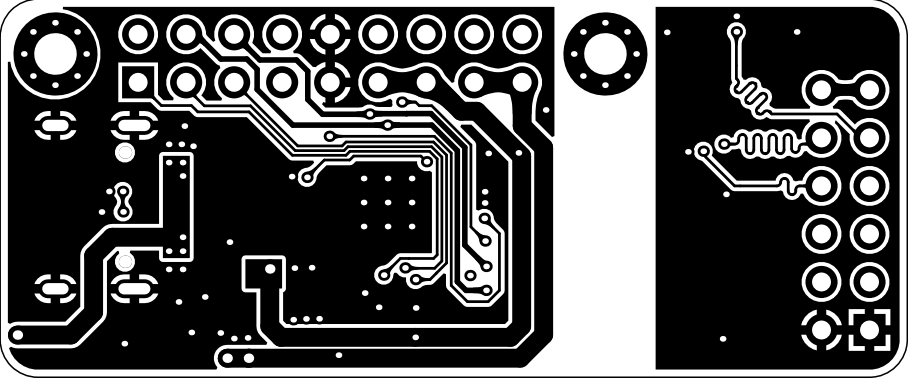
Top Solder (Scale 5:2)



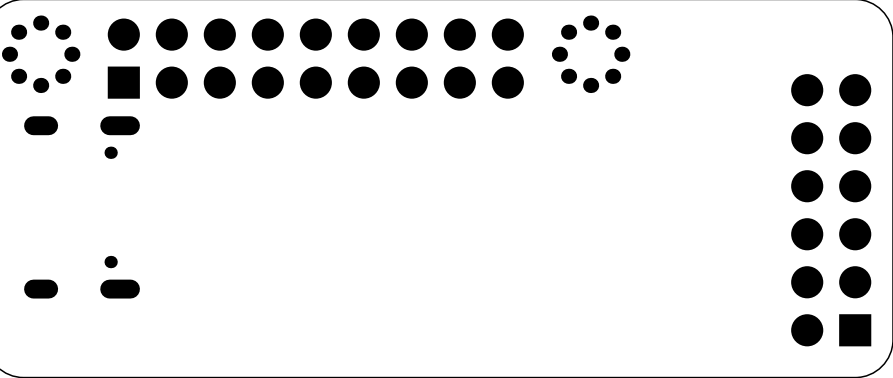
Top Overlay (Scale 5:2)



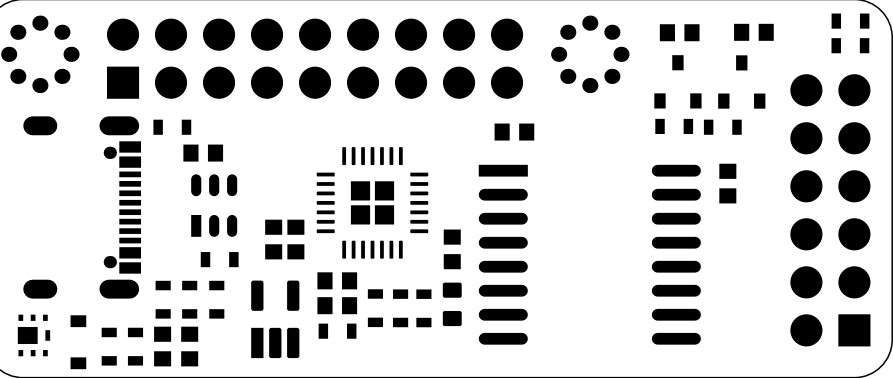
Bottom Layer (Scale 5:2)



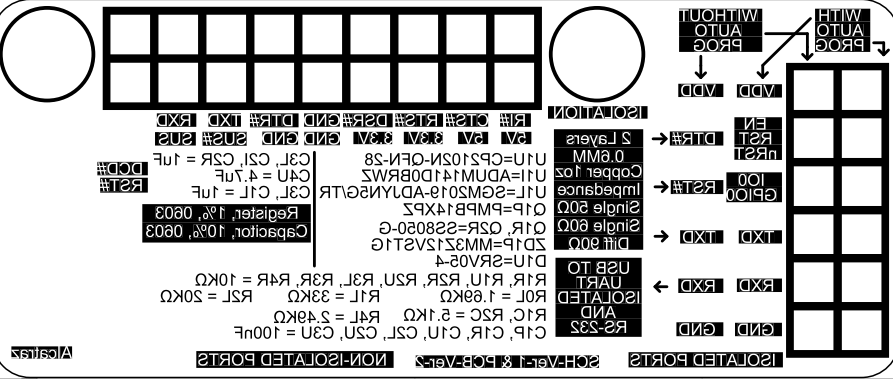
Bottom Paste (Scale 5:2)



Top Paste (Scale 5:2)



Bottom Overlay (Scale 5:2)



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PCB ASSEMBLY SPECIFICATIONS

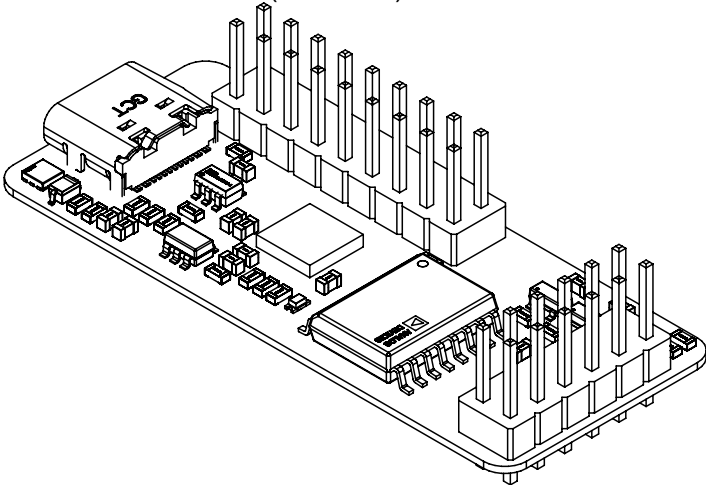
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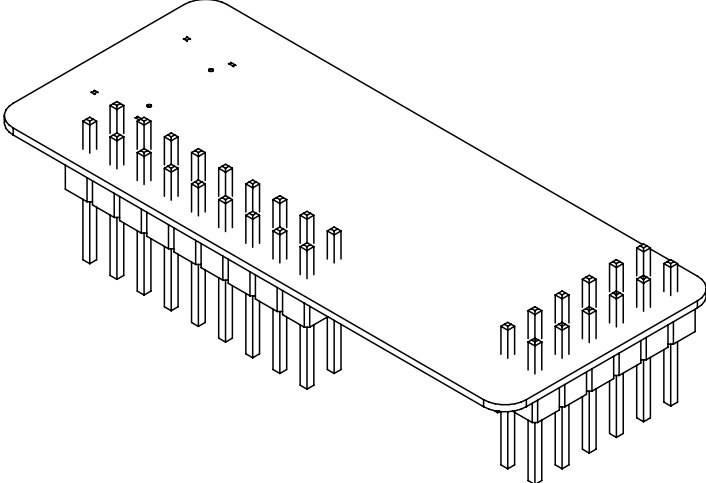
SPECIFICATIONS

NOTE #	NOTE
1	ALL SPECIFICATIONS REFERENCED SHALL BE OF THE LATEST REVISION UNLESS OTHERWISE NOTED
2	SUPPLIER SHALL NOT MODIFY THE DESIGN WITHOUT WRITTEN PERMISSION
3	REFER TO EXCEL BOM FOR UP-TO-DATE INFORMATION
4	THE BOM IN THIS DOCUMENT IS PURELY AN AID TO ASSEMBLY OPERATIONS AND MAY NOT HAVE THE MOST UP-TO-DATE DATA OR ALL APPROVED COMPONENT ALTERNATIVE.

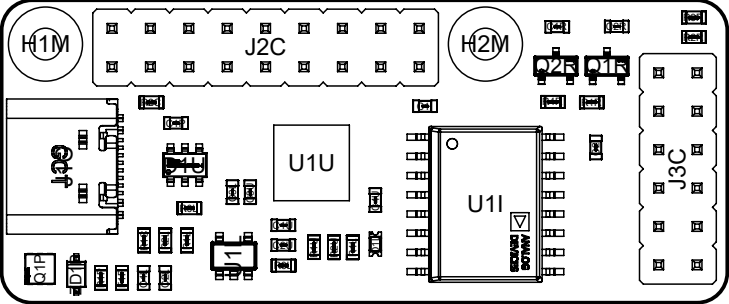
View from Front side (Scale 2:1)



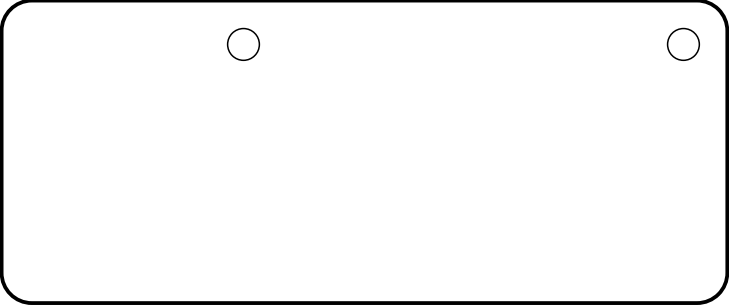
View from Back side (Scale 2:1)



View from Top side (Scale 2:1)

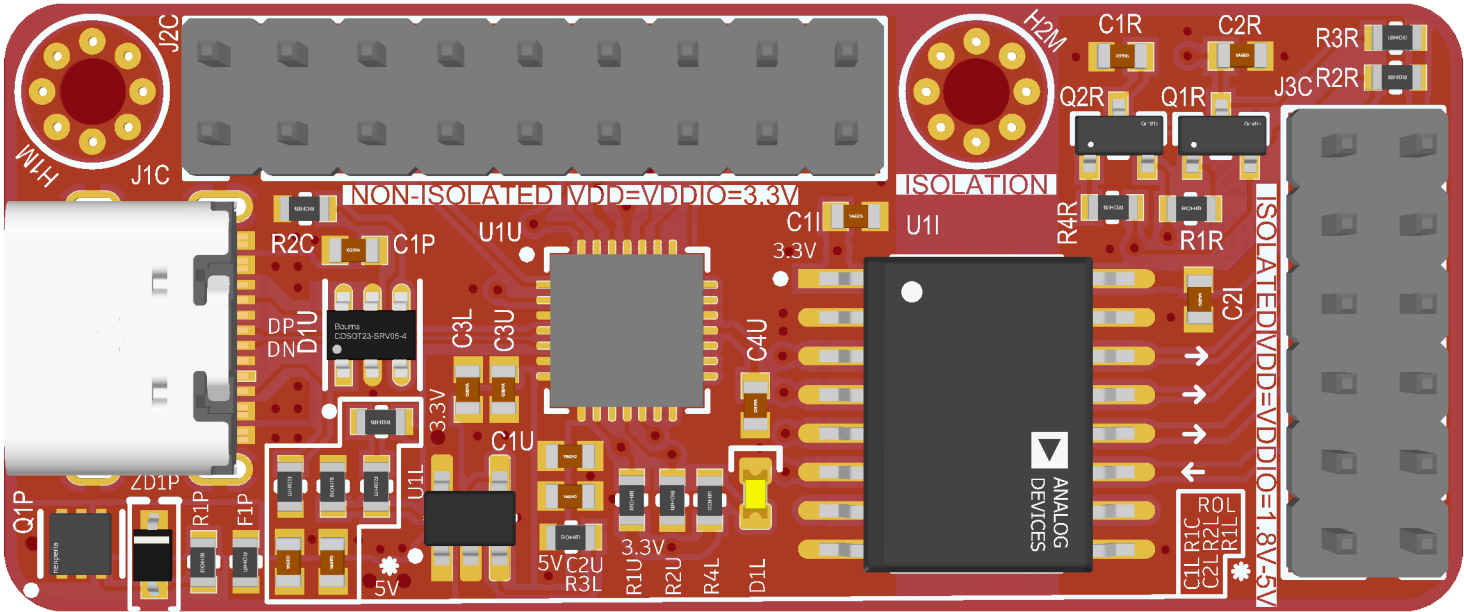


View from Bottom side (Scale 2:1)



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		Approved:		
Size: A3	Prj: =ProjectTitle	Edited: 19-11-2024		
Unit: mm		Variant: [No Variations]		
Date: 19-11-2024 09:56 AM		SW version: 24.10.1.45		
Sheet 1 of 9				
Git Hash:				
File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\USB-UART-ISO-CP2102\PCB ASSEMBLY USB-UAR				

Realistic View TOP



Title: =ProjectTitle			Author:		CONFIDENTIAL		<div>[YOUR LOGO HERE]</div>	6
Size: A3			Approved:		My Company			
Unit: mm			Prj: =ProjectTitle		Edited: 19-11-2024			
Date: 19-11-2024 09:56 AM Sheet 2 of 9			Variant: [No Variations]		Address Line 1			
Git Hash: 404 [No modification]			SW version: 24.10.1.45		Address Line 2			
					Address Line 3			
					Address Line 4			
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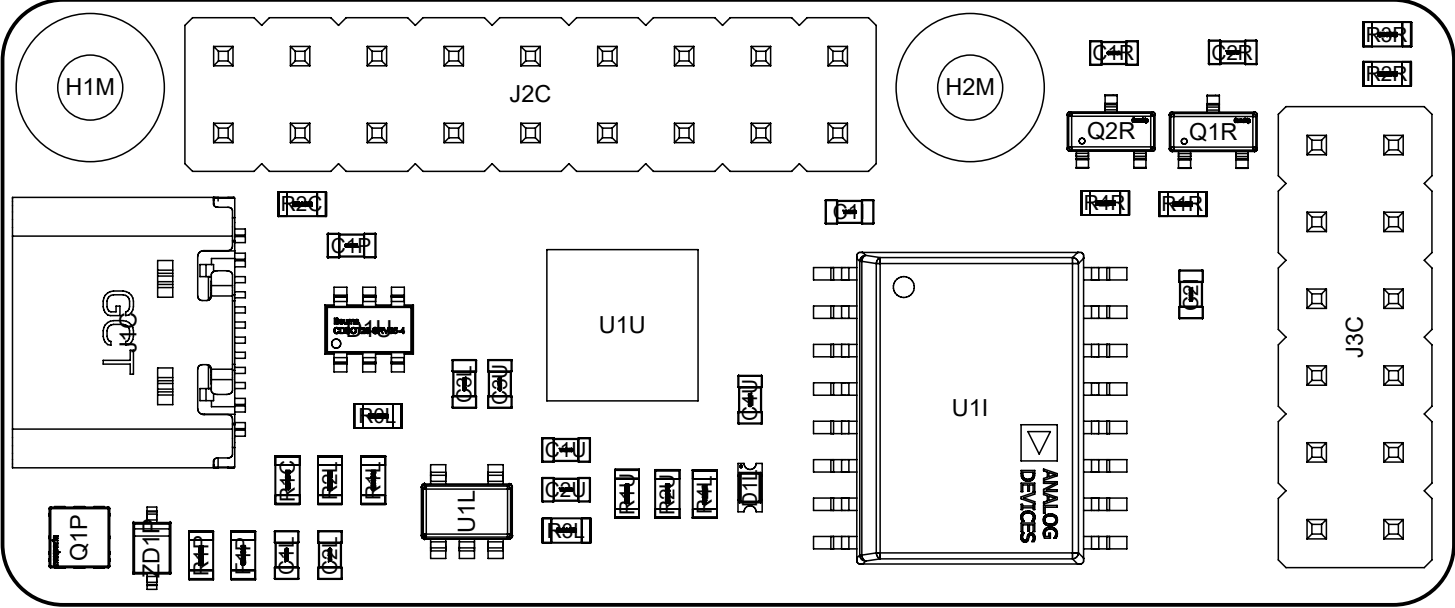
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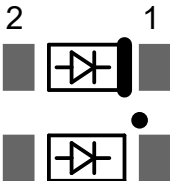
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View from Top side (Scale 4:1)



DIODE ORIENTATION

Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()

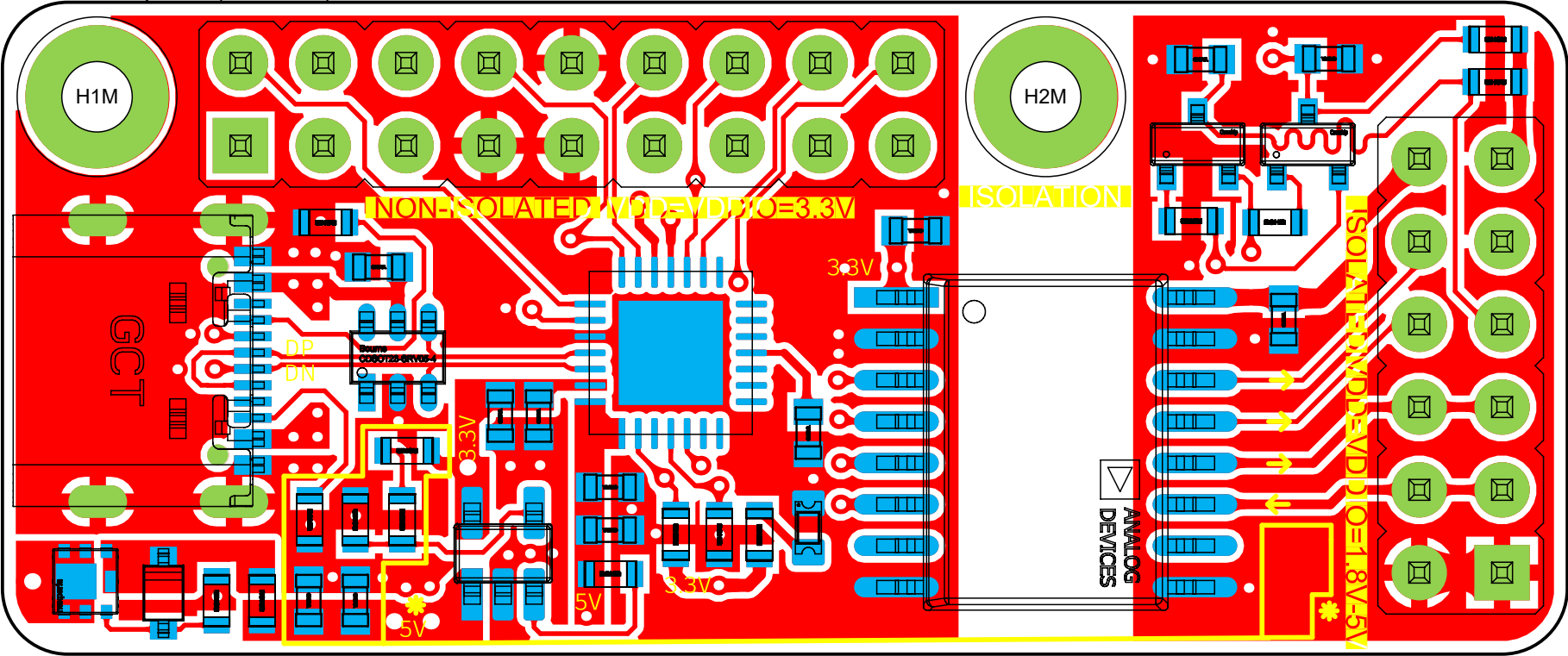


Name	CATHODE
Short name	K
Pin number	1
Silkscreen	thick line / dot
assembly view	dot (•)

Title: =ProjectTitle			Author:	CONFIDENTIAL My Company Address Line 1 Address Line 2 Address Line 3 Address Line 4	[YOUR LOGO HERE]	6
Size: A3			Approved:			
Unit: mm	Prj: =ProjectTitle		Edited: 19-11-2024			
Date: 19-11-2024 09:56		AMSheet 3 of 9	Variant: [No Variations]			
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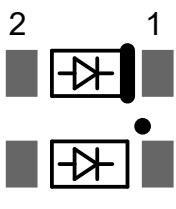
[YOUR LOGO HERE]

View from Top side (Scale 5:1)



DIODE ORIENTATION

Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()



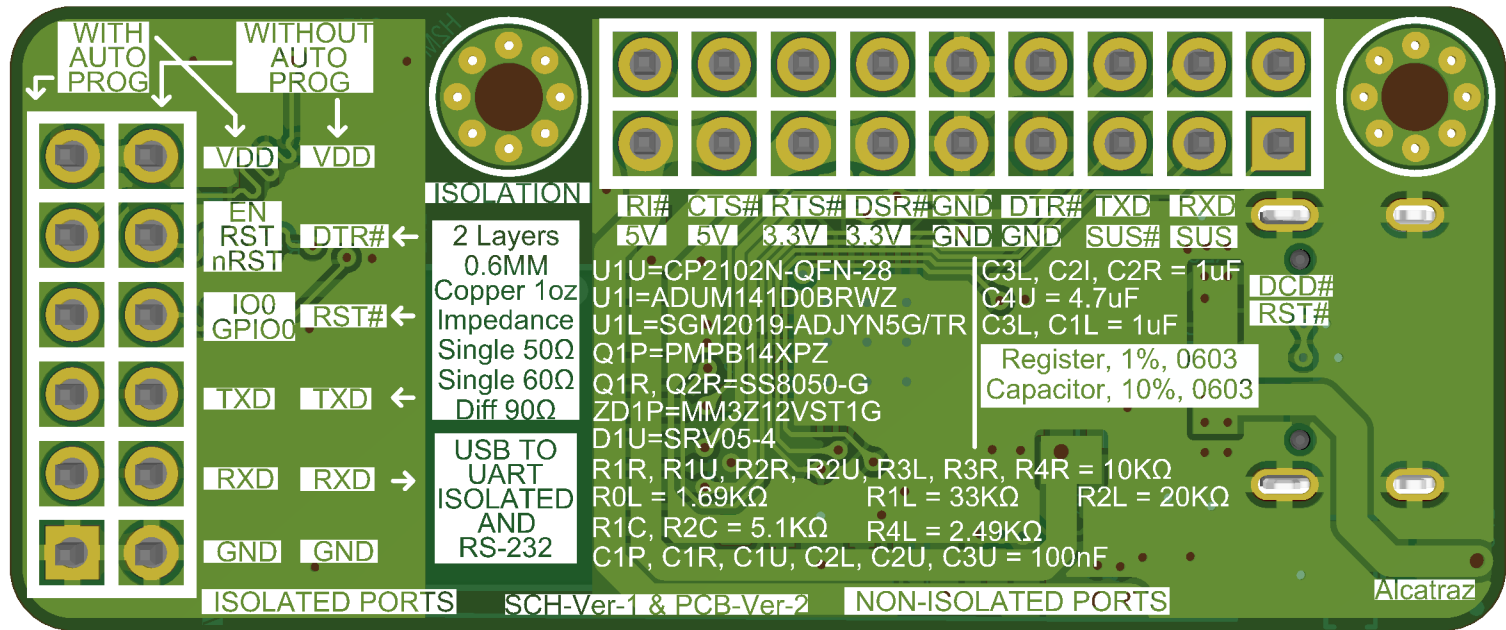
Name	CATHODE
Short name	K
Pin number	1
Silkscreen	thick line / dot
assembly view	dot (•)

Title: =ProjectTitle		Author:
Size: A3		Approved:
Unit: mm		Edited: 19-11-2024
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CONFIDENTIAL	
My Company	
Address Line 1	
Address Line 2	
Address Line 3	
Address Line 4	

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Realistic View BOTTOM



Title: =ProjectTitle			Author:	CONFIDENTIAL My Company Address Line 1 Address Line 2 Address Line 3 Address Line 4	[YOUR LOGO HERE]	6
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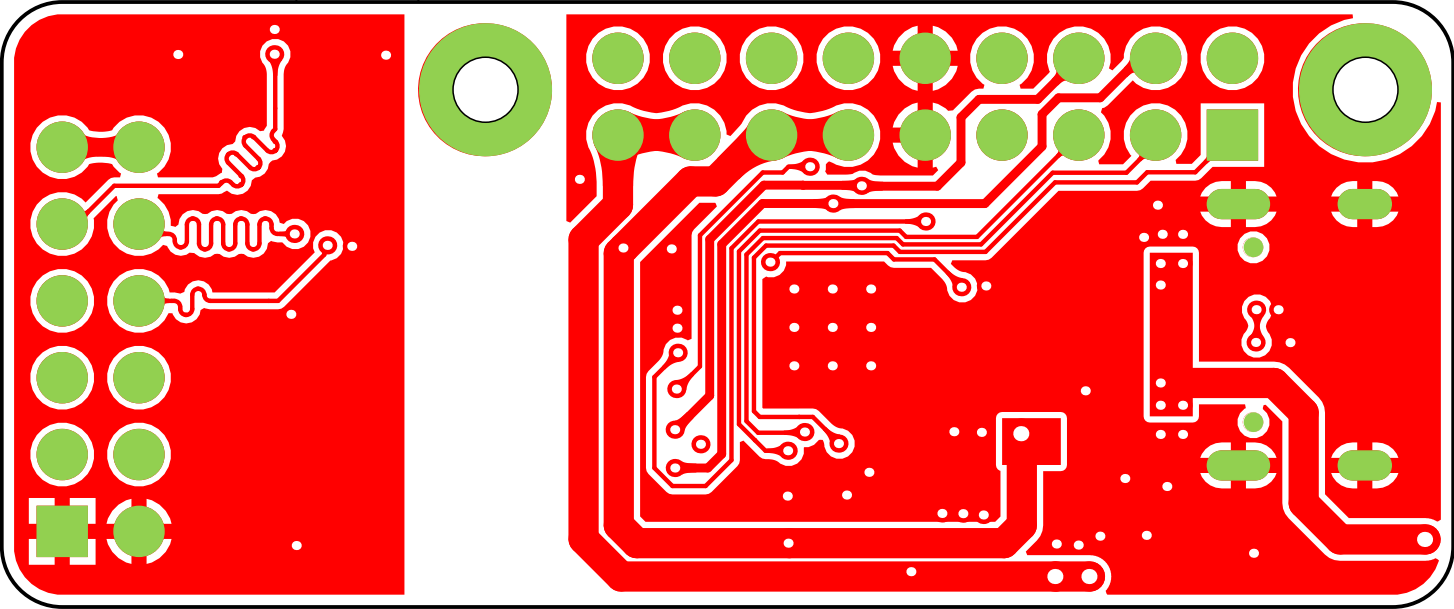
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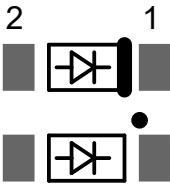
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View from Bottom side (Scale 4:1)



DIODE ORIENTATION

Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()



Name	CATHODE
Short name	K
Pin number	1
Silkscreen	thick line / dot
assembly view	dot (•)

Title: =ProjectTitle		Author:	CONFIDENTIAL	[YOUR LOGO HERE]	6	
Size: A3		Approved:				My Company
Unit: mm	Prj: =ProjectTitle	Edited: 19-11-2024				
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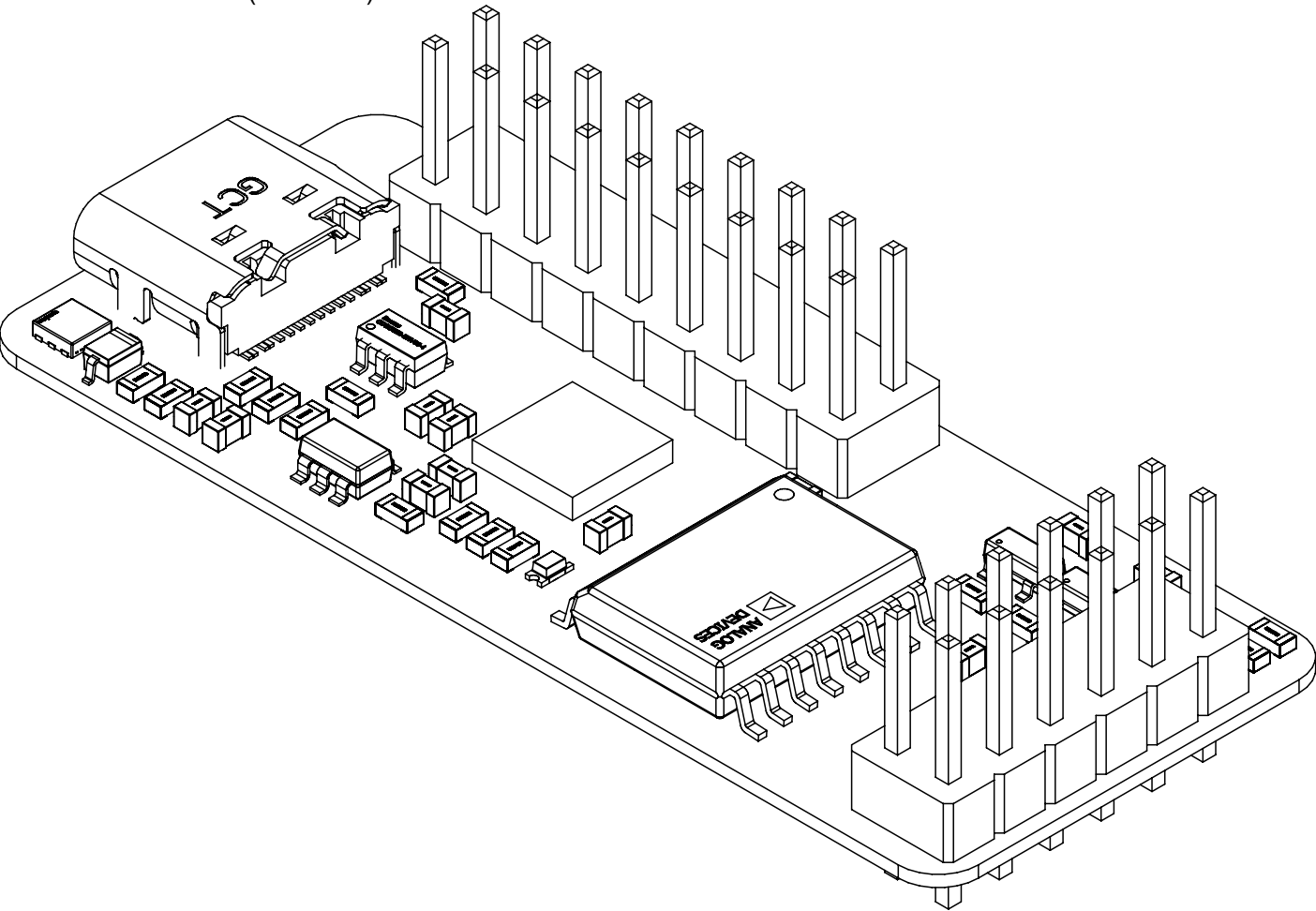
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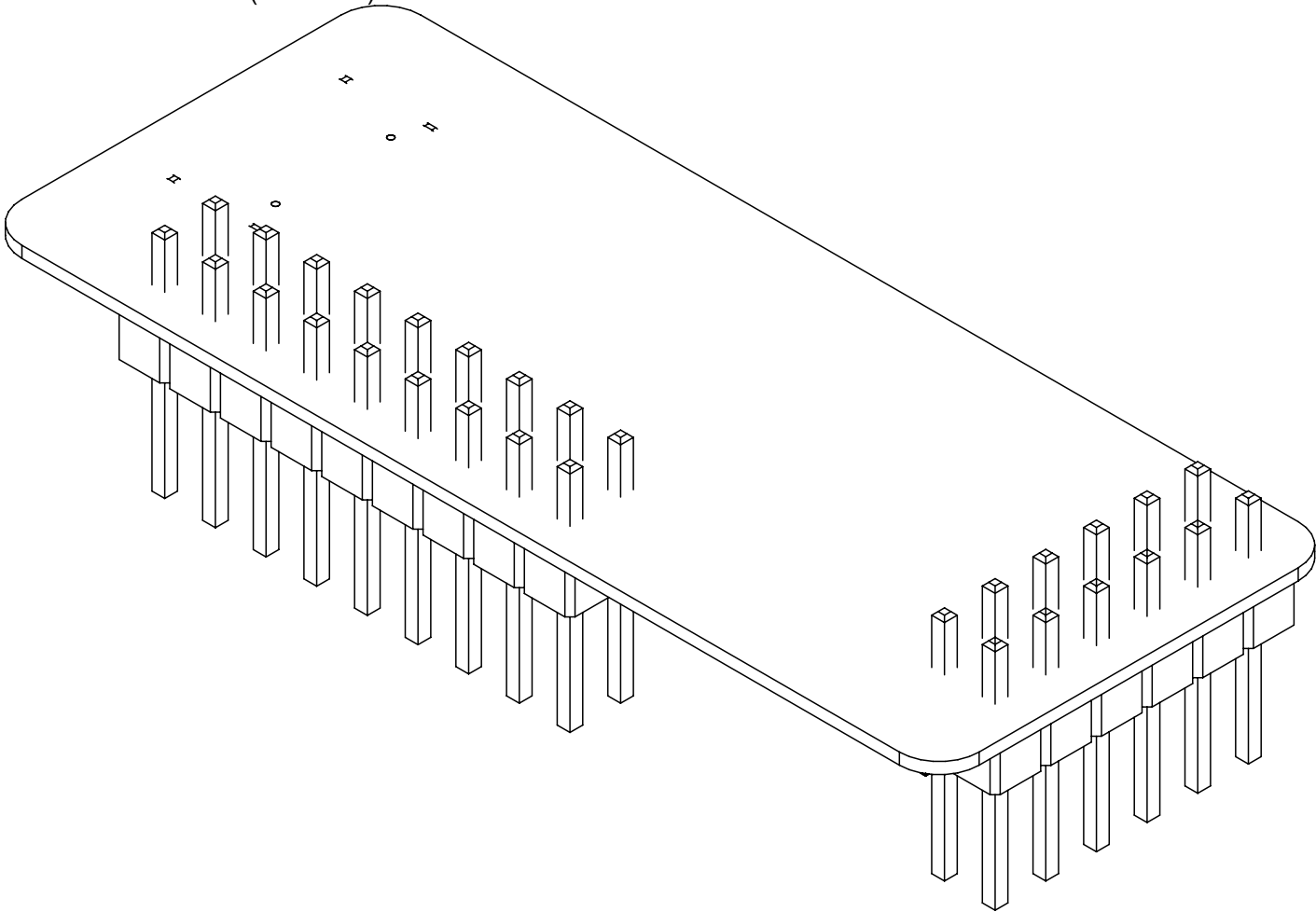
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View from Front side (Scale 4:1)



View from Back side (Scale 4:1)



Title: =ProjectTitle			Author:	<div>CONFIDENTIAL</div> <div>My Company</div> <div>Address Line 1</div> <div>Address Line 2</div> <div>Address Line 3</div> <div>Address Line 4</div> <div>[YOUR LOGO HERE]</div>	6
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Unit: mm			Variant: [No Variations]		
Date: 19-11-2024 09:56			SW version: 24.10.1.45		
AMSheet 8 of 9					
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2	<div>Bill Of Materials</div> <table><tr><th>Line #</th><th>Description</th><th>Designator</th><th>Quantity</th><th>Manufacturer Part Number 1</th><th>Part Number</th><th>Layer</th></tr><tr><td></td><td>Ceramic Capacitor, Multilayer, Ceramic, 6.3V, 10% +Tol, 10% -Tol, X7R, 15% TC, 1uF, Surface Mount, 0603</td><td>C1I</td><td>1</td><td>C0603C105K9PACTU</td><td></td><td></td></tr><tr><td></td><td>Multilayer Ceramic Capacitor, 10 uF, 10 V, ± 10%, X5R, 0603 [1608 Metric]</td><td>C1L</td><td>1</td><td></td><td></td><td></td></tr><tr><td></td><td>Multilayer Ceramic Capacitor, 0.1 uF, 10 V, ± 10%, X7R, 0603 [1608 Metric]</td><td>C1P, C1U, C2L, C2U</td><td>4</td><td>C0603C104K8RAC7867</td><td></td><td></td></tr><tr><td></td><td>Cap Cer 0.1UF 6.3V X7R 0603</td><td>C1R, C3U</td><td>2</td><td>KGM15AR70J104KM</td><td></td><td></td></tr><tr><td></td><td>Ceramic Capacitor, Multilayer, Ceramic, 10V, 10% +Tol, 10% -Tol, X5R, 15% TC, 1uF, 0603</td><td>C2I, C2R</td><td>2</td><td></td><td></td><td></td></tr><tr><td></td><td>Multilayer Ceramic Capacitor, 10 uF, 6.3 V, ± 10%, X5R, 0603 [1608 Metric]</td><td>C3L</td><td>1</td><td>CL10A106KQ8NNNC</td><td></td><td></td></tr><tr><td></td><td>Multilayer Ceramic Capacitor, 4.7 uF, 10 V, ± 10%, X5R, 0603 [1608 Metric]</td><td>C4U</td><td>1</td><td>CL10A475KP8NNNC</td><td></td><td></td></tr><tr><td></td><td>LED 0603 YELLOW SMD</td><td>D1L</td><td>1</td><td></td><td></td><td></td></tr><tr><td></td><td>TVS DIODE 5V 15V SOT23-6</td><td>D1U</td><td>1</td><td></td><td></td><td></td></tr><tr><td></td><td>Fuse PPTC SMD 0603</td><td>F1P</td><td>1</td><td></td><td></td><td></td></tr><tr><td></td><td>USB Connector Type C SMT 16 Pin (Power pins joints = 12 pins)</td><td>J1C</td><td>1</td><td></td><td></td><td></td></tr><tr><td></td><td></td><td>J2C</td><td>1</td><td></td><td></td><td></td></tr><tr><td></td><td></td><td>J3C</td><td>1</td><td></td><td></td><td></td></tr><tr><td></td><td>PMPB14XPZ</td><td>Q1P</td><td>1</td><td>PMPB14XPZ</td><td></td><td></td></tr><tr><td></td><td>Trans Gp Bjt NPN 25V 1.5A 300MW 3-PIN SOT-23 T/r</td><td>Q1R, Q2R</td><td>2</td><td>SS8050-G</td><td></td><td></td></tr><tr><td></td><td>Surface Mount Thick Film Chip Resistor 0603 Case 1.69K Ohms 1% Tolerance 100 PPM</td><td>R0L</td><td>1</td><td>MCR03EZPFX1691</td><td></td><td></td></tr><tr><td></td><td>SMD Chip Resistor, 5.1 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose</td><td>R1C, R2C</td><td>2</td><td>CRCW06035K10FKEA</td><td></td><td></td></tr><tr><td></td><td>Res Thick Film 0603 33K Ohm 1% 0.1W(1/10W) ±100ppm/C Pad SMD Automotive T/R</td><td>R1L</td><td>1</td><td>ERJ-3EKF3302V</td><td></td><td></td></tr><tr><td></td><td>SMD Chip Resistor, 100 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose</td><td>R1P</td><td>1</td><td>AC0603FR-07100KL</td><td></td><td></td></tr><tr><td></td><td>SMD Chip Resistor, 10 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose</td><td>R1R, R1U, R2R, R2U, R3L, R3R, R4R</td><td>7</td><td>RC0603FR-0710KL</td><td></td><td></td></tr><tr><td></td><td>SMD Chip Resistor, 20 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose</td><td>R2L</td><td>1</td><td>CR0603-FX-2002ELF</td><td></td><td></td></tr><tr><td></td><td>Res Thick Film 0603 2.49K Ohm 1% 1/10W ±100ppm/°C Molded SMD SMD Paper T/R</td><td>R4L</td><td>1</td><td></td><td></td><td></td></tr><tr><td></td><td>DGTL ISO 3750VRMS 4CH GP 16SOIC</td><td>U1I</td><td>1</td><td></td><td></td><td></td></tr><tr><td></td><td>LDO U-Reg Adj 0, 3A SOT23-5</td><td>U1L</td><td>1</td><td></td><td></td><td></td></tr><tr><td></td><td>Single-Chip USB to UART Bridge, 1024 Bytes EEPROM, -40 to 85 degC, 28-pin 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Metric]	C4U	1	CL10A475KP8NNNC				LED 0603 YELLOW SMD	D1L	1					TVS DIODE 5V 15V SOT23-6	D1U	1					Fuse PPTC SMD 0603	F1P	1					USB Connector Type C SMT 16 Pin (Power pins joints = 12 pins)	J1C	1						J2C	1						J3C	1					PMPB14XPZ	Q1P	1	PMPB14XPZ				Trans Gp Bjt NPN 25V 1.5A 300MW 3-PIN SOT-23 T/r	Q1R, Q2R	2	SS8050-G				Surface Mount Thick Film Chip Resistor 0603 Case 1.69K Ohms 1% Tolerance 100 PPM	R0L	1	MCR03EZPFX1691				SMD Chip Resistor, 5.1 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose	R1C, R2C	2	CRCW06035K10FKEA				Res Thick Film 0603 33K Ohm 1% 0.1W(1/10W) ±100ppm/C Pad SMD Automotive T/R	R1L	1	ERJ-3EKF3302V				SMD Chip Resistor, 100 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose	R1P	1	AC0603FR-07100KL				SMD Chip Resistor, 10 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General Purpose	R1R, R1U, R2R, R2U, R3L, R3R, R4R	7	RC0603FR-0710KL				SMD Chip Resistor, 20 kOhm, ± 1%, 100 mW, 0603 [1608 Metric], Thick Film, General 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6	<div>Please consider LCSC (立创商城) as our first supplier BOM FOR REFERENCE ONLY ALWAYS REFER TO THE LATEST EXCEL BOM PROVIDED</div> <table><tr><td colspan="2">Title: =ProjectTitle</td><td colspan="2">Author:</td><td colspan="2">CONFIDENTIAL</td></tr><tr><td>Size: A3</td><td rowspan="5">Prj: =ProjectTitle</td><td colspan="2">Approved:</td><td colspan="2">My Company</td></tr><tr><td>Unit: mm</td><td colspan="2">Edited: 19-11-2024</td><td colspan="2">Address Line 1</td></tr><tr><td>Date: 19-11-2024 09:56 AM</td><td colspan="2">Variant: [No Variations]</td><td colspan="2">Address Line 2</td></tr><tr><td colspan="2">Git Hash: 404 [No modification]</td><td colspan="2">SW version: 24.10.1.45</td><td colspan="2">Address Line 3</td></tr><tr><td colspan="2">File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\USB-UART-ISO-CP2102\PCB ASSEMBLY USB-UAR</td><td colspan="2"></td><td colspan="2">Address Line 4</td></tr></table> <div>YOUR LOGO HERE</div>								Title: =ProjectTitle		Author:		CONFIDENTIAL		Size: A3	Prj: =ProjectTitle	Approved:		My Company		Unit: mm	Edited: 19-11-2024		Address Line 1		Date: 19-11-2024 09:56 AM	Variant: [No Variations]		Address Line 2		Git Hash: 404 [No modification]		SW version: 24.10.1.45		Address Line 3		File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\USB-UART-ISO-CP2102\PCB ASSEMBLY USB-UAR				Address Line 4		6																																																																																																																																																											
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