

Note:

- 1 Text element with square border.
2. Text element with no border
- 3 Text element with circle border

THE INFORMATION CONTAINED IN
THIS DRAWING IS THE SOLE
PROPERTY OF
. ANY REPRODUCTION IN PART OR
AS A WHOLE WITHOUT THE
WRITTEN PERMISSION OF IS

PROPRIETARY AND CONFIDENTIAL

		UNLESS OTHERWISE SPECIFIED:		NAME	DATE				
		DIMENSIONS ARE IN INCHES TOLERANCES:	DRAWN		06-12-2024				
		FRACTIONAL ±	CHECKED			TITLE			
		ANGULAR: MACH± BEND ±	ENG APPR.						
		TWO PLACE DECIMAL ±	MFG APPR.						
		THREE PLACE DECIMAL ±							
		INTERPRET GEOMETRIC TOLERANCING PER:	Q.A.						
		MATERIAL	COMMENTS:			SIZE	DWG. NO.		
		FINISH							
NEXT ASSY	USED ON								
APPLICATION		DO NOT SCALE DRAWING				SCALE: 1:1	WEIGHT:	SHEET 1 OF 1	

PCB MANUFACTURING SPECIFICATIONS

"=ProjectTitle"

SPECIFICATIONS

NOTE #	NOTE
1	ALL SPECIFICATIONS REFERENCED ARE OF THE REVISION SPECIFIED IN THE TITLE BLOCK
2	SUPPLIER SHALL NOT MODIFY THE DESIGN OR APPROVED STACK-UP WITHOUT WRITTEN PERMISSION
3	ALL MATERIALS SHALL BE RoHS COMPLIANT AND FINAL PRODUCT SHALL BE ACCEPTABLE TO USE IN RoHS ASSEMBLY. RoHS LOGO SHALL BE MARKED IN SILKSCREEN INK BY THE SUPPLIER WHERE INDICATED BY THE TEXT "PLACE MARKINGS HERE"
4	COPPER FOIL: REFER TO LAYER STACK LEGEND FOR Cu THICKNESS DETAILS. ALL Cu THICKNESSES ARE FINISHED AND INCLUDE BASE FOIL PLUS Cu PLATING ON PLATED LAYERS
5	ELECTRICAL TEST: ALL PRINTED CIRCUITS SHALL BE 100% ELECTRICALLY TESTED FOR OPENS/SHORTS USING PROVIDED NETLIST. REJECTED PRINTED BOARDS MUST BE CLEARLY MARKED WITH NON-CONDUCTIVE, PERMANENT INK.
6	MARKINGS: VENDOR MARKING AND DATE/LOT CODES SHALL BE LOCATED ON THE BOARD IN THE RESERVED AREA AS SPECIFIED IN THE GERBER LAYER "PCBM_NOTES" BY THE TEXT "PLACE MARKINGS HERE".
7	MARKINGS: THE SIDE ONTO WHICH PLACE THE MARKINGS IS AT THE SUPPLIER DISCRESSION UNLESS OTHERWISE NOTED ONTO THE LAYER "PCBM_NOTES"
8	SUPPLIER SHALL CHECK PCBM_NOTES LAYER BEFORE ASKING FOR CLARIFICATIONS
9	MANUFACTURE TENTED/PLUGGED VIAS AS SPECIFIED IN THE GERBER FILES

SPECIFICATIONS

LENGHT	20.00mm
WIDTH	48.00mm
LAYERS	2
MATERIAL	FR-4
MATERIAL MIN TG	130-140
TRACK WIDTH/CLEARANCE	10 mils / 10 mils
THICKNESS	0.6mm
COPPER THICKNESS	35um (1oz)
SOLDERMASK	YES, TOP AND BOTTOM
SOLDERMASK COLOR	GREEN
SILKSCREEN	YES, TOP AND BOTTOM
SILKSCREEN COLOR	WHITE
SURFACE FINISH	HASL LEAD FREE
GOLD FINGERS	NO
CHAMFERING	YES
IMPEDANCE CONTROL	YES
HALF-CUT/CASTELLATED HOLES	NO
BURIED/BLIND VIAS	NO
VIAS FILLED WITH RESIN	NO
CARBON MASK	NO
COUNTERSINKS/COUNTERBORES	NO
Z-AXIS MILLING	NO
PEELABLE SOLDERMASK	NO

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
Surface Material	Top Solder	0.02mm	Solder Resist	Solder Mask	GTS
Copper	Top Layer	0.04mm		Signal	GTL
Core		0.50mm	FR-4	Dielectric	
Copper	Bottom Layer	0.04mm		Signal	GBL
Surface Material	Bottom Solder	0.02mm	Solder Resist	Solder Mask	GBS
	Bottom Overlay			Legend	GBO

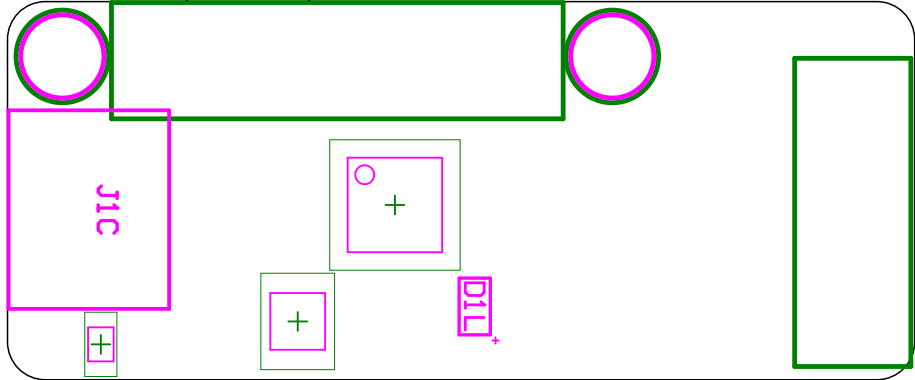
Total thickness: 0.60mm

NON-COPPER LAYER THICKNESS FOR REFERENCE ONLY
LAYERS OF TYPE "INTERNAL PLANE" ARE NEGATIVE

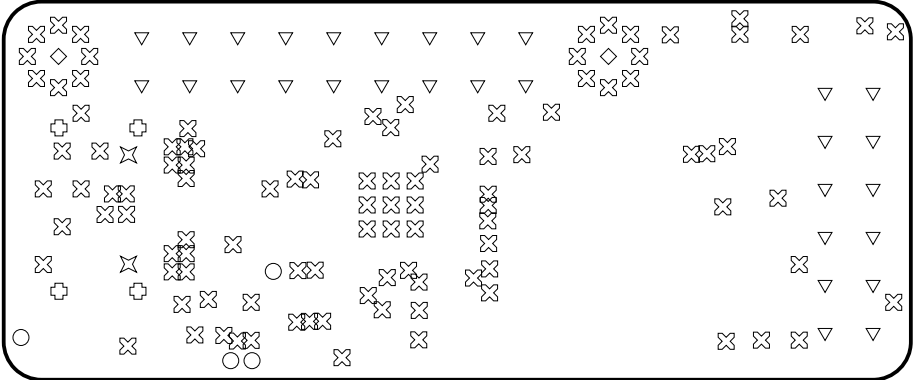
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			Approved:			
Size: A3	Prj: =ProjectTitle		Edited: 22-11-2024			
Unit: mm			Variant: [No Variations]			
Date: 06-12-2024 02:50			SW version: 24.10.1.45			
FMSheet 1 of 3						
Git Hash: 431 [No modification]						
File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\USB-UART-ISO-CP2102\PCB MANUFACTURING U						

[YOUR LOGO HERE]

Mechanical 1 (Scale 5:2)



Drill Drawing View (Scale 5:2)



Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
x	105	0.30mm	Есть	
o	4	0.50mm	Есть	
+	4	0.60mm	Есть	
x	2	0.65mm	Нет	
v	30	1.00mm	Есть	
d	2	2.20mm	Есть	
	147 Total			

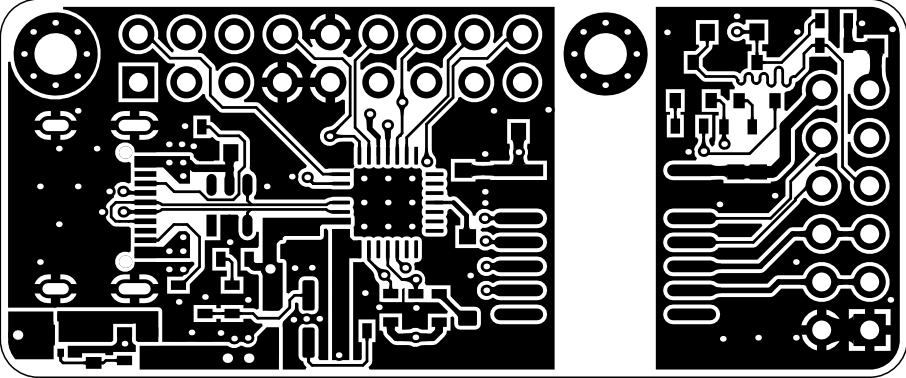
Dimensions (Scale 5:2)



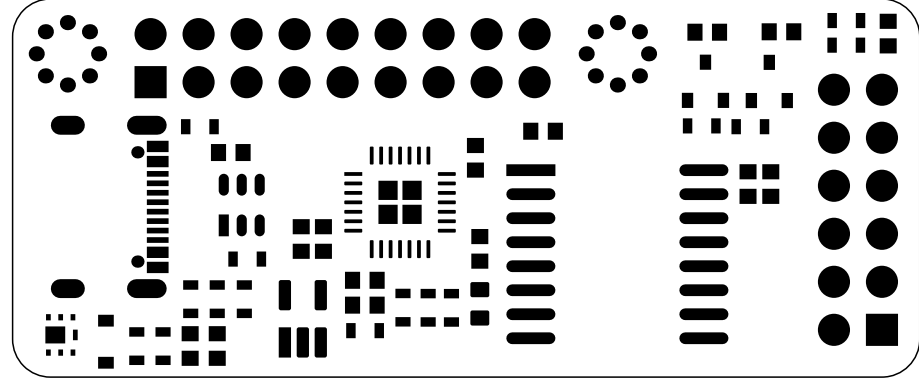
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Unit: mm			Variant: [No Variations]			
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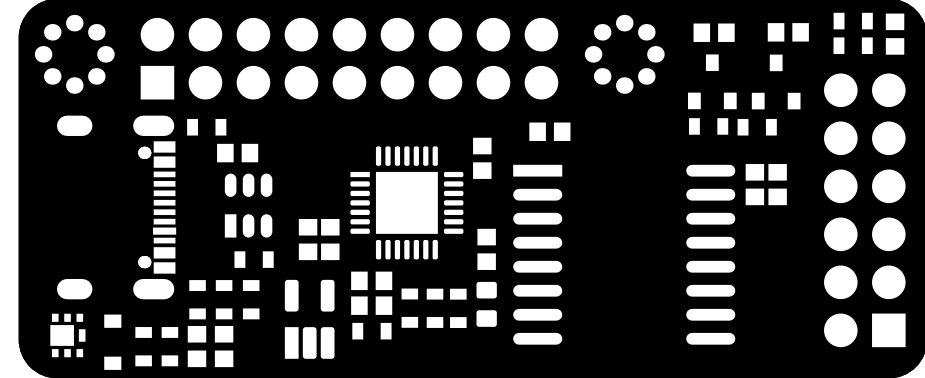
Top Layer (Scale 5:2)



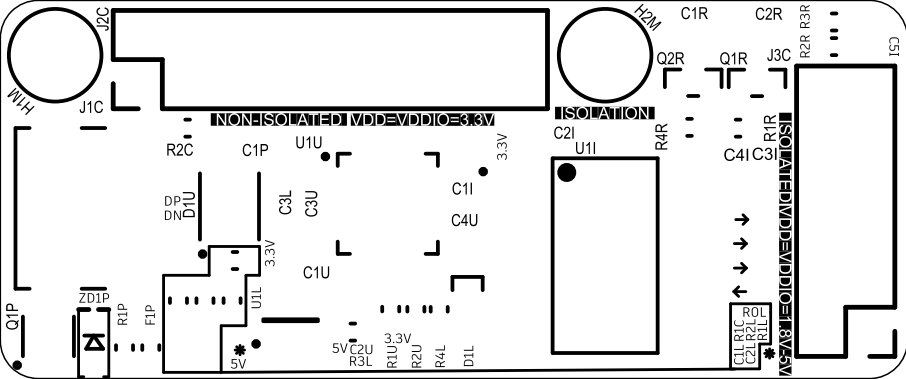
Top Paste (Scale 5:2)



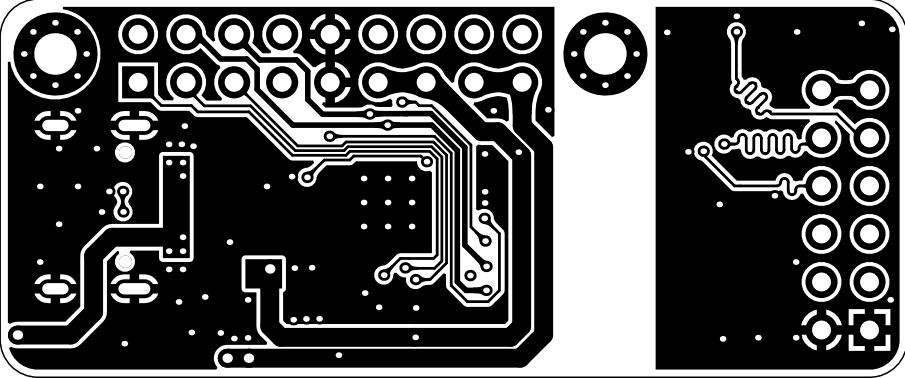
Top Solder (Scale 5:2)



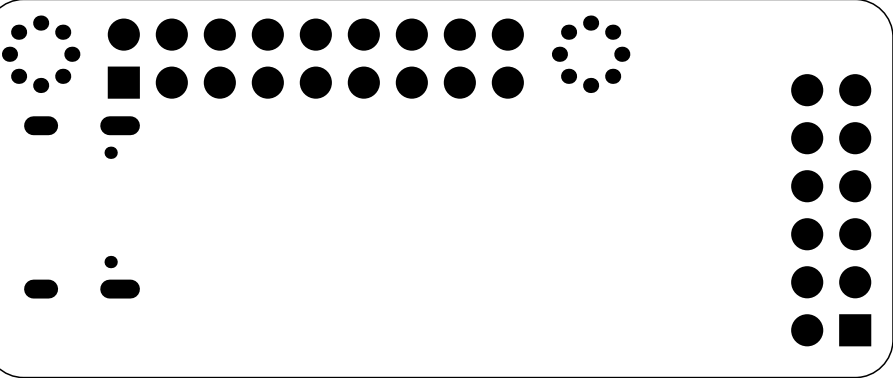
Top Overlay (Scale 5:2)



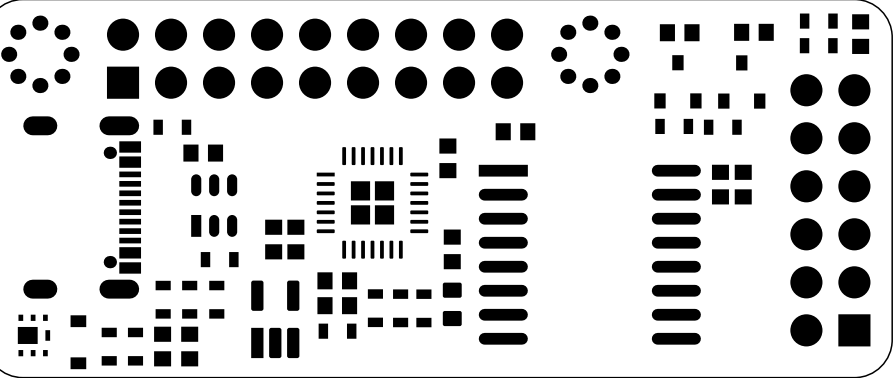
Bottom Layer (Scale 5:2)



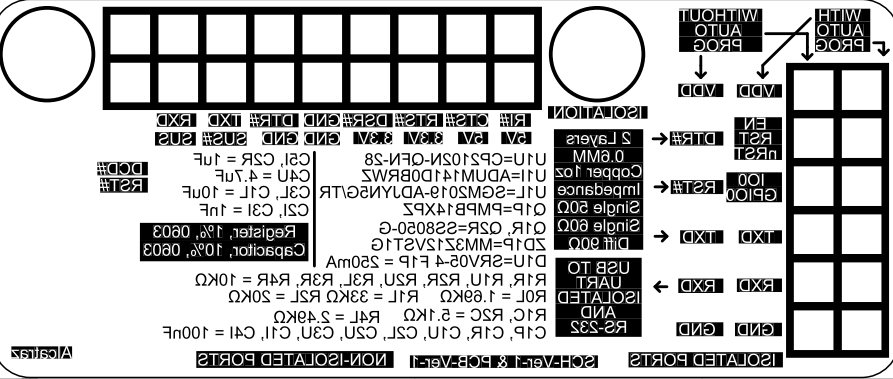
Bottom Paste (Scale 5:2)



Top Paste (Scale 5:2)



Bottom Overlay (Scale 5:2)



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PCB ASSEMBLY SPECIFICATIONS

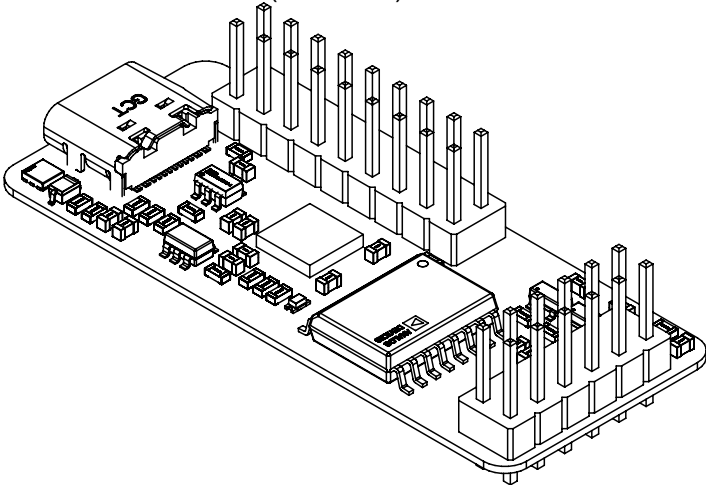
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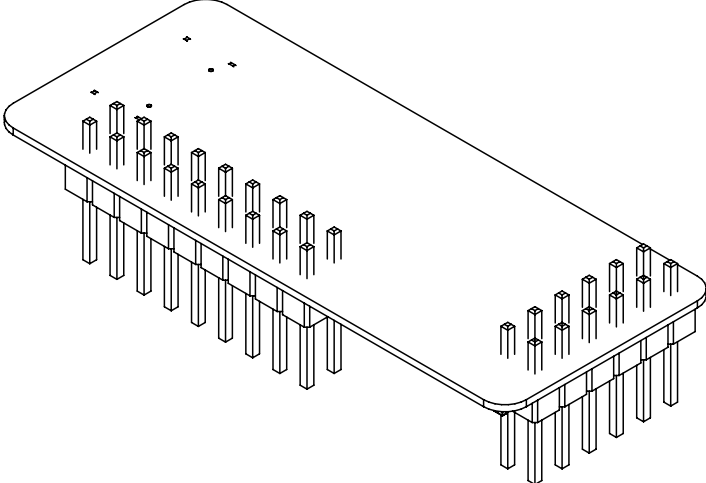
SPECIFICATIONS

NOTE #	NOTE
1	ALL SPECIFICATIONS REFERENCED SHALL BE OF THE LATEST REVISION UNLESS OTHERWISE NOTED
2	SUPPLIER SHALL NOT MODIFY THE DESIGN WITHOUT WRITTEN PERMISSION
3	REFER TO EXCEL BOM FOR UP-TO-DATE INFORMATION
4	THE BOM IN THIS DOCUMENT IS PURELY AN AID TO ASSEMBLY OPERATIONS AND MAY NOT HAVE THE MOST UP-TO-DATE DATA OR ALL APPROVED COMPONENT ALTERNATIVE.

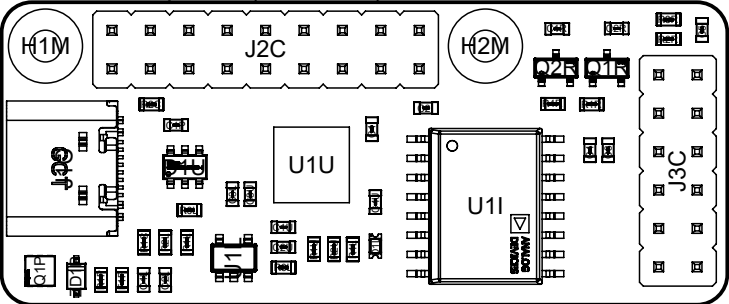
View from Front side (Scale 2:1)



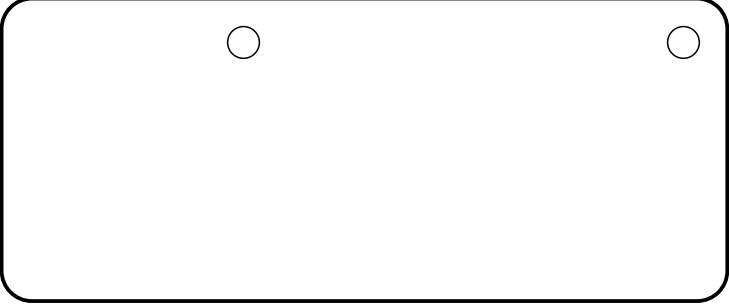
View from Back side (Scale 2:1)



View from Top side (Scale 2:1)

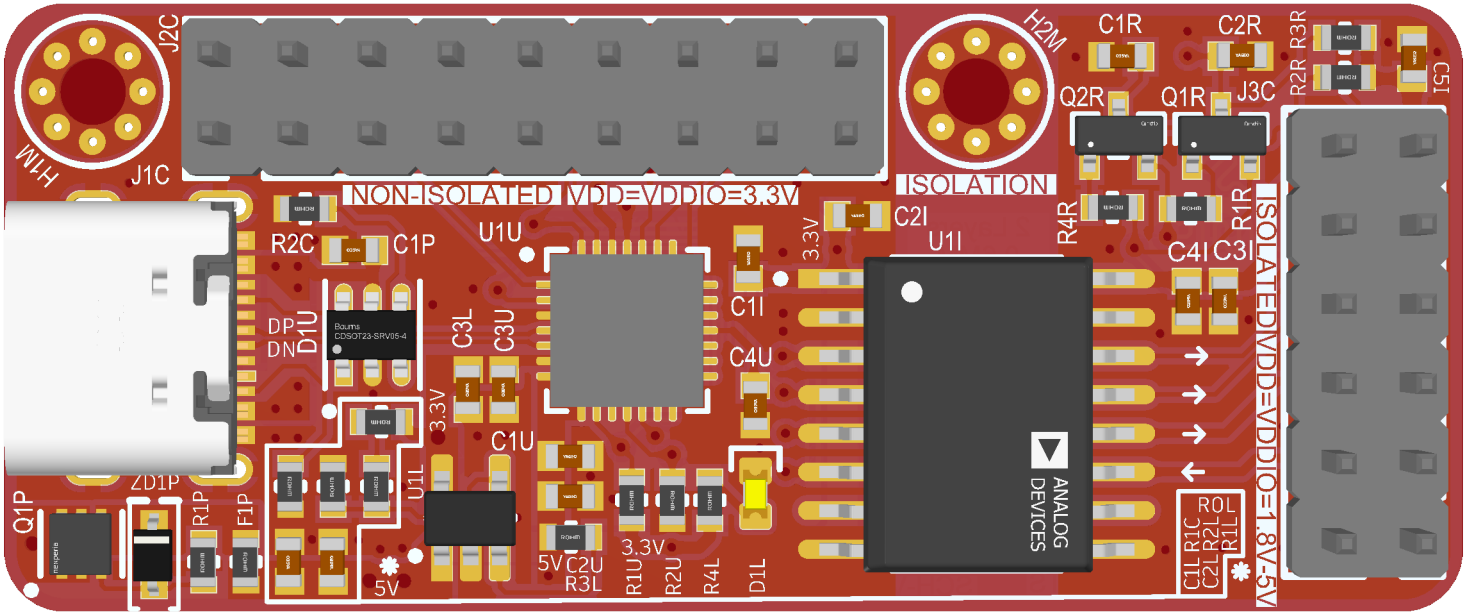


View from Bottom side (Scale 2:1)



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Prj: =ProjectTitle			Approved:		
Size: A3	Unit: mm		Edited: 22-11-2024		
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File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\USB-UART-ISO-CP2102\PCB ASSEMBLY USB-UAR					

Realistic View TOP



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Unit: mm			Prj: =ProjectTitle		Edited: 22-11-2024			
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					Address Line 3			
					Address Line 4			
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[YOUR LOGO HERE]

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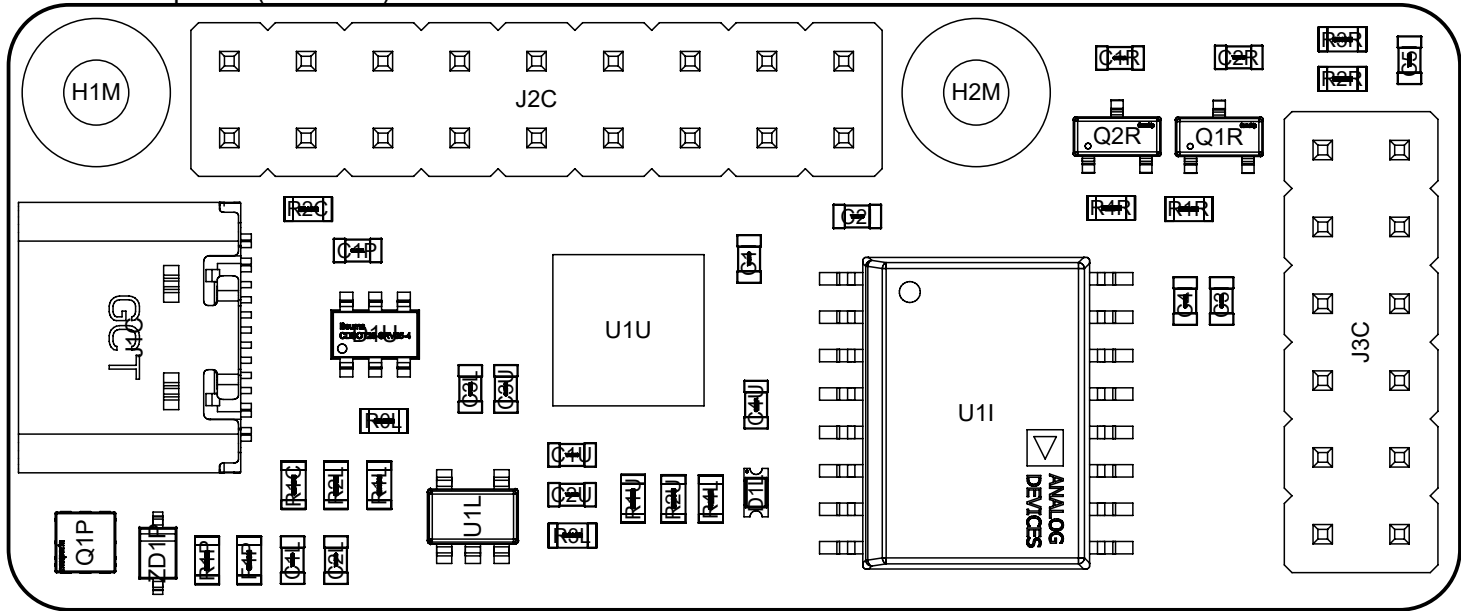
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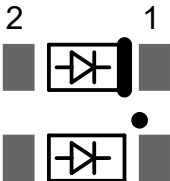
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View from Top side (Scale 4:1)



DIODE ORIENTATION

Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()

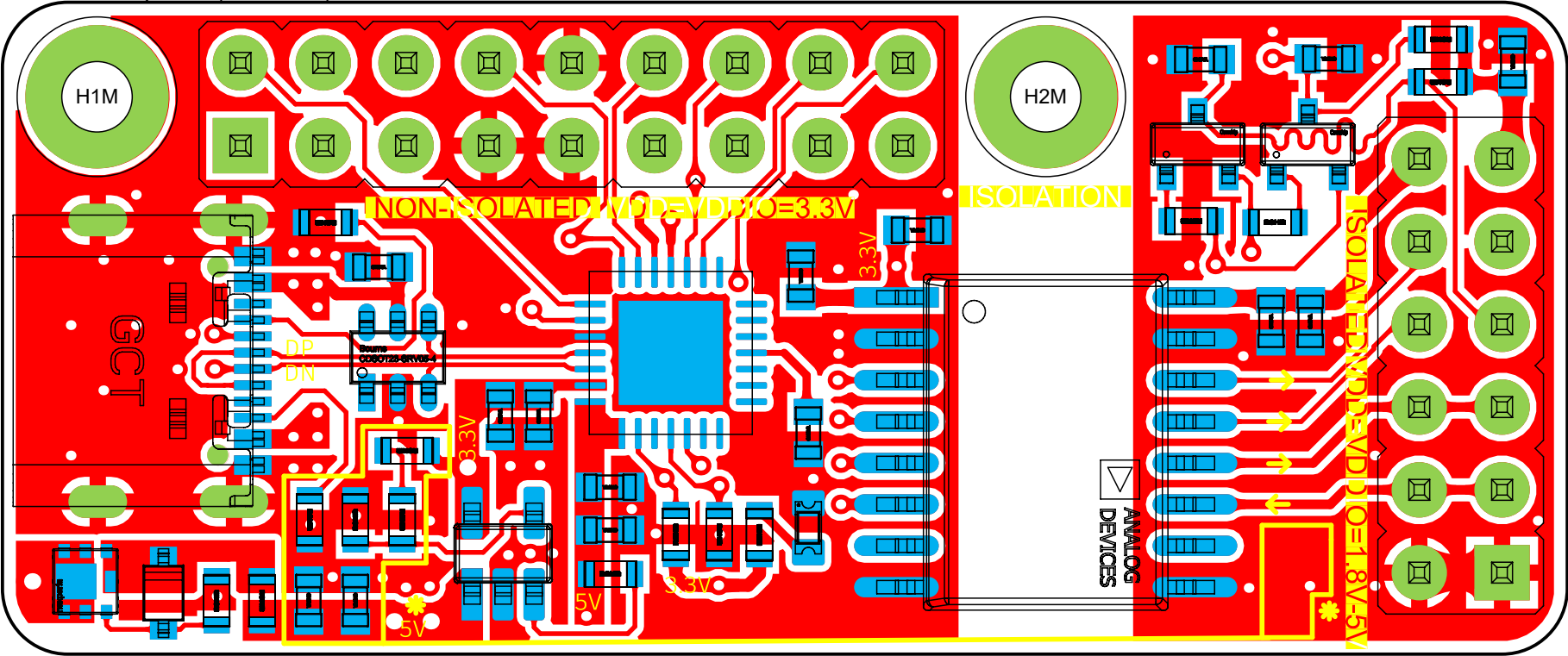


Name	CATHODE
Short name	K
Pin number	1
Silkscreen	thick line / dot
assembly view	dot (•)

Title: =ProjectTitle			Author:	CONFIDENTIAL My Company Address Line 1 Address Line 2 Address Line 3 Address Line 4 [YOUR LOGO HERE]	6
			Approved:		
Size: A3	Prj: =ProjectTitle		Edited: 22-11-2024		
Unit: mm			Variant: [No Variations]		
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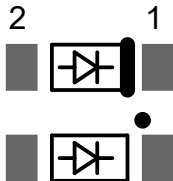
[YOUR LOGO HERE]

View from Top side (Scale 5:1)



DIODE ORIENTATION

Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()



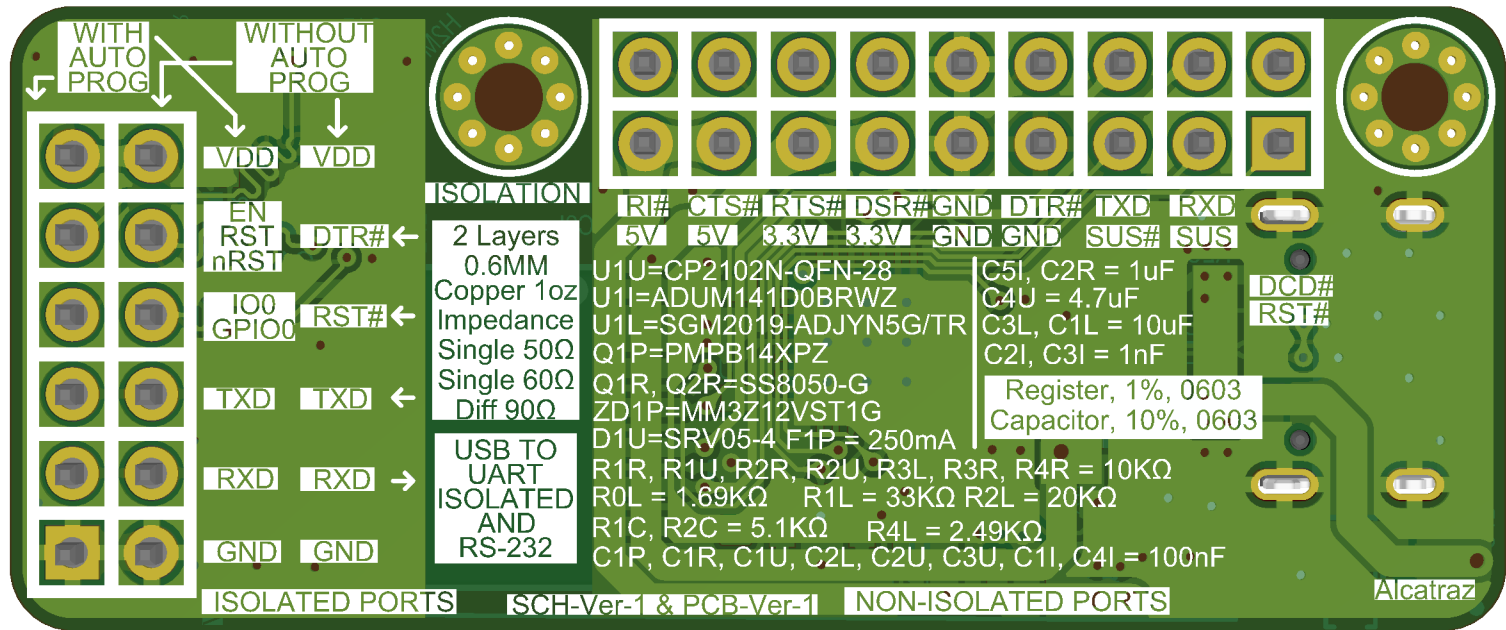
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Short name	K
Pin number	1
Silkscreen	thick line / dot
assembly view	dot (•)

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CONFIDENTIAL
My Company
Address Line 1
Address Line 2
Address Line 3
Address Line 4

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Realistic View BOTTOM



Title: =ProjectTitle			Author:		CONFIDENTIAL		6
Size: A3			Approved:		My Company Address Line 1 Address Line 2 Address Line 3 Address Line 4		
Unit: mm			Prj: =ProjectTitle				
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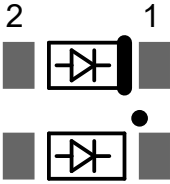
[YOUR LOGO
HERE]

View from Bottom side (Scale 4:1)



DIODE ORIENTATION

Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()



Name	CATHODE
Short name	K
Pin number	1
Silkscreen	thick line / dot
assembly view	dot (•)

Title: =ProjectTitle		Author:	CONFIDENTIAL
Size: A3		Approved:	
Unit: mm		Edited: 22-11-2024	
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Date: 06-12-2024 02:50 PM		SW version: 24.10.1.45	[YOUR LOGO HERE]
Git Hash: 431 [No modification]			
File: C:\Users\desktop\Documents\Project Files\Altium\Projects\Project - Development\USB-UART-ISO-CP2102\PCB ASSEMBLY USB-UART-ISO-CP2102			

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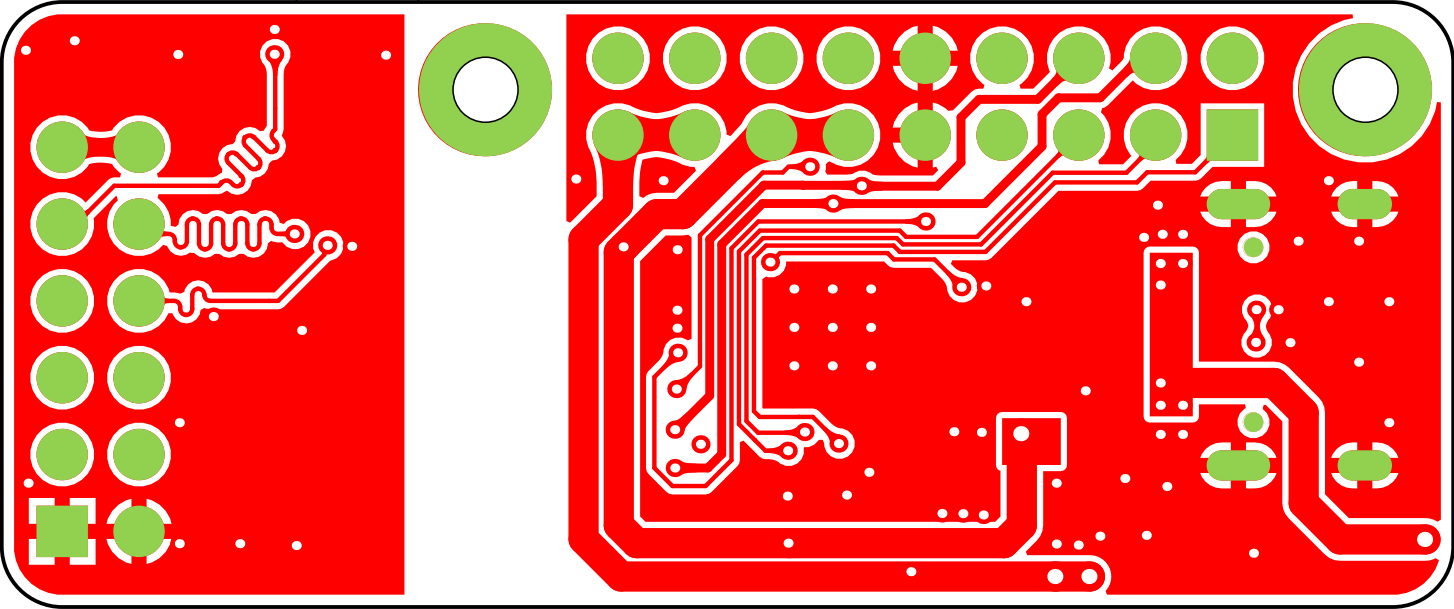
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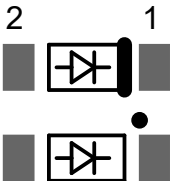
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View from Bottom side (Scale 4:1)



DIODE ORIENTATION

Name	ANODE
Short name	A
Pin number	2
Silkscreen	thin line
assembly view	no dot ()



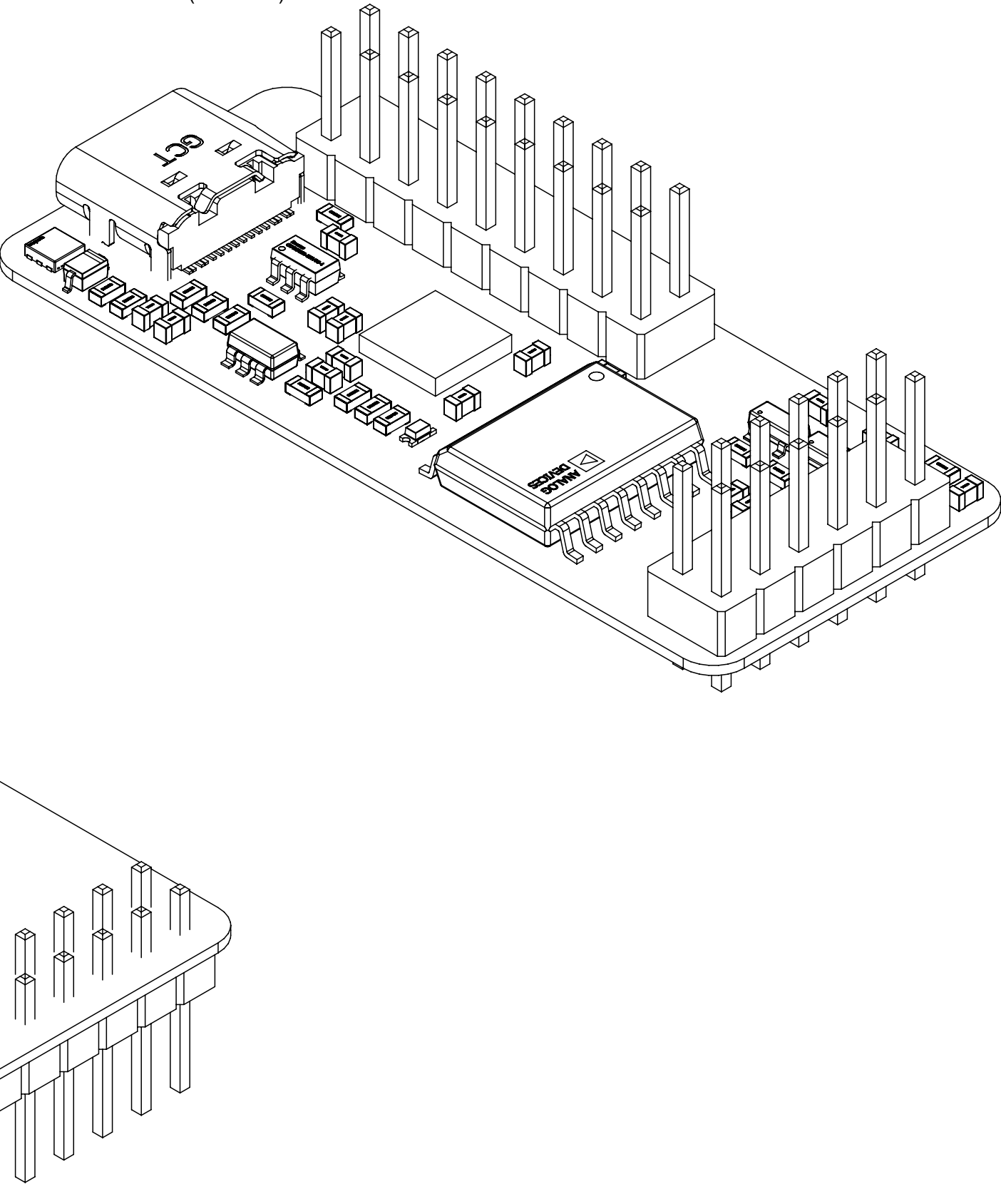
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Short name	K
Pin number	1
Silkscreen	thick line / dot
assembly view	dot (•)

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Size: A3		Approved:
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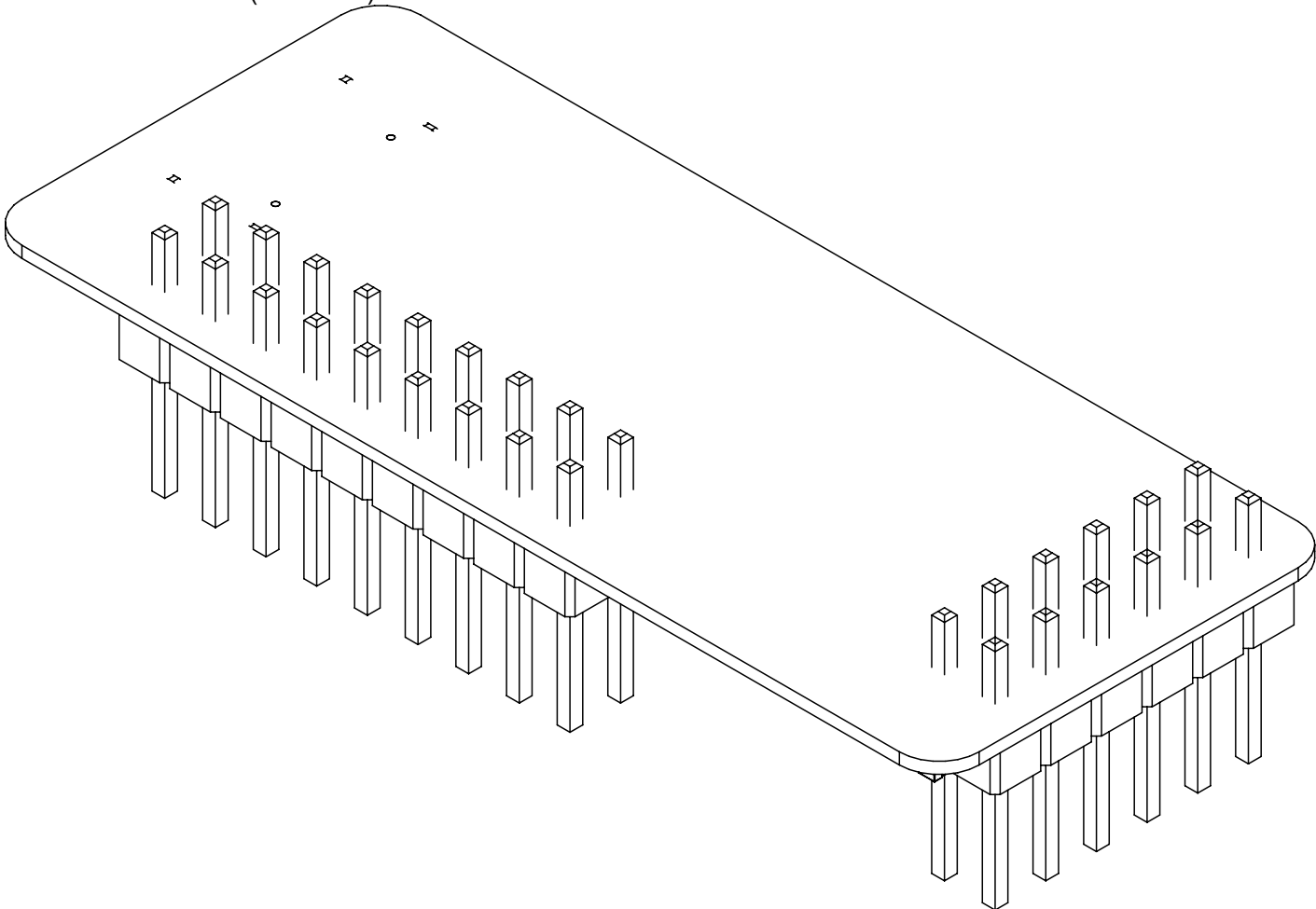
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Address Line 1
Address Line 2
Address Line 3
Address Line 4

[YOUR LOGO HERE]

View from Front side (Scale 4:1)



View from Back side (Scale 4:1)



Title: =ProjectTitle			Author:	<div>CONFIDENTIAL</div> <div>My Company</div> <div>Address Line 1</div> <div>Address Line 2</div> <div>Address Line 3</div> <div>Address Line 4</div> <div>[YOUR LOGO HERE]</div>	6
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Unit: mm	Prj: =ProjectTitle		Edited: 22-11-2024		
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EMSheet 8 of 9		SW version: 24.10.1.45	Address Line 4		
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