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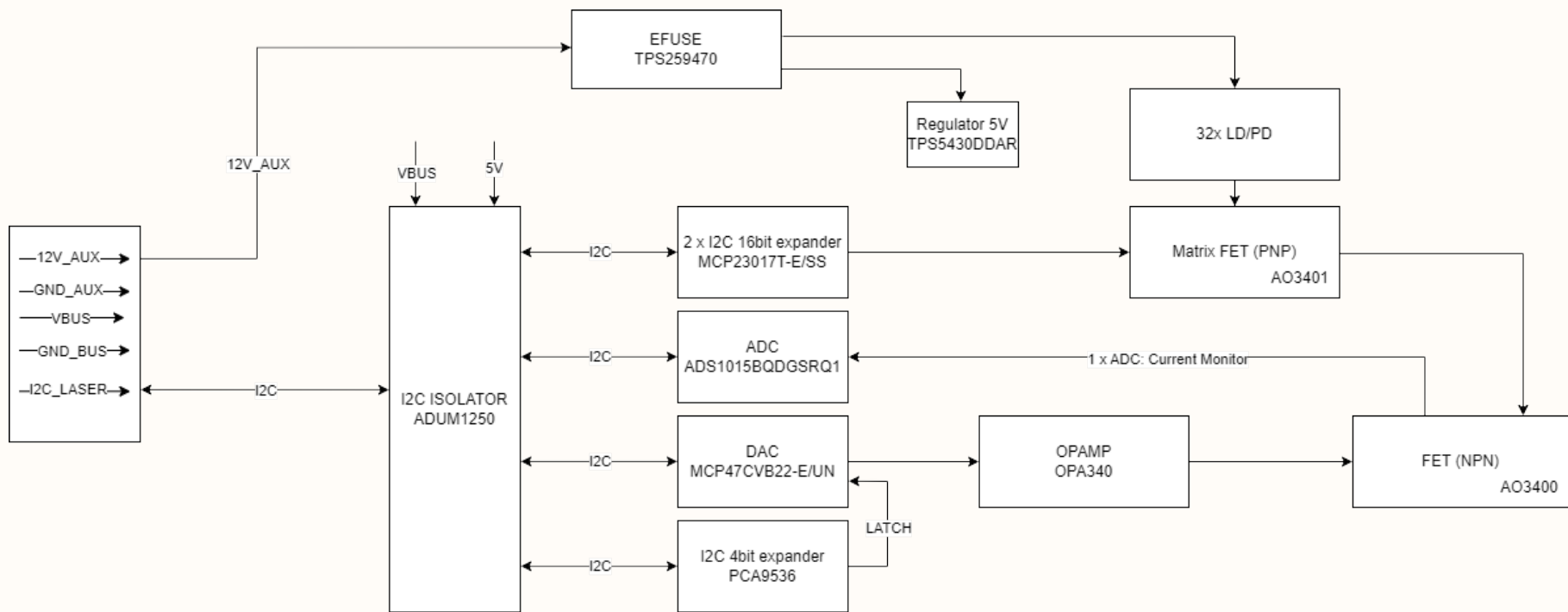
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NANORACK LASER

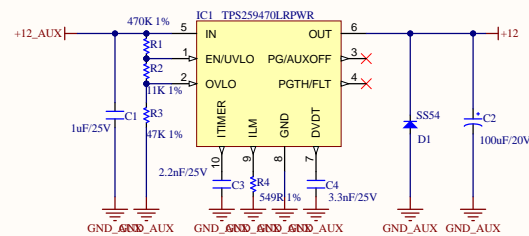
Schematic V1.0.0



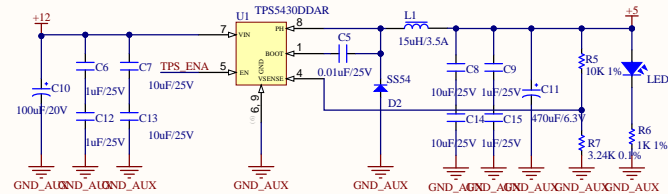
Project: BEE-PC1	Sub-system: NANORACK_LASER.PrjPcb	
Design by: Hung Nguyen D.	Sheet title: INDEX.SchDoc	Sheet 1 of 6
Checked by: Bao Bui Q.	Document No: 1	Size: A3
Approved by: Vu Pham	Revision: V1.0.0	Date: 10/3/2024



Project: BEE-PC1	Sub-system: NANORACK_LASER.PrjPcb	
Design by: Hung Nguyen D.	Sheet title: BLOCK_DIAGRAM.SchDoc	Sheet 2 of 6
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Approved by: Vu Pham	Revision: V1.0.0	Date: 10/3/2024



EFUSE



REGULATOR

$$SR\left(\frac{V}{mS}\right) = \frac{V_{IN}(V)}{t_R(ms)}$$

$$C_{advdt}(pF) = \frac{2000}{SR \left(\frac{V}{m_s} \right)}$$

$$I_{INRUSH}(mA) = SR(V/ms) \times C_{OUT}(\mu F)$$

$$R_{LIM}(\Omega) = \frac{3334}{I_{LIM}(A)}$$

$$R_2 = \frac{R_1 \times 1,221}{V_{OUT} - 1,221}$$



Project: BEE-PC1

Sub-system: NANORACK_LASER.PrjPcb

Design by: Hung Nguyen D.

Sheet title: EFUSE_AND_REGULATOR.SchDoc Sheet 3 of 6

Checked by: **Bao Bui Q.**

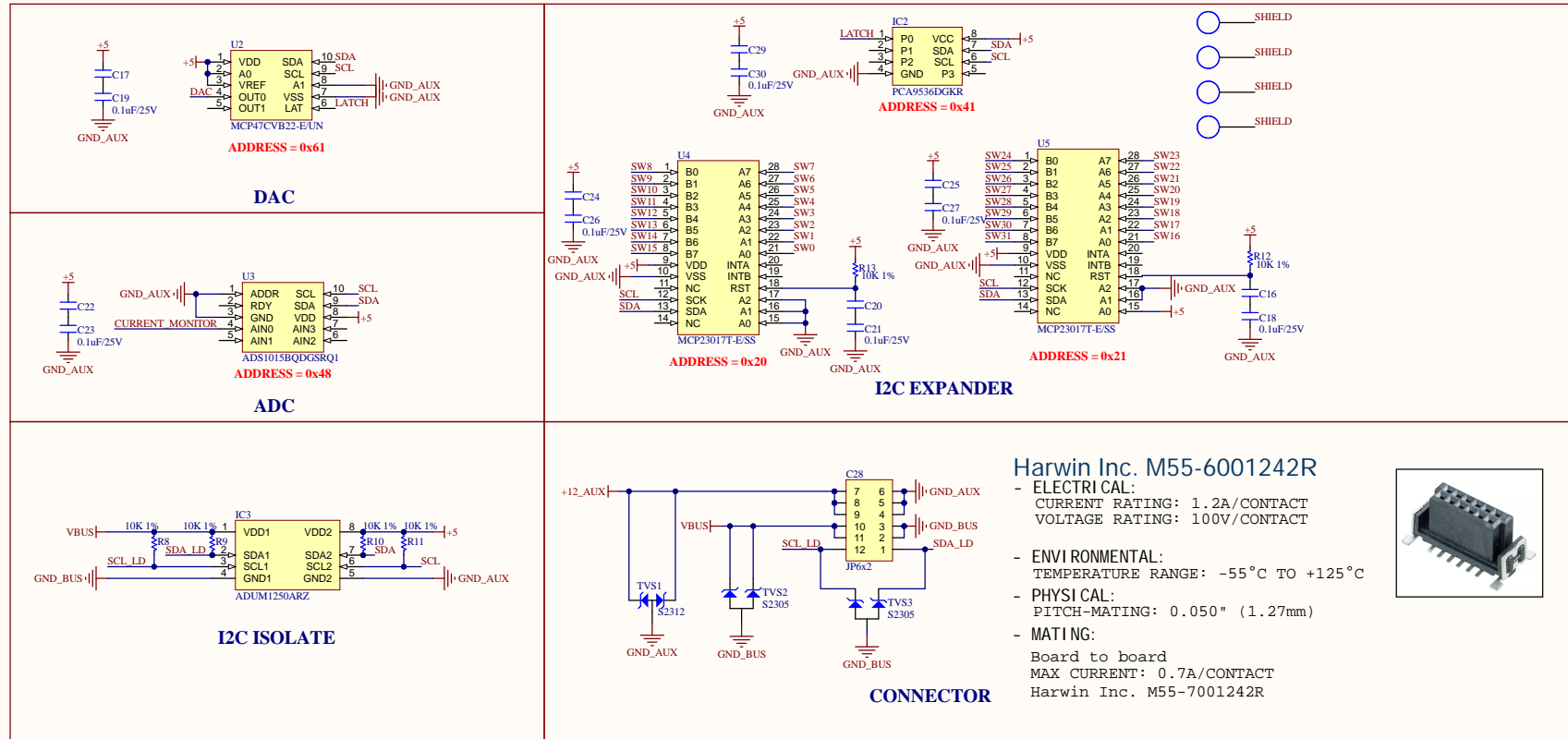
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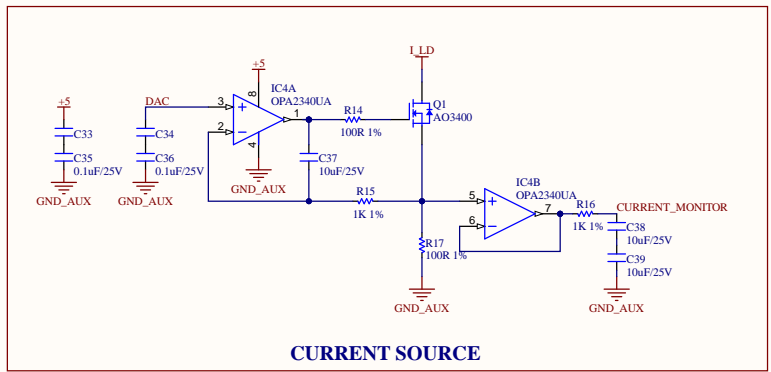
Approved by: **Vu Pham**

Revision: V1.0.0

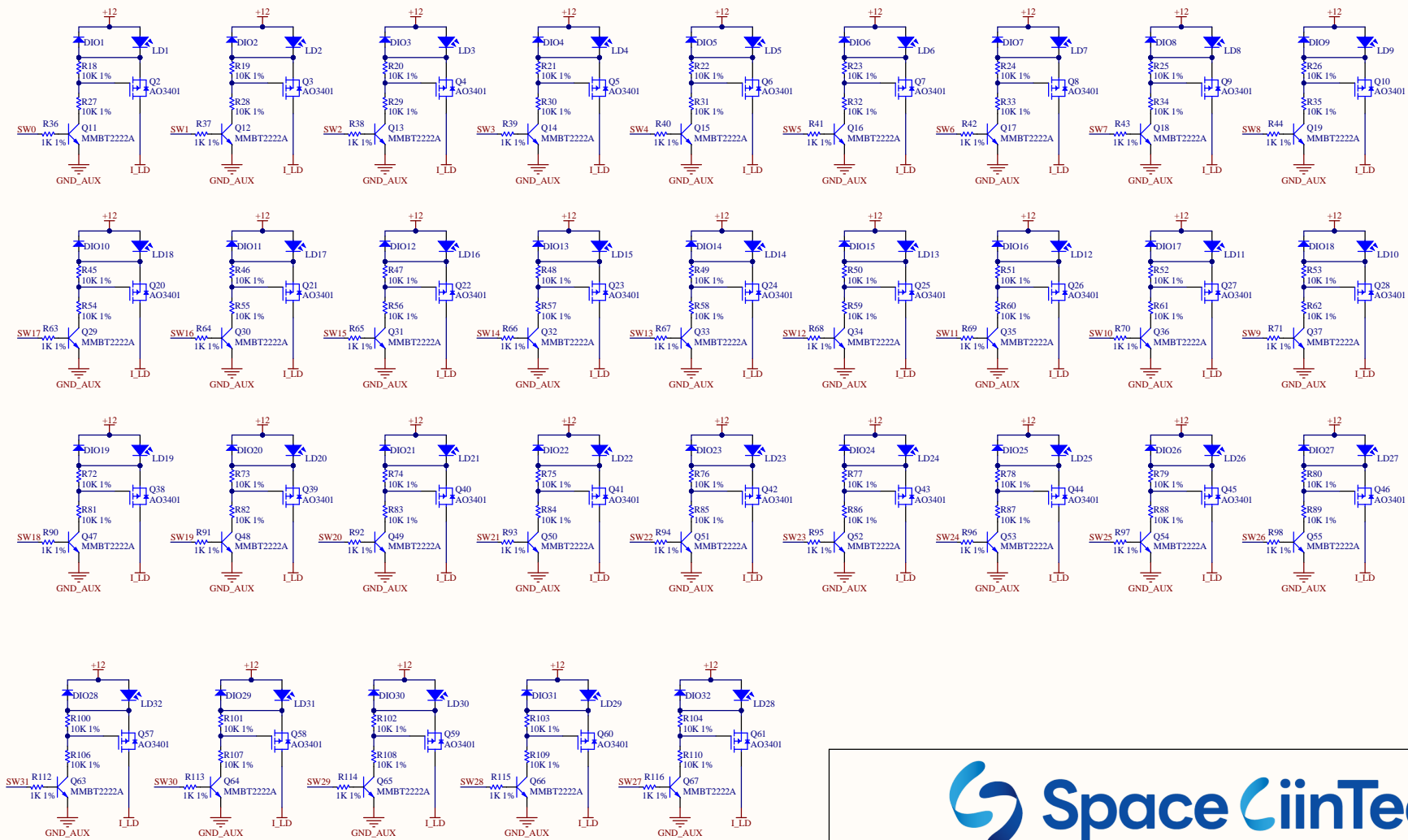
Date: 10/3/2024



Project: BEE-PC1	Sub-system: NANORACK_LASER.PrjPcb	
Design by: Hung Nguyen D.	Sheet title: COMMUNICATION.SchDoc	Sheet 4 of 6
Checked by: Bao Bui Q.	Document No: 4	Size: A3
Approved by: Vu Pham	Revision: V1.0.0	Date: 10/3/2024



Project: BEE-PC1	Sub-system: NANORACK_LASER.PrjPcb	
Design by: Hung Nguyen D.	Sheet title: CURRENT_SOURCE.SchDoc	Sheet 5 of 6
Checked by: Bao Bui Q.	Document No: 5	Size: A3
Approved by: Vu Pham	Revision: V1.0.0	Date: 10/3/2024



Project: BEE-PC1	Sub-system: NANORACK_LASER.PrjPcb	
Design by: Hung Nguyen D.	Sheet title: SWITCHING.SchDoc	Sheet 6 of 6
Checked by: Bao Bui Q.	Document No: 6	Size: A3
Approved by: Vu Pham	Revision: V1.0.0	Date: 10/3/2024