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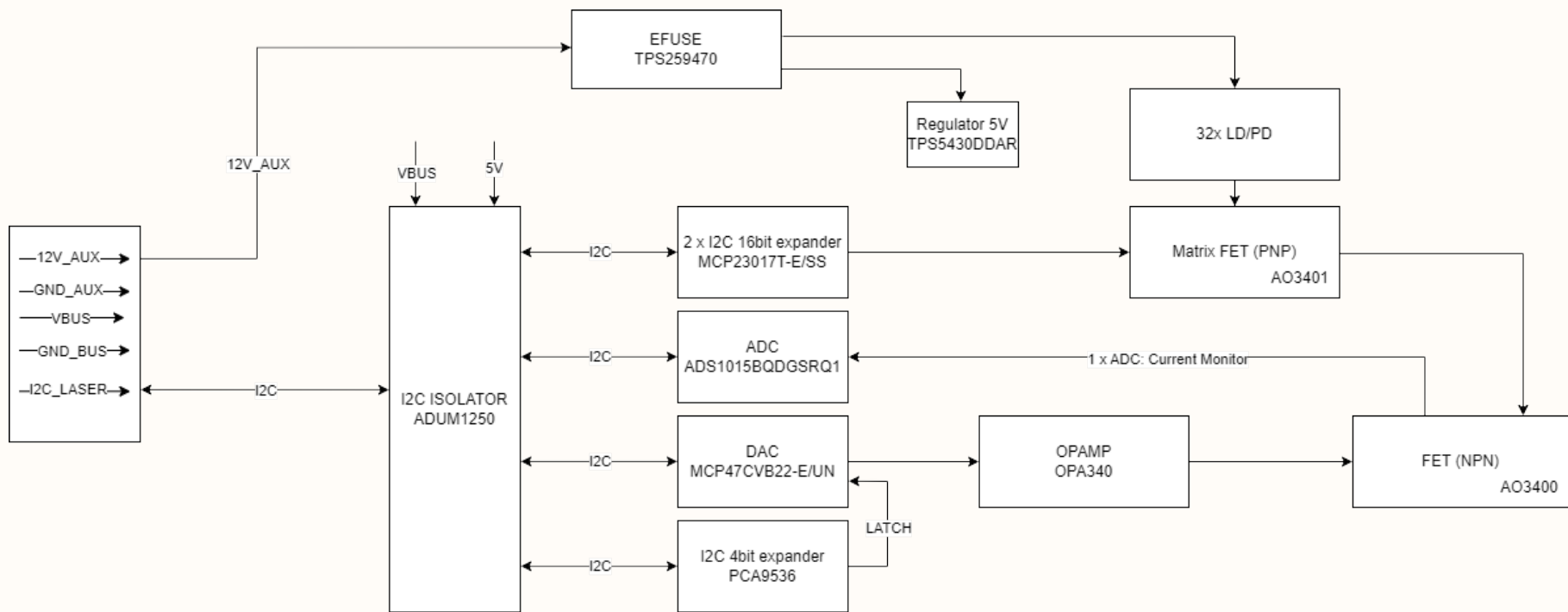
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## NANORACK LASER

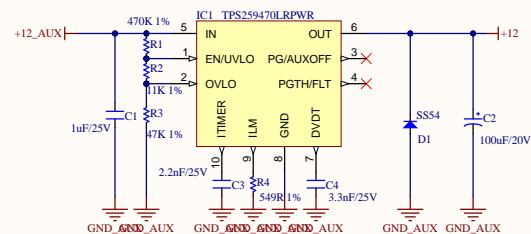
### Schematic V1.0.0



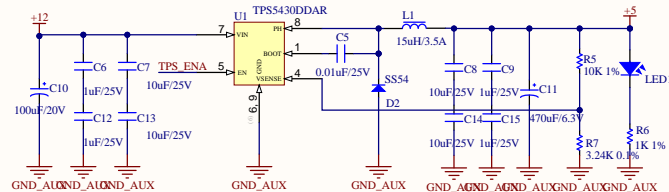
Project: BEE-PC1	Sub-system: NANORACK_LASER.PrjPcb	
Design by: Hung Nguyen D.	Sheet title: INDEX.SchDoc	Sheet 1 of 6
Checked by: Bao Bui Q.	Document No: 1	Size: A3
Approved by: SLT	Revision: V1.0.0	Date: 10/3/2024



Project: <b>BEE-PC1</b>	Sub-system: <b>NANORACK_LASER.PrjPcb</b>	
Design by: <b>Hung Nguyen D.</b>	Sheet title: <b>BLOCK_DIAGRAM.SchDoc</b>	Sheet <b>2</b> of <b>6</b>
Checked by: <b>Bao Bui Q.</b>	Document No: <b>2</b>	Size: <b>A3</b>
Approved by: <b>SLT</b>	Revision: <b>V1.0.0</b>	Date: <b>10/3/2024</b>



EFUSE



REGULATOR

$$SR\left(\frac{V}{ms}\right) = \frac{V_{IN}(V)}{t_R(ms)}$$

$$C_{avdt}(pF) = \frac{2000}{SR\left(\frac{V}{ms}\right)}$$

$$I_{INRUSH}(mA) = SR(V/ms) \times C_{OUT}(uF)$$

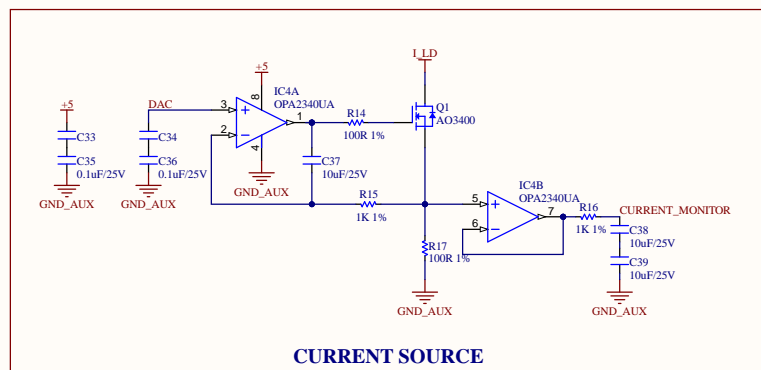
$$R_{LIM}(\Omega) = \frac{3334}{I_{LIM}(A)}$$

$$R_2 = \frac{R_1 \times 1,221}{V_{OUT} - 1,221}$$



Project: <b>BEE-PC1</b>	Sub-system: <b>NANORACK_LASER.PrjPcb</b>	
Design by: <b>Hung Nguyen D.</b>	Sheet title: <b>EFUSE_AND_REGULATOR.SchDoc</b>	Sheet <b>3</b> of <b>6</b>
Checked by: <b>Bao Bui Q.</b>	Document No: <b>3</b>	Size: <b>A3</b>
Approved by: <b>SLT</b>	Revision: <b>V1.0.0</b>	Date: <b>10/3/2024</b>





Project: <b>BEE-PC1</b>	Sub-system: <b>NANORACK_LASER.PrjPcb</b>	
Design by: <b>Hung Nguyen D.</b>	Sheet title: <b>CURRENT_SOURCE.SchDoc</b>	Sheet <b>5</b> of <b>6</b>
Checked by: <b>Bao Bui Q.</b>	Document No: <b>5</b>	Size: <b>A3</b>
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