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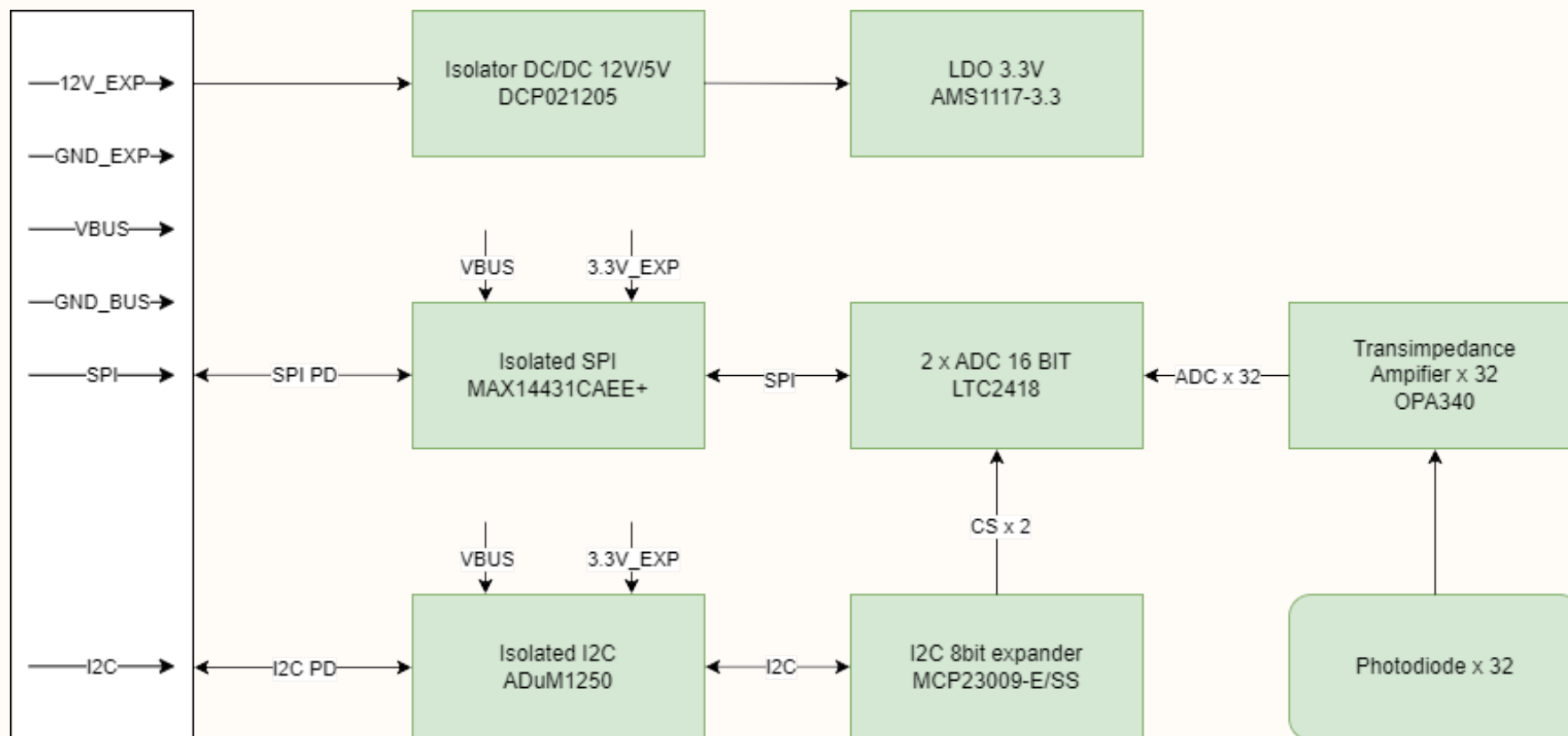
| PAGE | COMPONENT/ FUNCTION |
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NANORACK PHOTODIODE

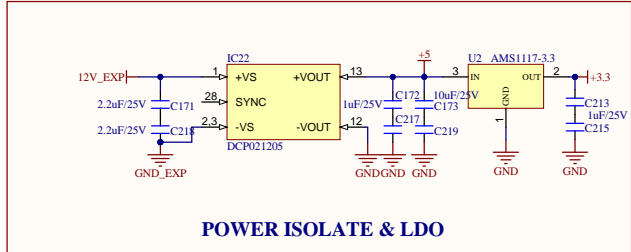
Schematic V1.0.0



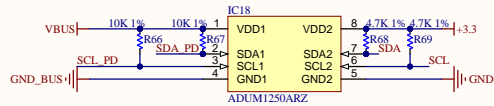
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| Project: BEE-PC1 | Sub-system: NANORACK_PHOTODIODE.PrjPcb | |
| Design by: Hung Nguyen D. | Sheet title: INDEX.SchDoc | Sheet 1 of 6 |
| Checked by: Bao Bui Q. | Document No: 1 | Size: A3 |
| Approved by: Vu Pham | Revision: V1.0.0 | Date: 10/3/2024 |



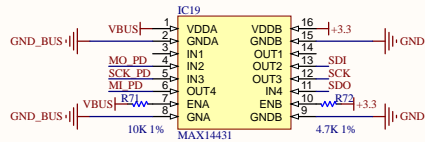
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| Project: BEE-PC1 | Sub-system: NANORACK_PHOTODIODE.PrjPcb | |
| Design by: Hung Nguyen D. | Sheet title: BLOCK_DIAGRAM.SchDoc | Sheet 2 of 6 |
| Checked by: Bao Bui Q. | Document No: 2 | Size: A3 |
| Approved by: Vu Pham | Revision: V1.0.0 | Date: 10/6/2024 |



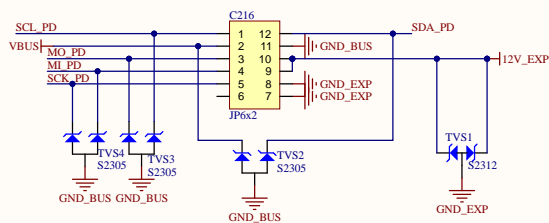
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| Project: BEE-PC1 | Sub-system: NANORACK_PHOTODIODE.PrjPcb | |
| Design by: Hung Nguyen D. | Sheet title: POWER_ISOLATE_AND_LDO.SchDoc | Sheet 3 of 6 |
| Checked by: Bao Bui Q. | Document No: 3 | Size: A3 |
| Approved by: Vu Pham | Revision: V1.0.0 | Date: 10/6/2024 |



ISOLATED I2C



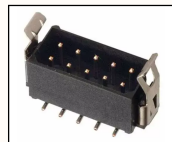
ISOLATED SPI



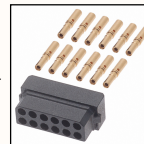
CONNECTOR

Harwin Inc. M80-8281242

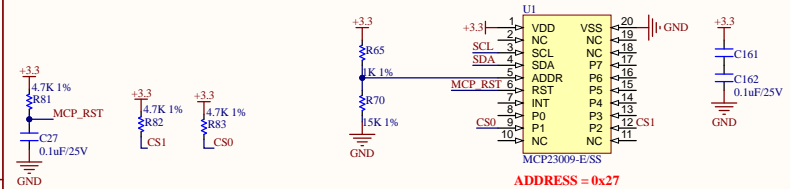
- ELECTRICAL:
CURRENT RATING: 2.2A/CONTACT
VOLTAGE RATING: 800V/CONTACT
- ENVIRONMENTAL:
TEMPERATURE RANGE: -55°C TO +125°C
- PHYSICAL:
PITCH-MATING: 0.079" (2.00mm)
- WIRE:
WIRE SIZE: 24 AWG
MAX CURRENT: 1A per Wire
ISULATION MATERIAL: PTFE
- MATING:
Harwin Inc. M80-8881205



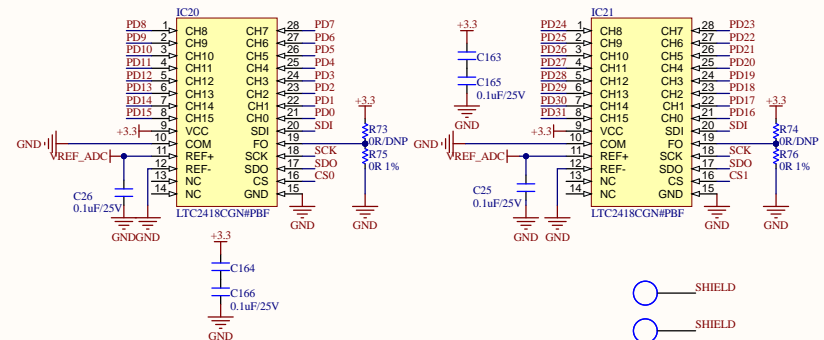
- MATING -



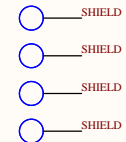
M80-8881205



I2C EXPANDER



2 x ADC 16 CHANNEL



| | | |
|----------------------------------|---|----------------------------|
| Project: BEE-PC1 | Sub-system: NANORACK_PHOTODIODE.PrjPcb | |
| Design by: Hung Nguyen D. | Sheet title: COMMUNICATION.SchDoc | Sheet 4 of 6 |
| Checked by: Bao Bui Q. | Document No: 4 | Size: A3 |
| Approved by: Vu Pham | Revision: V1.0.0 | Date: 10/6/2024 |

A

B

C

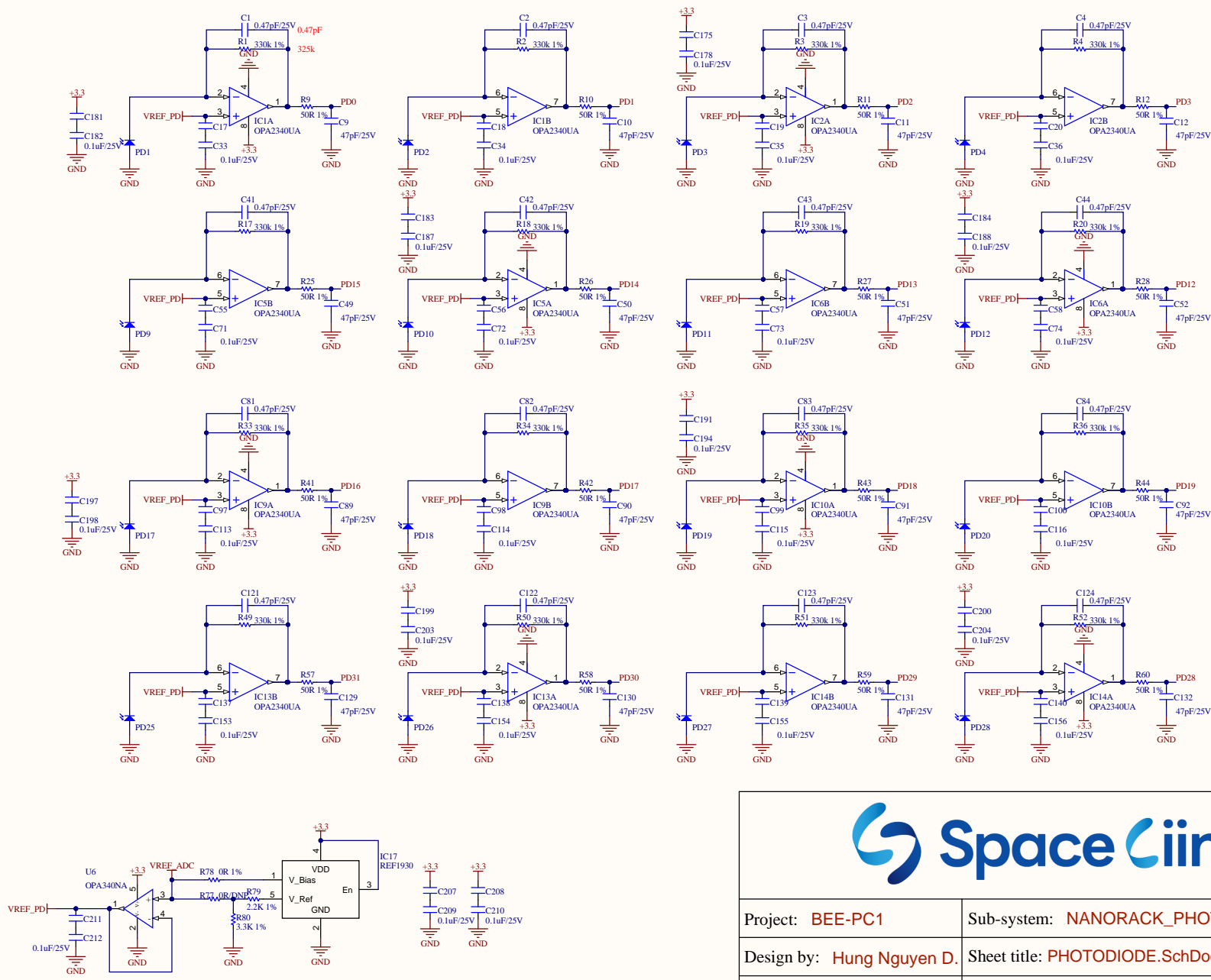
D

A

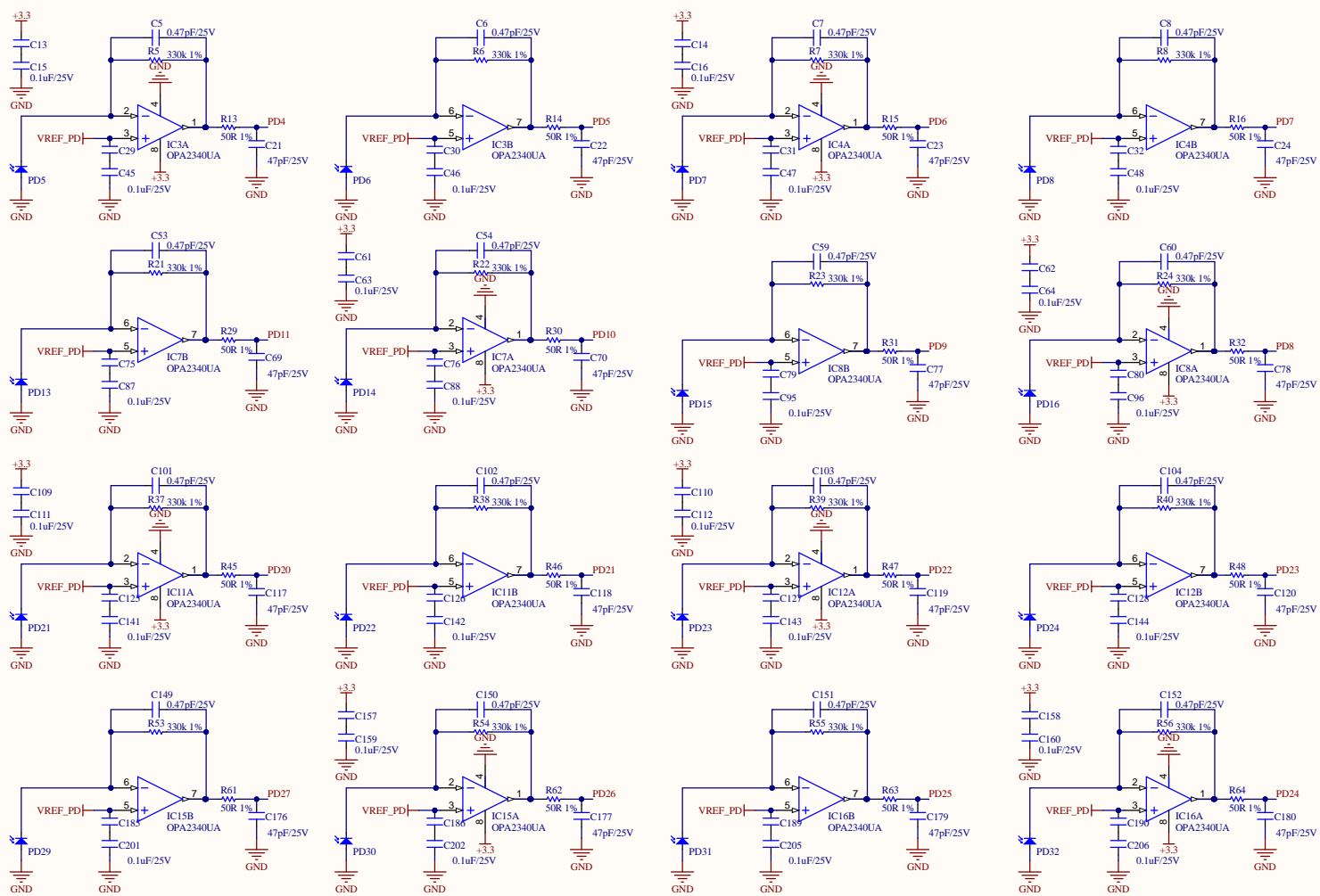
B

C

D



| | | | |
|---------------------------|--------------------------------|--|--|
| Project: BEE-PC1 | | Sub-system: NANORACK_PHOTODIODE.PrjPcb | |
| Design by: Hung Nguyen D. | Sheet title: PHOTODIODE.SchDoc | Sheet 5 of 6 | |
| Checked by: Bao Bui Q. | Document No: 5 | Size: A3 | |
| Approved by: Vu Pham | Revision: V1.0.0 | Date: 10/4/2024 | |



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|---------------------------|--|-----------------|
| Project: BEE-PC1 | Sub-system: NANORACK_PHOTODIODE.PrjPcb | |
| Design by: Hung Nguyen D. | Sheet title: PHOTODIODE (CONT).SchDoc | Sheet 6 of 6 |
| Checked by: Bao Bui Q. | Document No: 6 | Size: A3 |
| Approved by: Vu Pham | Revision: V1.0.0 | Date: 10/4/2024 |