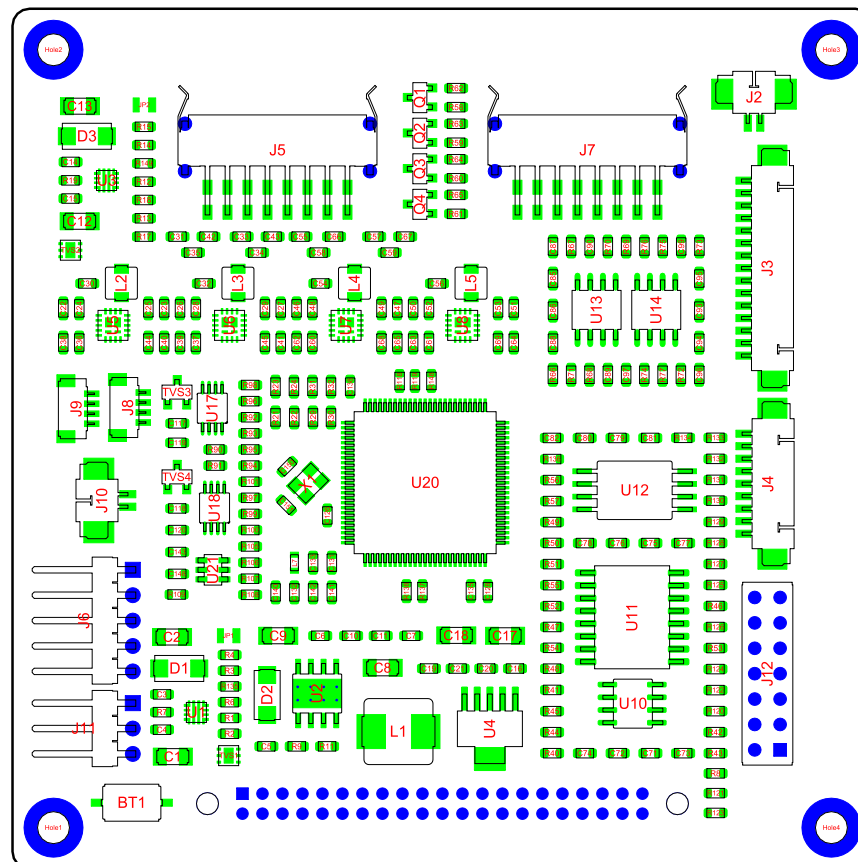
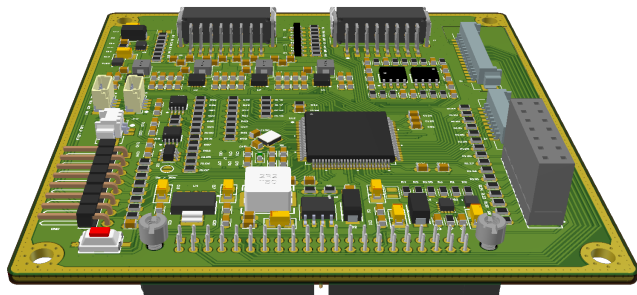
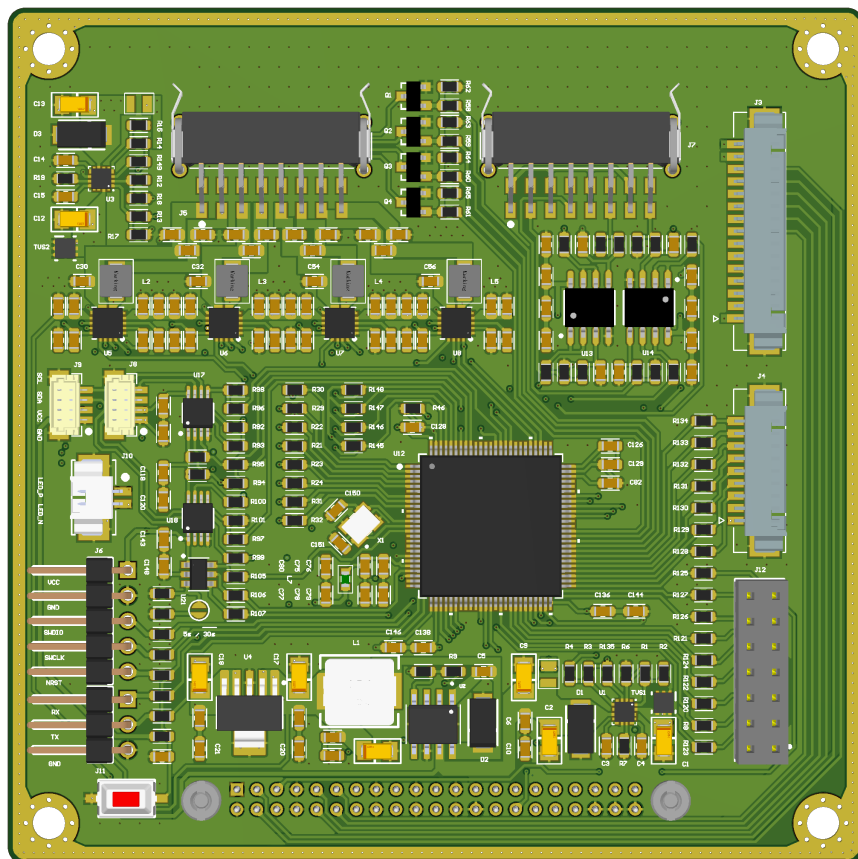


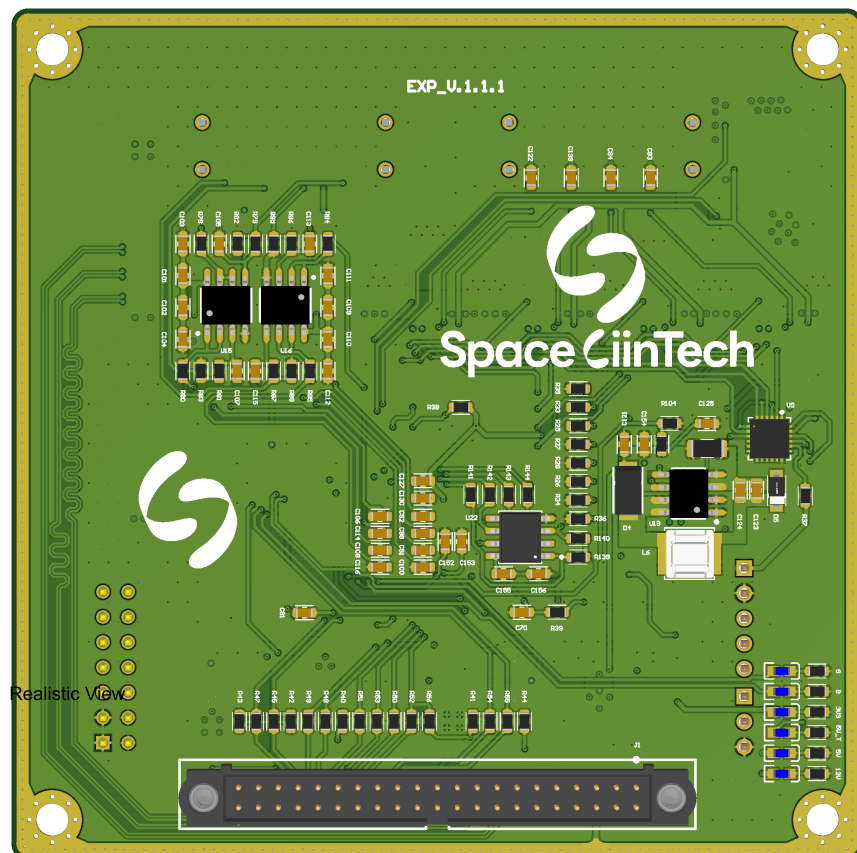
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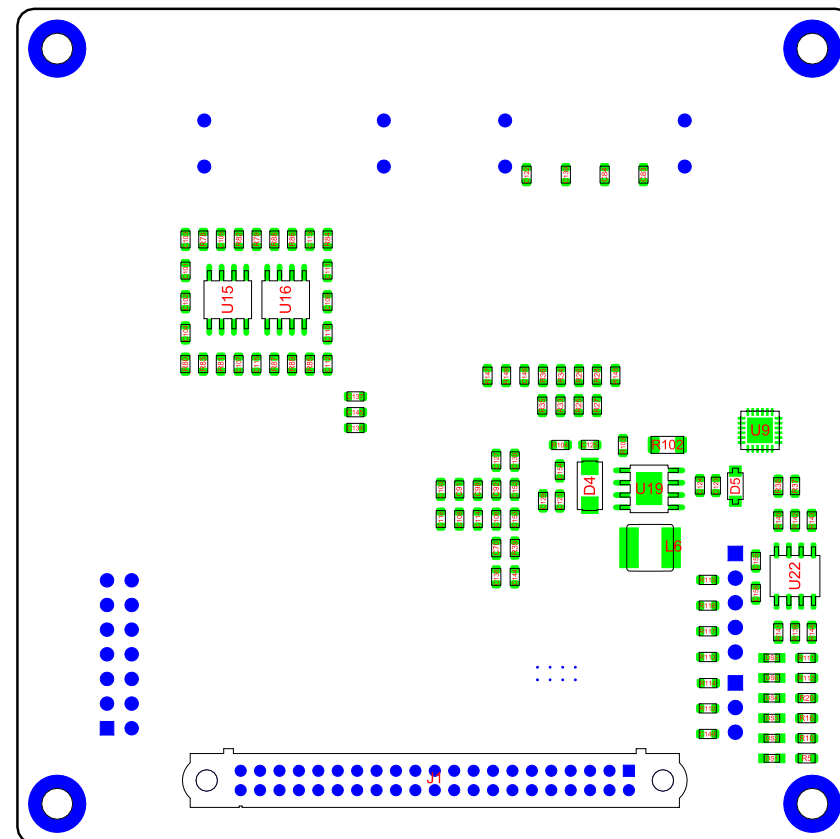
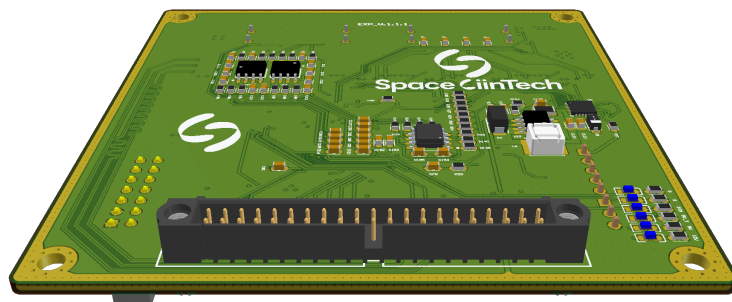


Project: BEE-PC1	Sub-system: NANORACK_EXP_CONTROLLER	
Design by: SANG HUYNH	Sheet title: EXP_V111.PCBDwf	Sheet 1 of 3
Checked by: BAO BUI Q.	Document No: 1	Size: A3
Approved by: VU PHAM	Revision: V1.1.0	Date: 3/26/2025

# BOTTOM



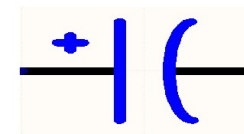
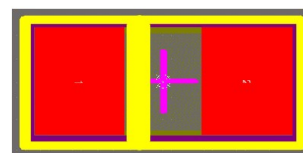
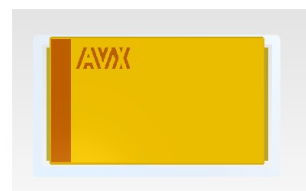
Realistic View



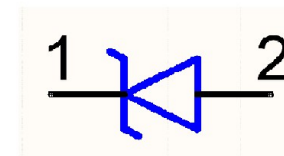
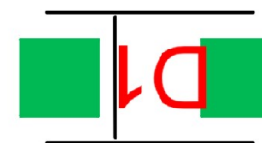
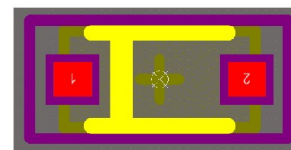
Project: BEE-PC1	Sub-system: NANORACK_EXP_CONTROLLER	
Design by: SANG HUYNH	Sheet title: EXP_V111.PCBDwf	Sheet 2 of 3
Checked by: BAO BUI Q.	Document No: 2	Size: A3
Approved by: VU PHAM	Revision: V1.1.0	Date: 3/26/2025

# NOTE

TANTALUM CAP



DIODE



Project: BEE-PC1	Sub-system: NANORACK_EXP_CONTROLLER	
Design by: SANG HUYNH	Sheet title: EXP_V111.PCBDwf	Sheet 3 of 3
Checked by: BAO BUI Q.	Document No: 3	Size: A3
Approved by: VU PHAM	Revision: V1.1.0	Date: 3/26/2025