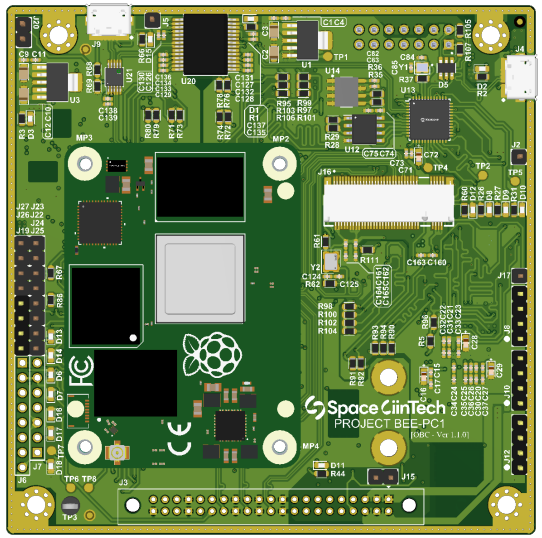
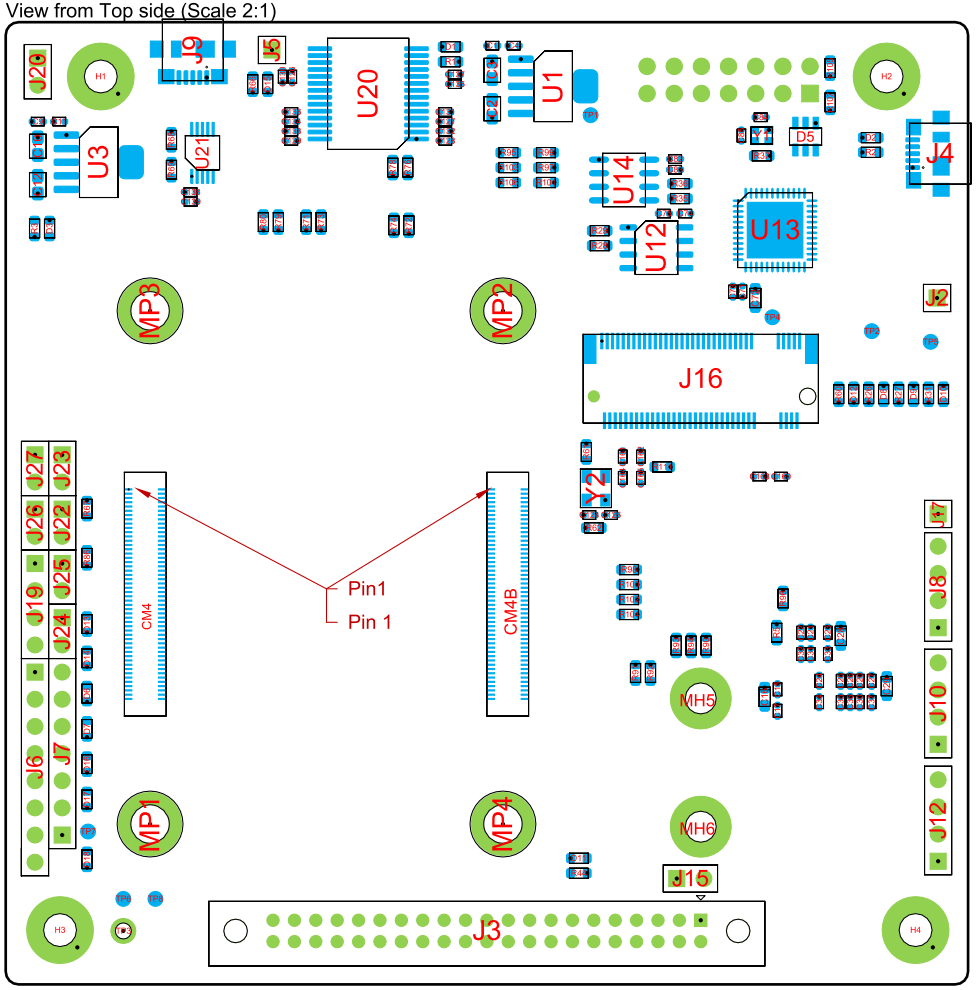
Realistic View

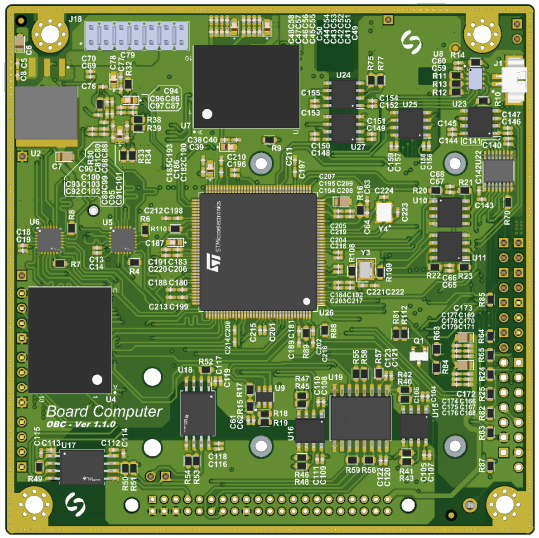


View from Top side (Scale 2:1)

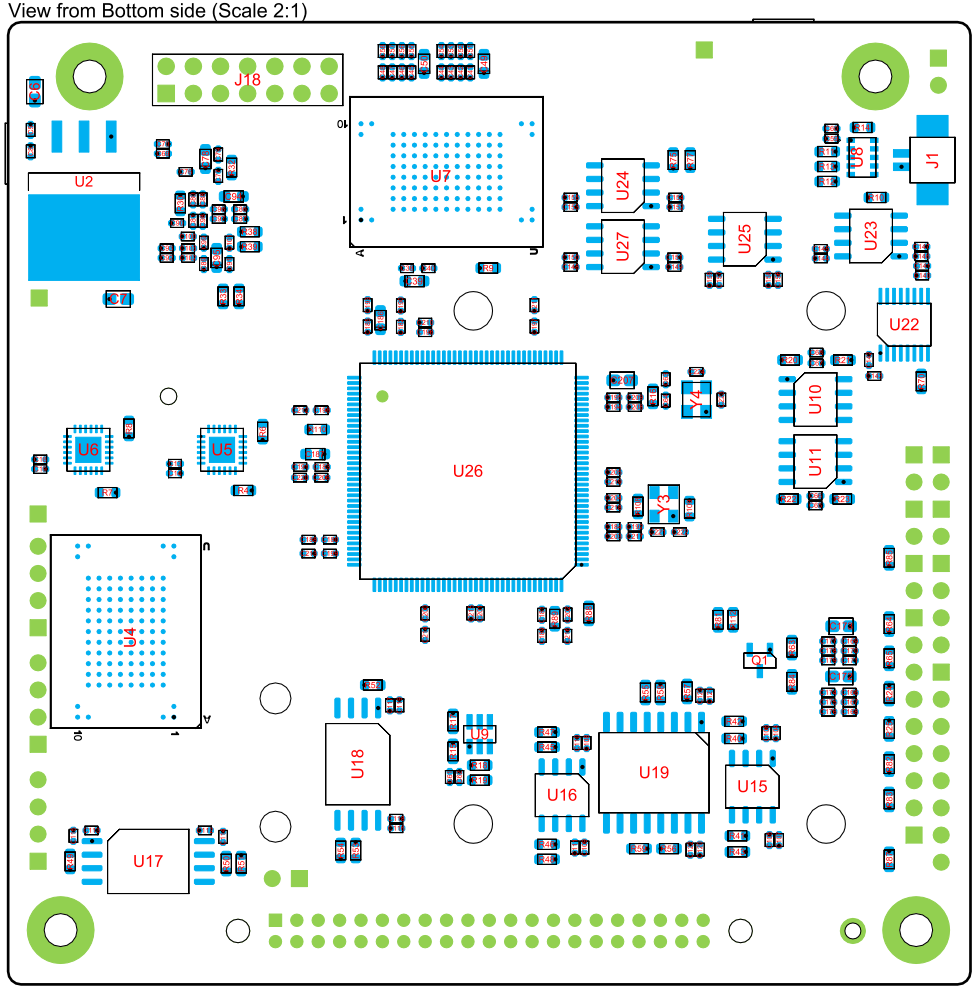


SpaceCiinTech		
Project: obc_ver2.PrjPcb	Sub-system: *	
Design by: CAM DOAN	Sheet title: *	Sheet 1 of 3
Checked by: BAO BUI Q.	Document No: 1	Size: A3
Approved by: VU PHAM	Revision:	Date: 01/26/25

Realistic View

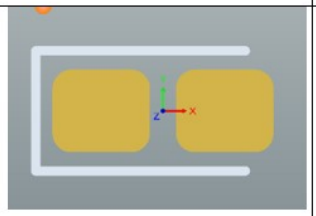
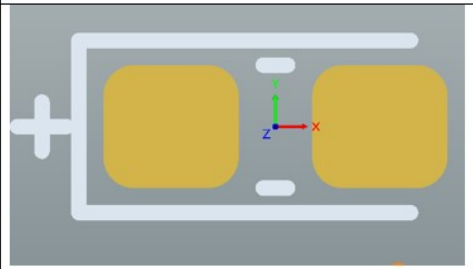
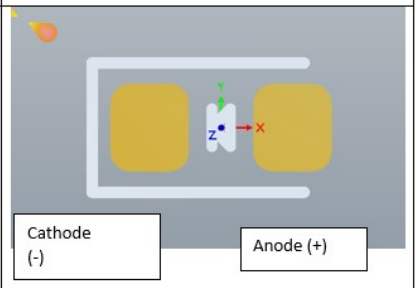
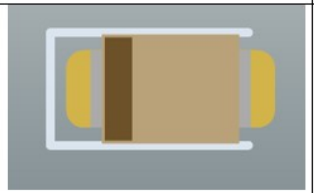
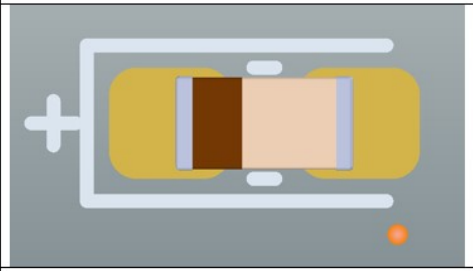
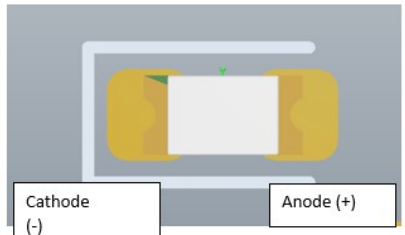
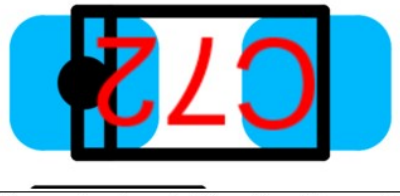
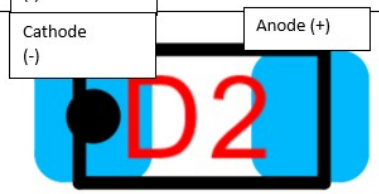


View from Bottom side (Scale 2:1)



Project: BEE-PC1	Sub-system: NANORACK_Single_OBC	
Design by: cd	Sheet title: *	Sheet 2 of 3
Checked by: BAO BUI Q.	Document No: 2	Size: A3
Approved by: VU PHAM	Revision: V1.0.0	Date: 01/10/2024

Components that need attention

Footprint	CAP_MP_2012X170	CAP_MP_1608X90	WL-SMCW_0603_150060xx75000
Reference	C3, C6, C10, C172, C173, C2, C7, C12, C207	C16, C39, C72, C78 C28, C29, C49, C50, C94, C95, C186, C187	D6, D7, D8, D9 D1, D2, D3, D10, D11, D12, D13, D14, D15, D16, D17, D18
Silk screen draw			
3D			
Assemply drawing			
Note	Line in silkscreen: Anode (+) Dot in Assemply drawing: Anode (+)	In silkscreen, the plus mark (+) is positive of the capacitor. Dot in Assemply drawing: Anode (+)	Dot in Assemply drawing: Cathode (-)

Project: BEE-PC1	Sub-system: NANORACK_Single_OBC	
Design by: cd	Sheet title: *	Sheet 3 of 3
Checked by: BAO BUI Q.	Document No: 3	Size: A3
Approved by: VU PHAM	Revision: V1.0.0	Date: 01/10/2024