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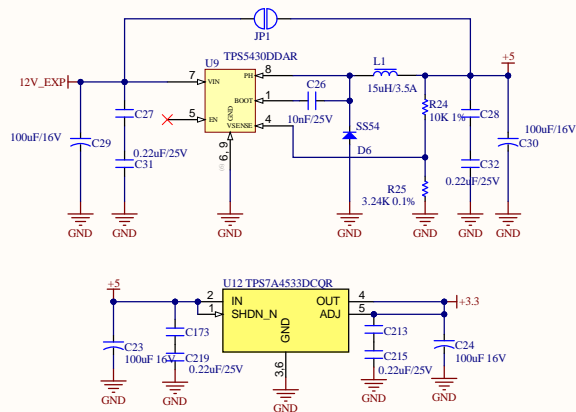
NANORACK PHOTODIODE

Schematic V1.1.0

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Project: BEE-PC1	Sub-system: NANORACK_PHOTODIODE.PrjPcb	
Design by: Hung Nguyen D.	Sheet title: INDEX.SchDoc	Sheet 1 of 5
Checked by: Bao Bui Q.	Document No: 1	Size: A3
Approved by: Vu Pham	Revision: V1.1.0	Date: 1/15/2025

1	2	3	4	5	6
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A					
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			Design by: Hung Nguyen D.	Sheet title: BLOCK_DIAGRAM.SchDoc	Sheet 2 of 5
			Checked by: Bao Bui Q.	Document No: 2	Size: A3
			Approved by: Vu Pham	Revision: V1.1.0	Date: 1/15/2025
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POWER SWITCH AND LDO

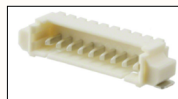


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Design by: Hung Nguyen D.	Sheet title: POWER SWITCH AND LDO.SchDoc	Sheet 3 of 5
Checked by: Bao Bui Q.	Document No: 3	Size: A3
Approved by: Vu Pham	Revision: V1.1.0	Date: 1/17/2025

Diagram illustrating the test setup for the AD7792. The setup includes a 12V_EXPT supply connected to the SCK_PD, MISO_PD, MOSI_PD, SW_CS, ADC_CS, and ADC_CONVST pins. The ADC_EOC pin is connected to a yellow box labeled J1, which has pins 1 through 9. The pins are also connected to ground through diodes D1, D2, D3, D4, and D5, which are labeled S2312 and S2305. The diagram includes labels for TP7, TP8, TP9, TP10, TP1, and TP1.2.

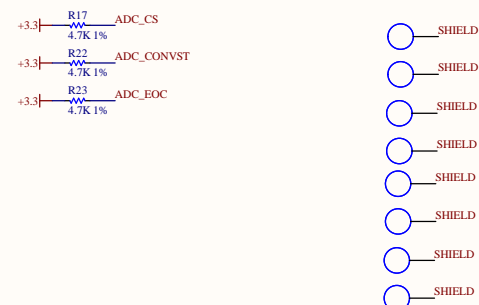
- ELECTRICAL:
CURRENT RATING: 1A/CONTACT
VOLTAGE RATING: 125V/CONTACT
- ENVIRONMENTAL:
TEMPERATURE RANGE: -40°C TO +105°C
- PHYSICAL:
PITCH-MATING: 0.049" (1.25mm)
- WIRE:
WIRE SIZE: 24 AWG
MAX CURRENT: 1A per Wire
ISULATION MATERIAL: PTFE
- MATING:
Molex - 0510210900



- MATING -



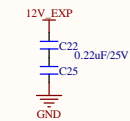
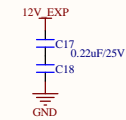
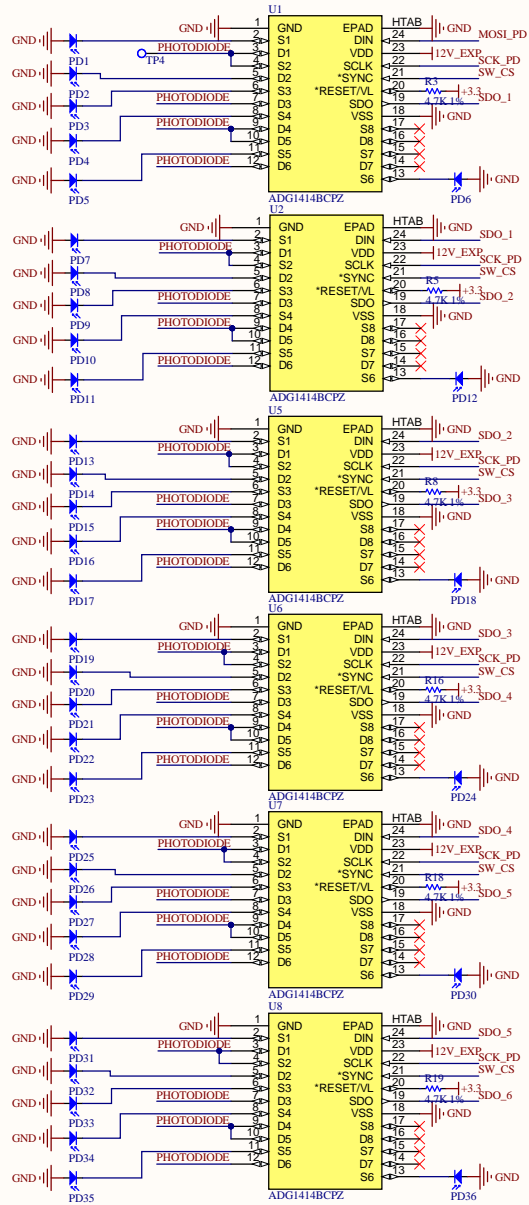
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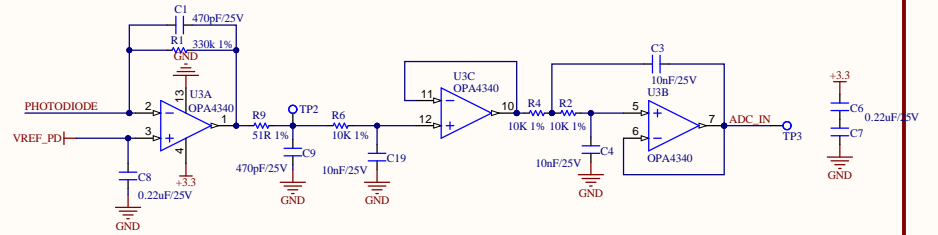
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Project: BEE-PC1	Sub-system: NANORACK_PHOTODIODE.PrjPcb	
Design by: Hung Nguyen D.	Sheet title: COMMUNICATION.SchDoc	Sheet 4 of 5
Checked by: Bao Bui Q.	Document No: 4	Size: A3
Approved by: Vu Pham	Revision: V1.1.0	Date: 1/17/2025

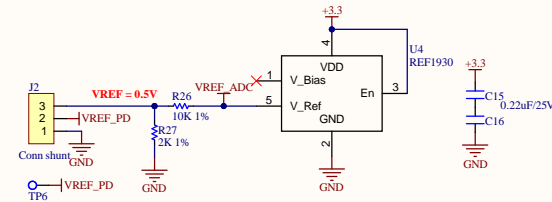
SWITCHING



TRANSIMPEDANCE, ANTI ALIAS



VREF



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Design by: Hung Nguyen D.	Sheet title: PHOTODIODE.SchDoc	Sheet 5 of 5
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