

PROJECT: SLT 6U Satellite

sub-system: SLT_110.PrjPcb

Version: 1.1.0



Project: SLT 6U Satellite

Sub-system: SLT 6U Satellite

Design by: Cam Doan

Sheet title: COVER PAGE.SchDoc

Sheet 1 of 21

Checked by: Bao Bui

Document No: 1

Size: A3

Approved by: Vu Pham

Revision: 1.1.0

Date: 09/29/2025

A

A

B

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C

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D

D



Project: SLT 6U Satellite

Sub-system: SLT 6U Satellite

Design by: Cam Doan

Sheet title: BLOCK DIAGRAM.SchDoc Sheet 2 of 21

Checked by: Bao Bui

Document No: 2

Size: A3

Approved by: Vu Pham

Revision: 1.1.0

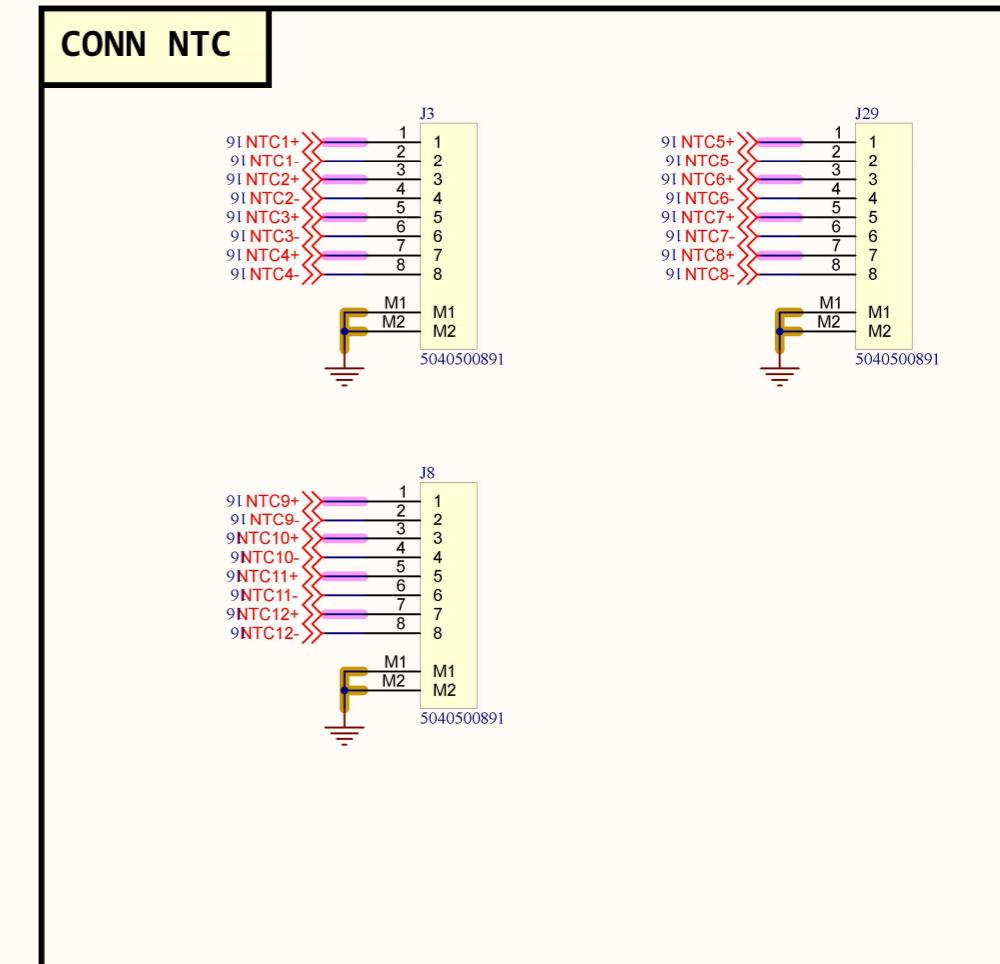
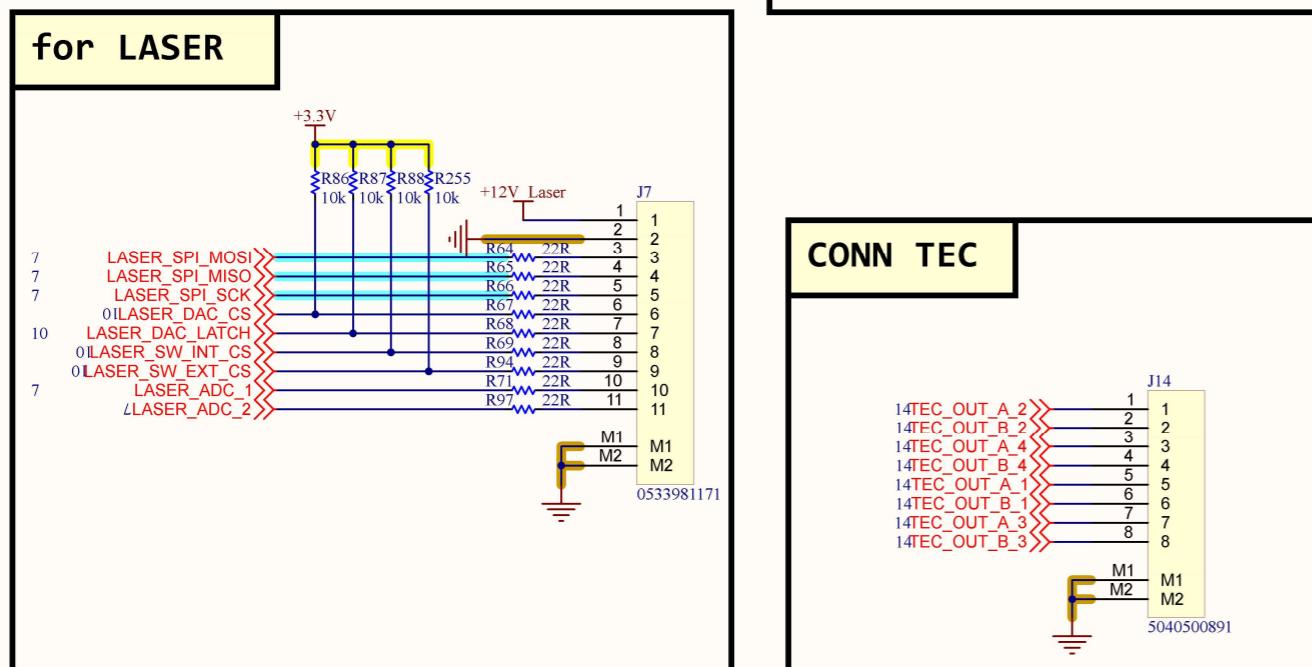
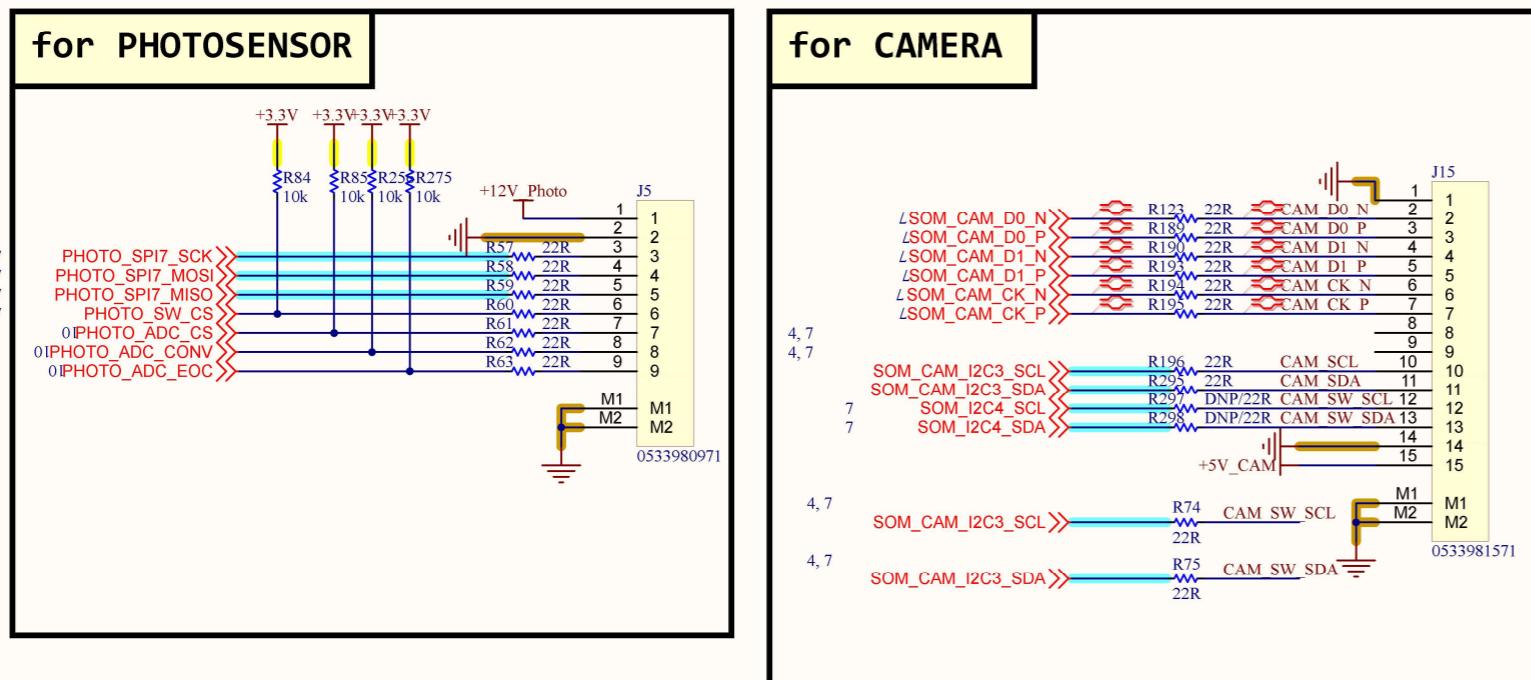
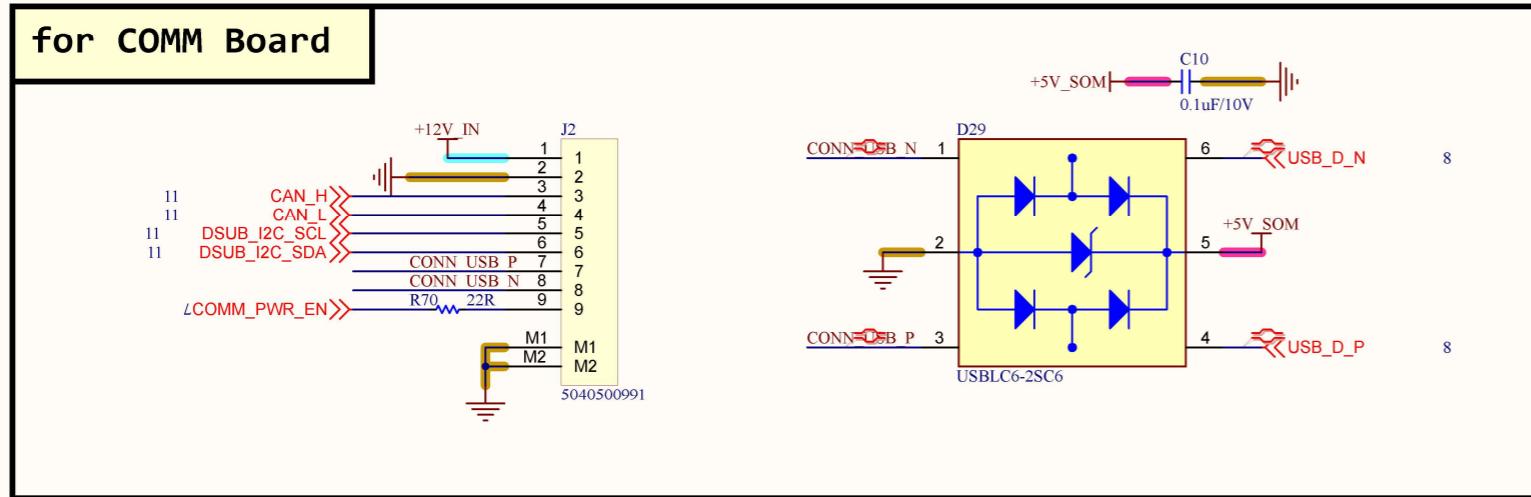
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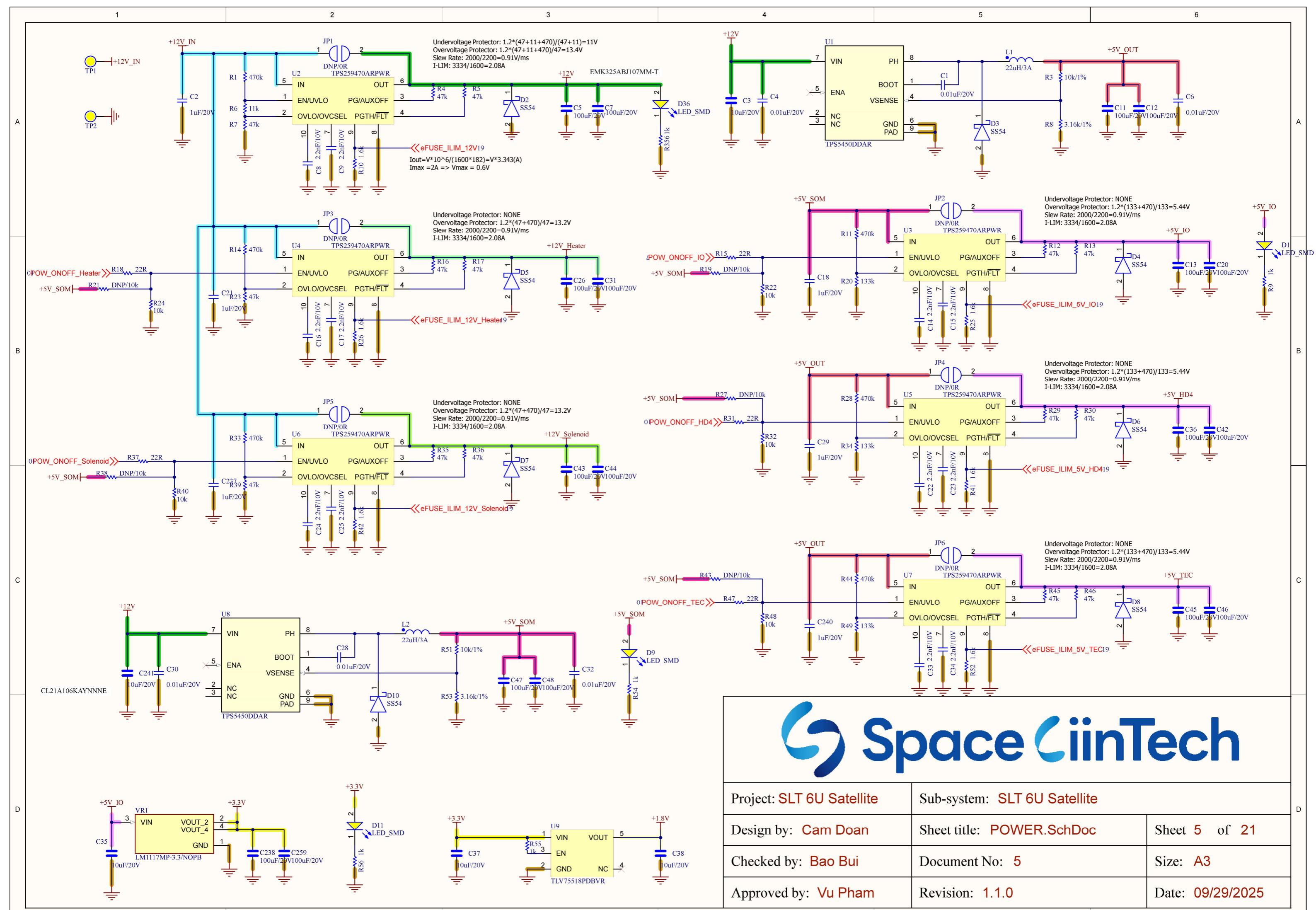


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Design by: Cam Doan	Sheet title: INDEX.SchDoc	Sheet 3 of 21
Checked by: Bao Bui	Document No: 3	Size: A3
Approved by: Vu Pham	Revision: 1.1.0	Date: 09/29/2025

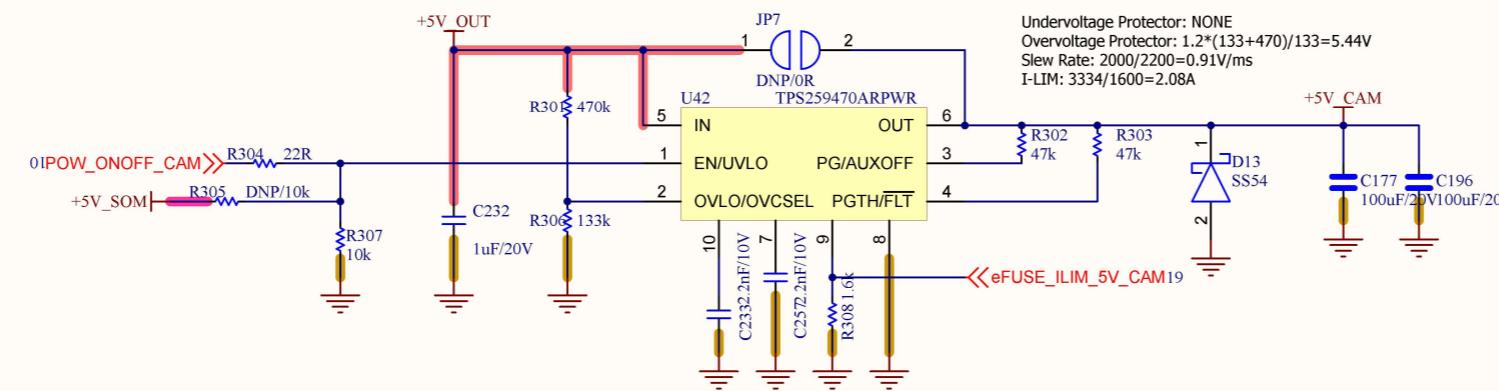


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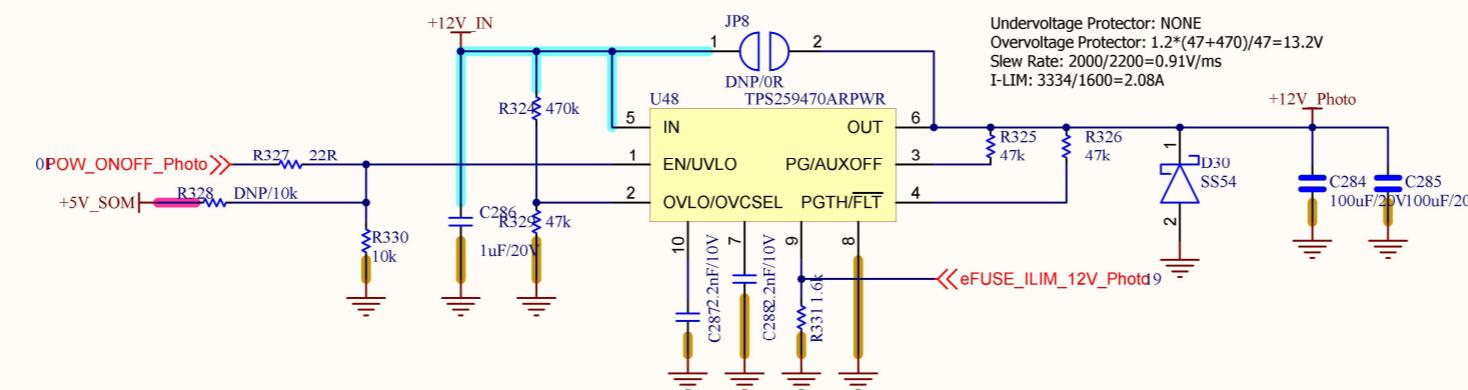
Project: SLT 6U Satellite	Sub-system: SLT 6U Satellite	
Design by: Cam Doan	Sheet title: CONNECTOR.SchDoc	Sheet 4 of 21
Checked by: Bao Bui	Document No: 4	Size: A3
Approved by: Vu Pham	Revision: 1.1.0	Date: 09/29/2025



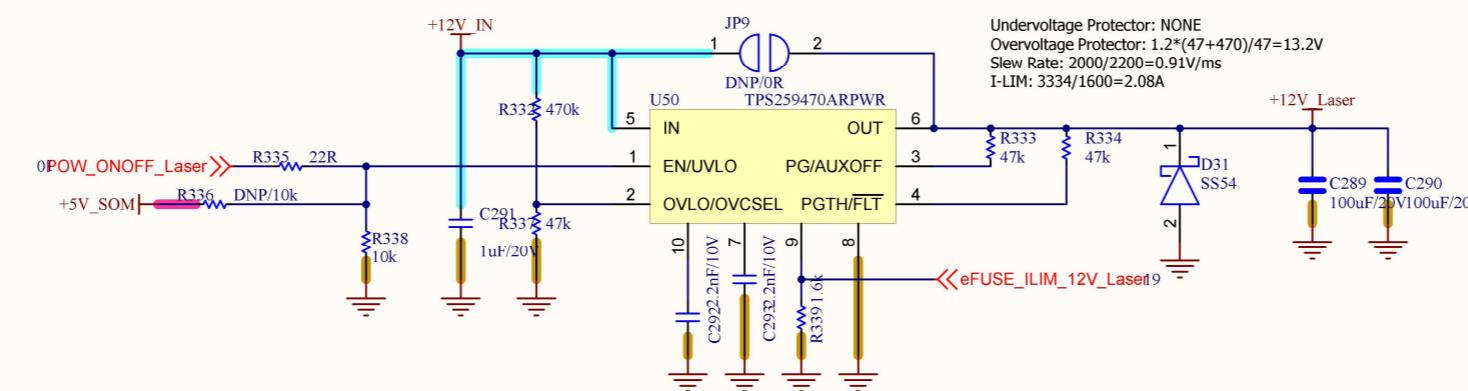
A



B



C



Project: SLT 6U Satellite

Sub-system: SLT 6U Satellite

Design by: Cam Doan

Sheet title: POWER_2.SchDoc

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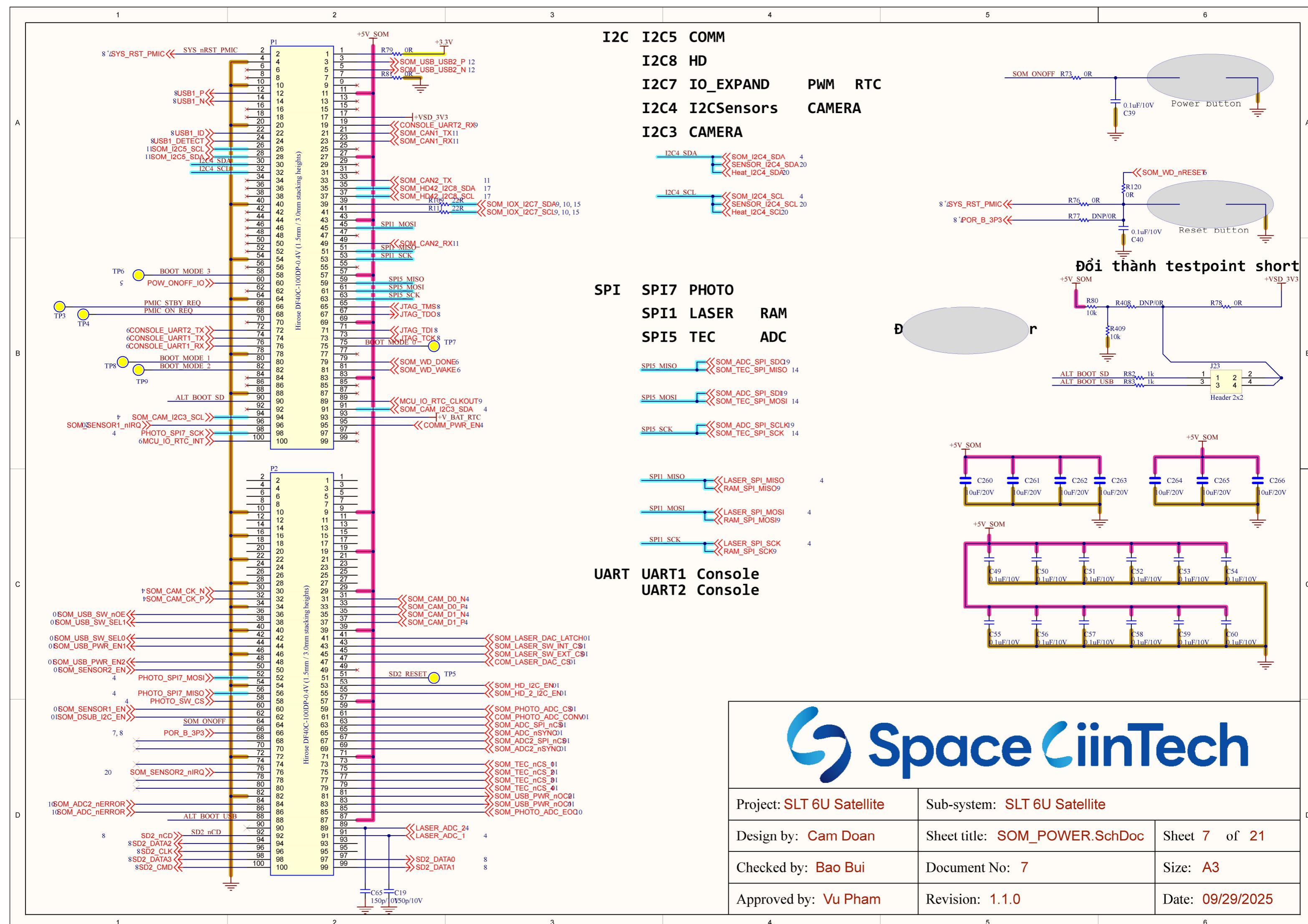
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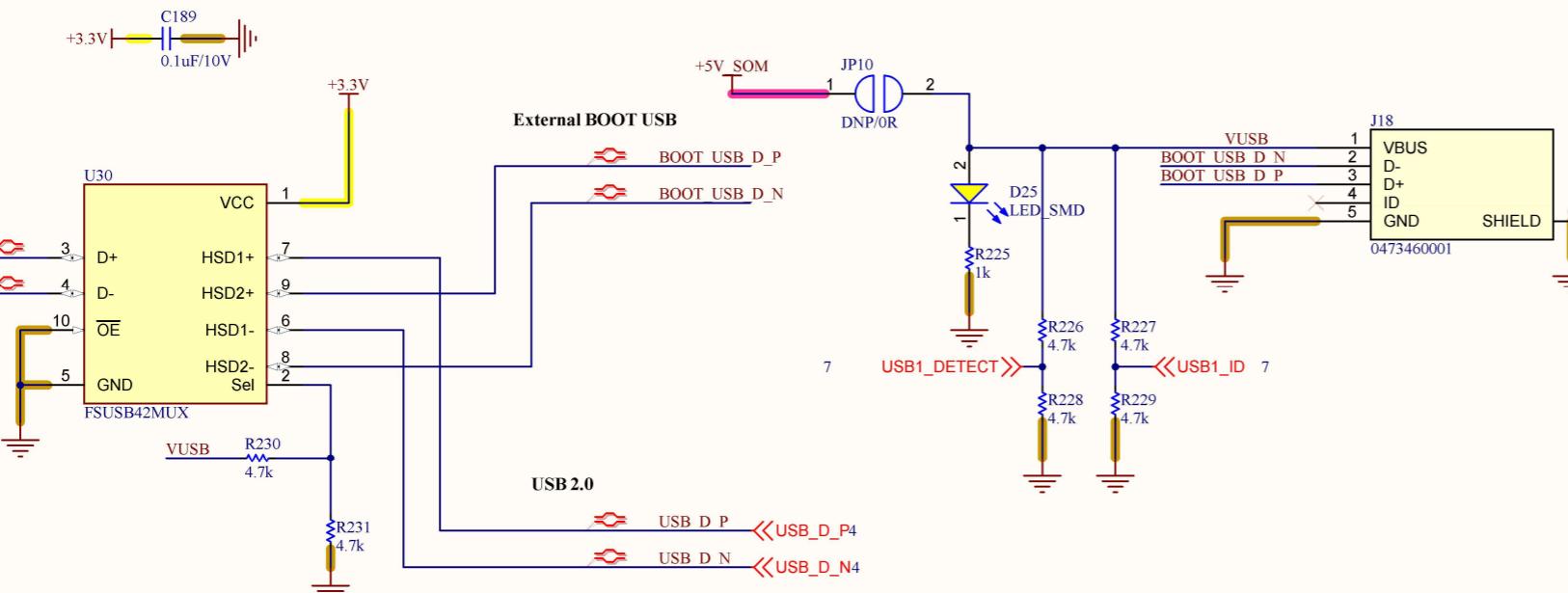
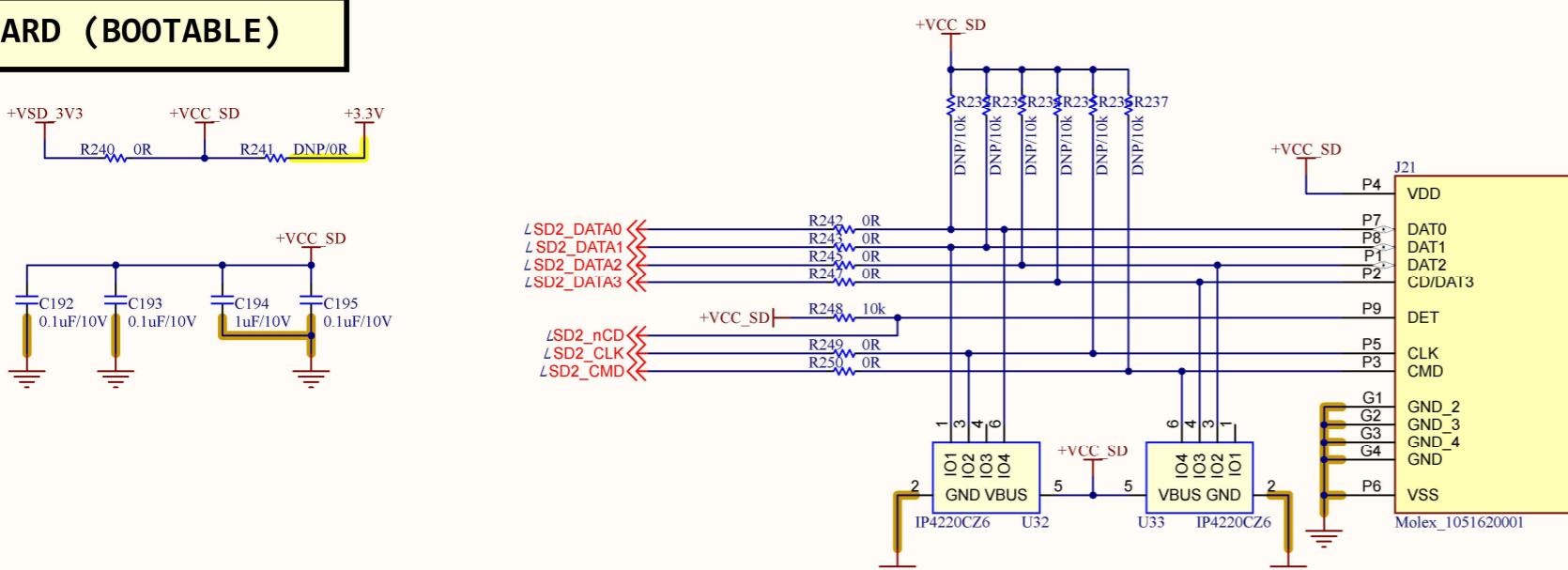
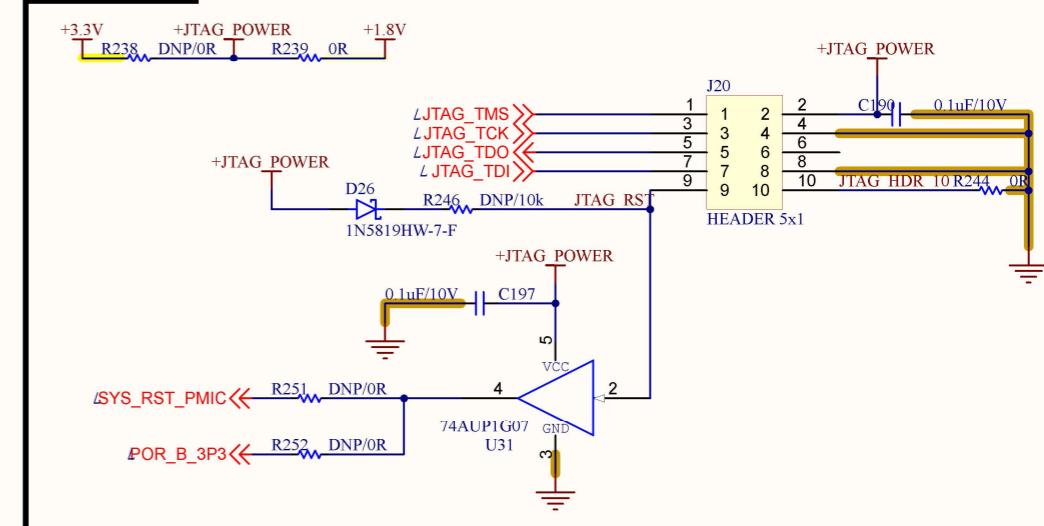
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Approved by: Vu Pham

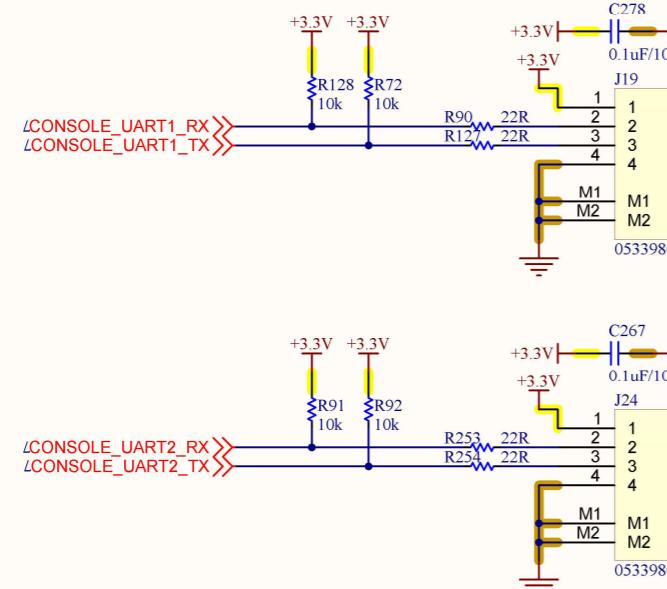
Revision: 1.1.0

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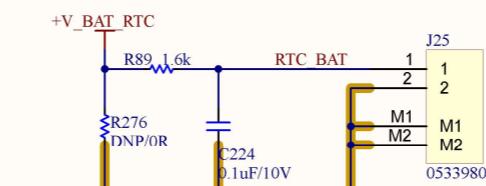


USB (BOOTABLE)**SD CARD (BOOTABLE)****JTAG**Project: **SLT 6U Satellite**Sub-system: **SLT 6U Satellite**Design by: **Cam Doan**Sheet title: **SOM_BOOT.SchDoc**Sheet **8** of **21**Checked by: **Bao Bui**Document No: **8**Size: **A3**Approved by: **Vu Pham**Revision: **1.1.0**Date: **09/29/2025**

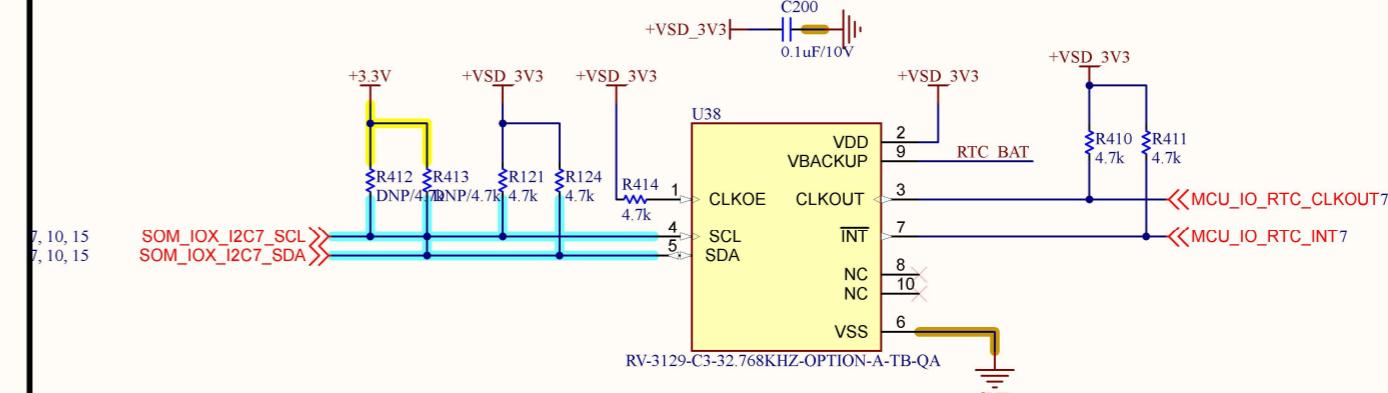
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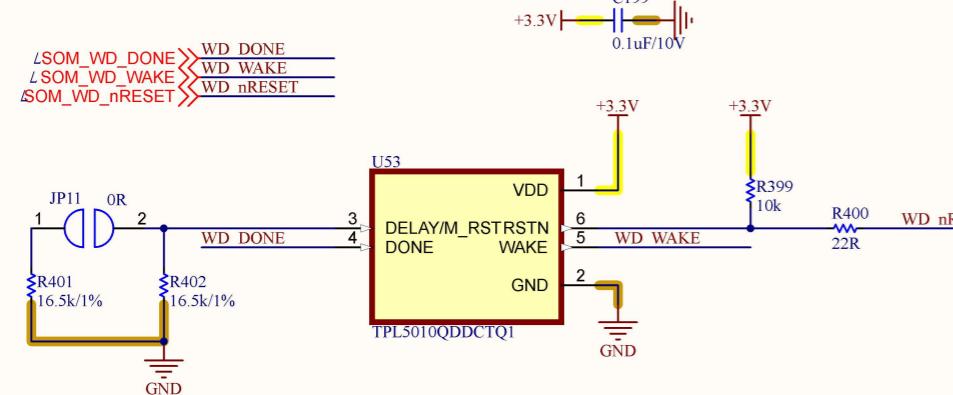
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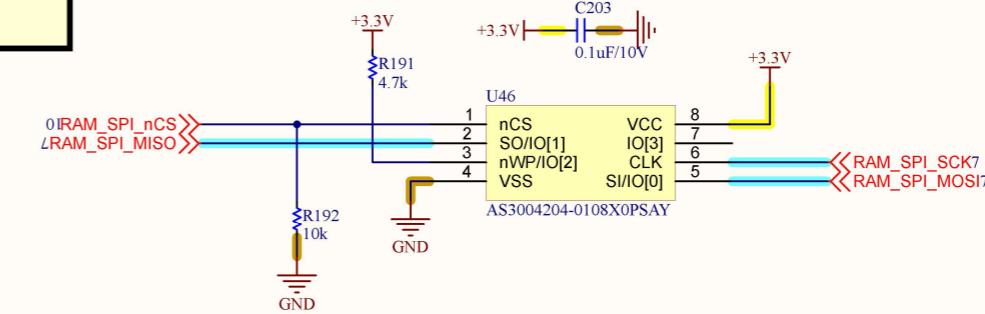
RTC



WATCH DOG



RAM



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Project: SLT 6U Satellite

Sub-system: SLT 6U Satellite

Design by: Cam Doan

Sheet title: SOM_Console.SchDoc

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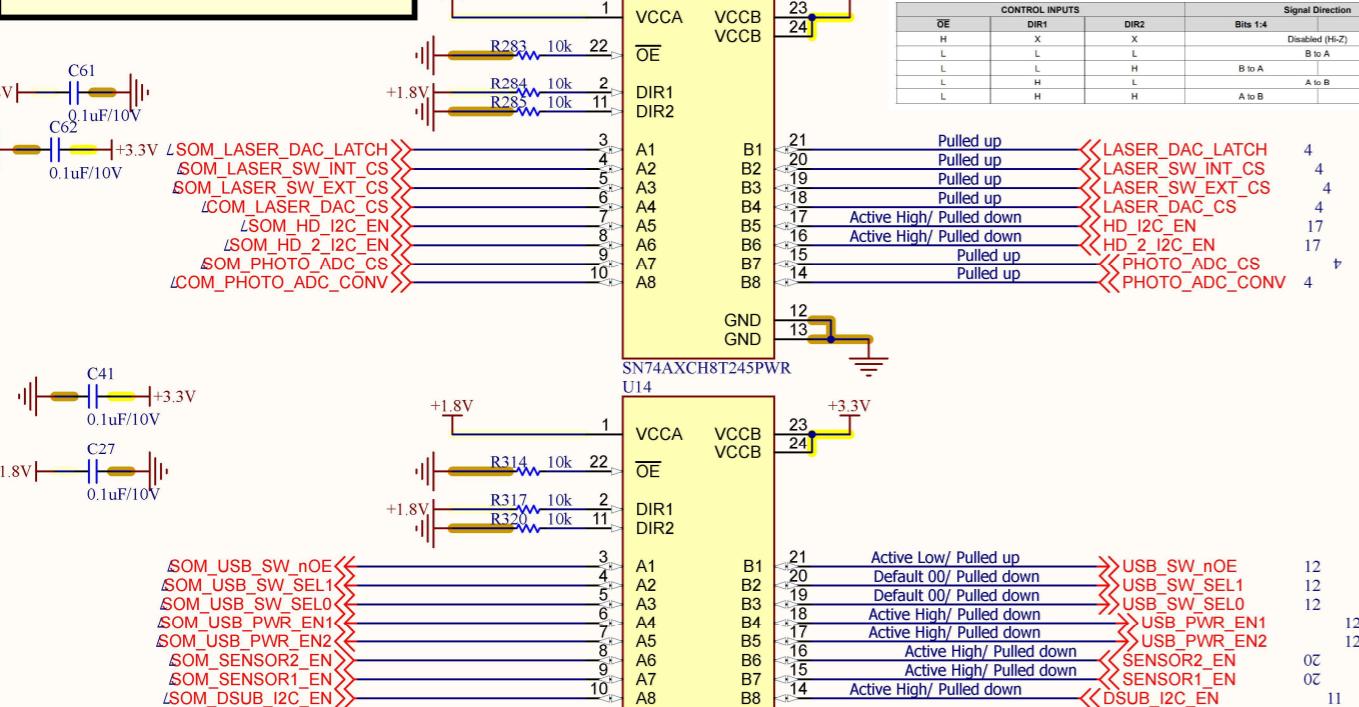
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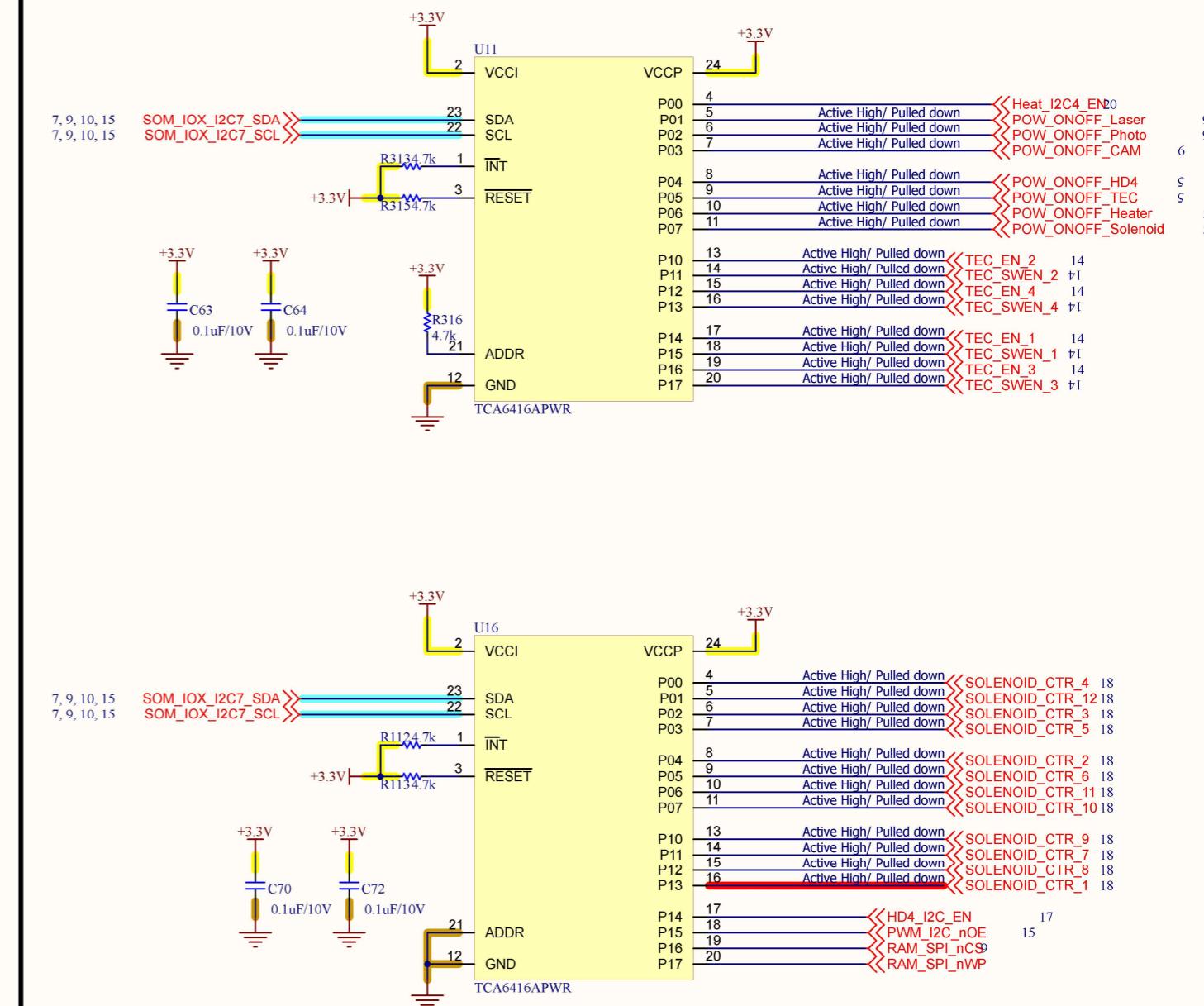
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IO VOLTAGE LEVEL



IO EXPANDER



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Project: SLT 6U Satellite

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Design by: Cam Doan

Sheet title: IO_EXPAND.SchDoc

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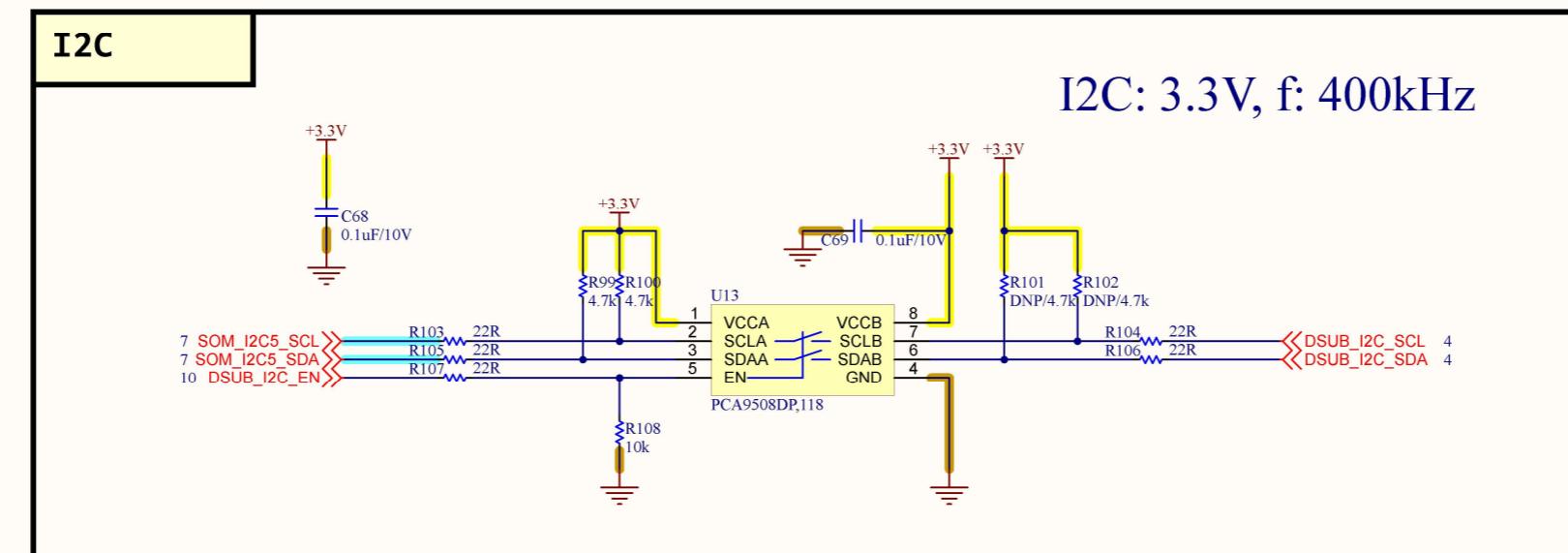
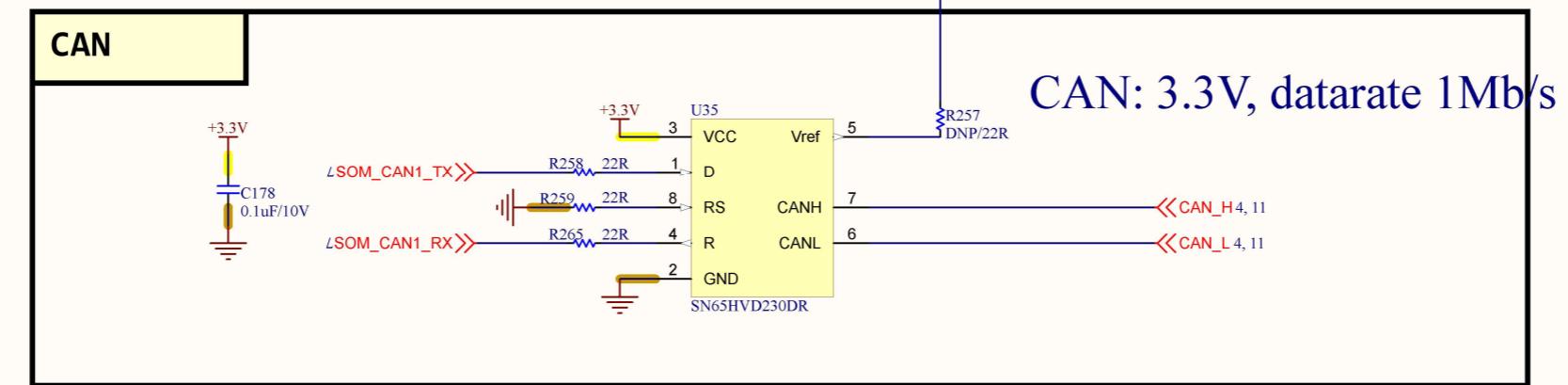
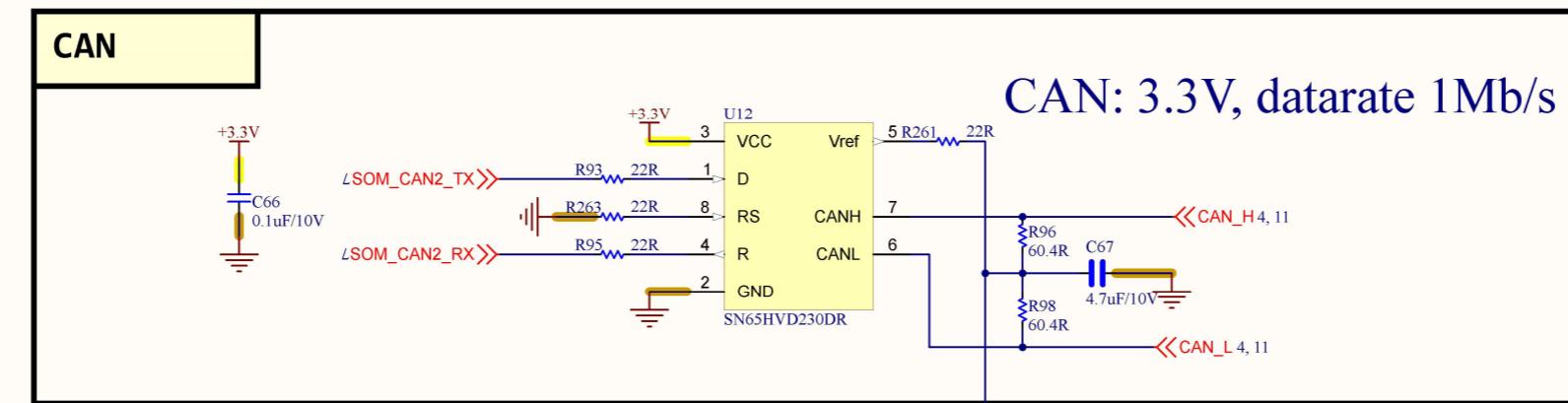
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Design by: Cam Doan

Sheet title: CAN_I2C.SchDoc

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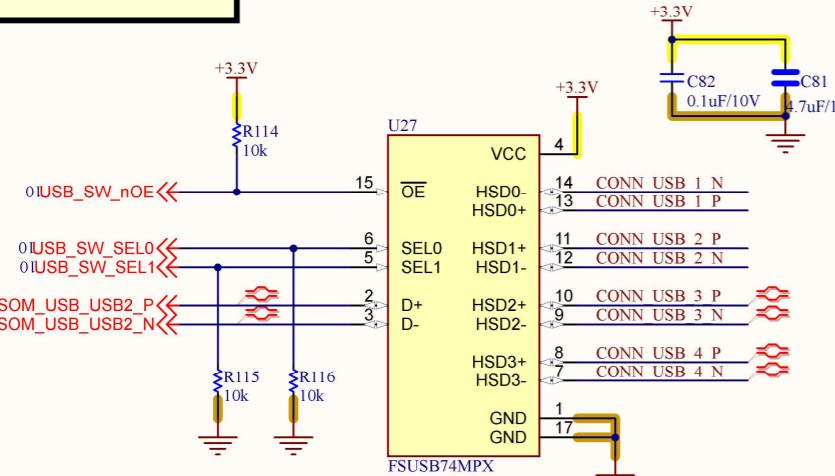
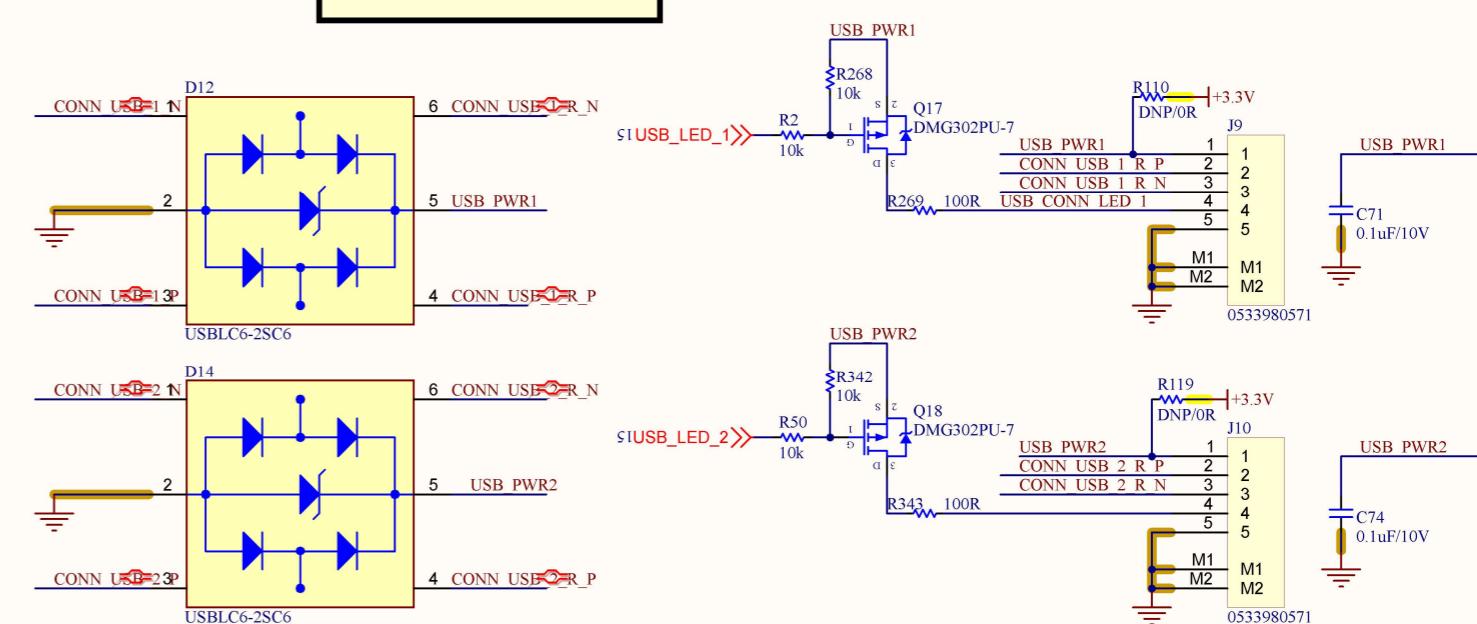
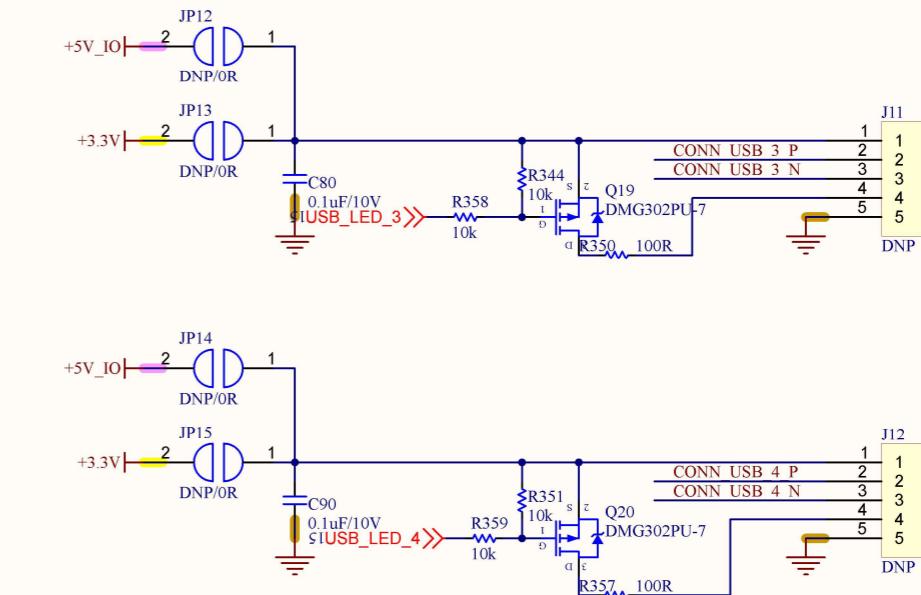
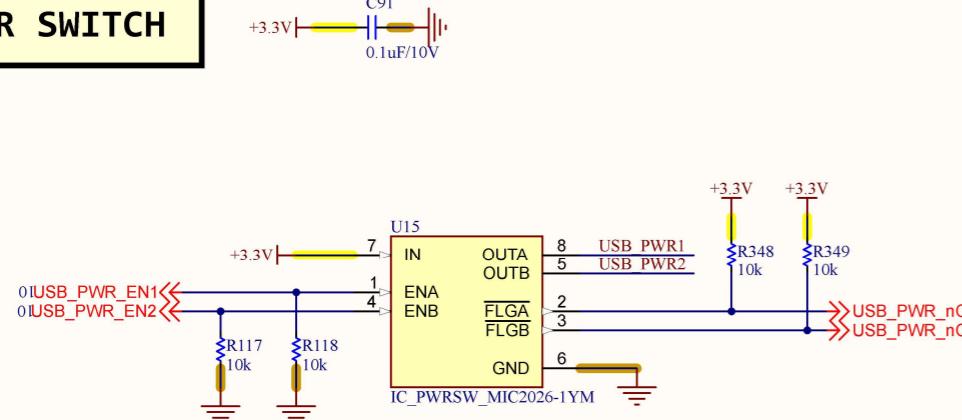
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USB SW 1:4**USB Connectors****POWER SWITCH**

Theme BJT keo LED



Project: SLT 6U Satellite

Sub-system: SLT 6U Satellite

Design by: Cam Doan

Sheet title: USB.SchDoc

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$$I_{LD} = (VDAC * R3) / (R32 * R8)$$

RAM



Project: SLT 6U Satellite

Sub-system: SLT 6U Satellite

Design by: Cam Doan

Sheet title: LED.SchDoc

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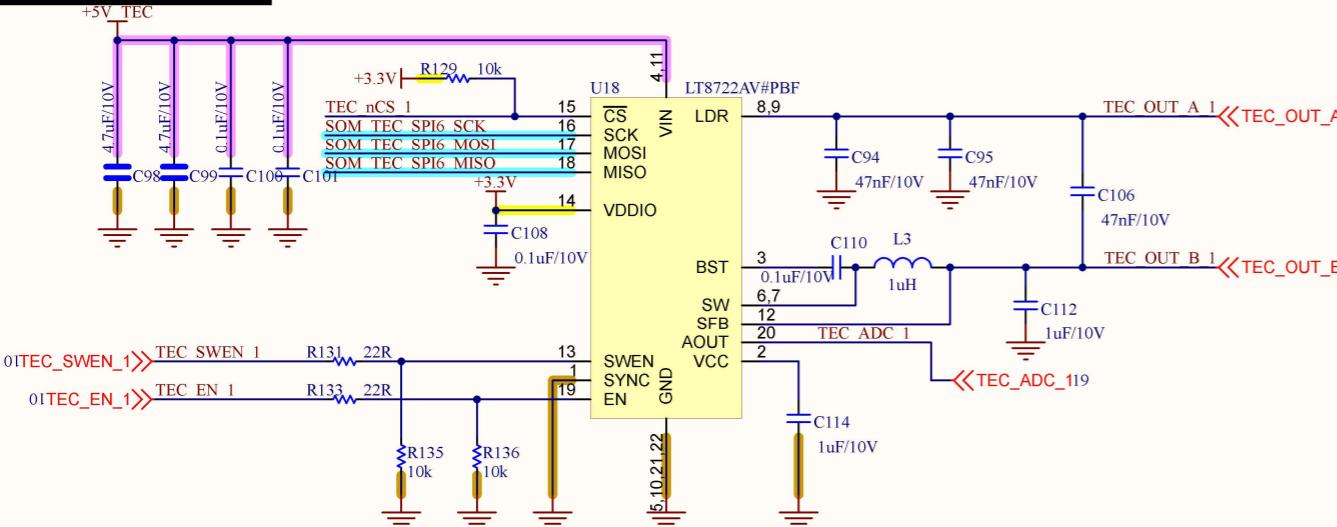
Date: 09/29/2025

L_SOM_TEC_SPI_SCK
 SOM TEC SPI6 SCK
 L_SOM_TEC_SPI_MOSI
 SOM TEC SPI6 MOSI
 L_SOM_TEC_SPI_MISO
 SOM TEC SPI6 MISO

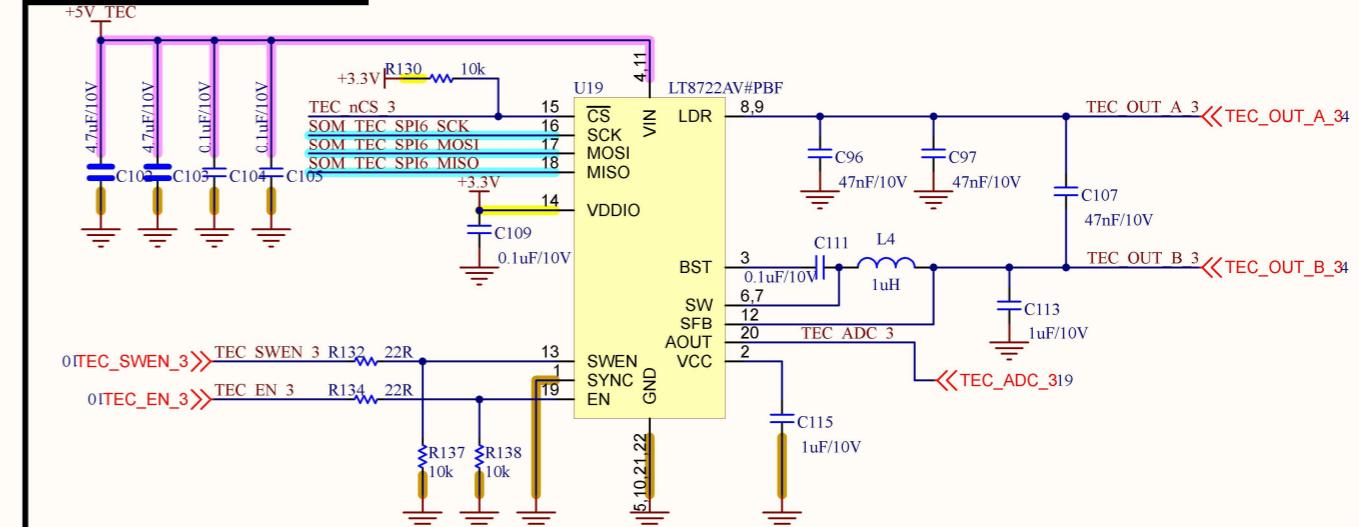
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 TEC nCS 1
 0I TEC_nCS_2
 TEC nCS 2
 0I TEC_nCS_3
 TEC nCS 3
 0I TEC_nCS_4
 TEC nCS 4

Check lại nhờ Sang

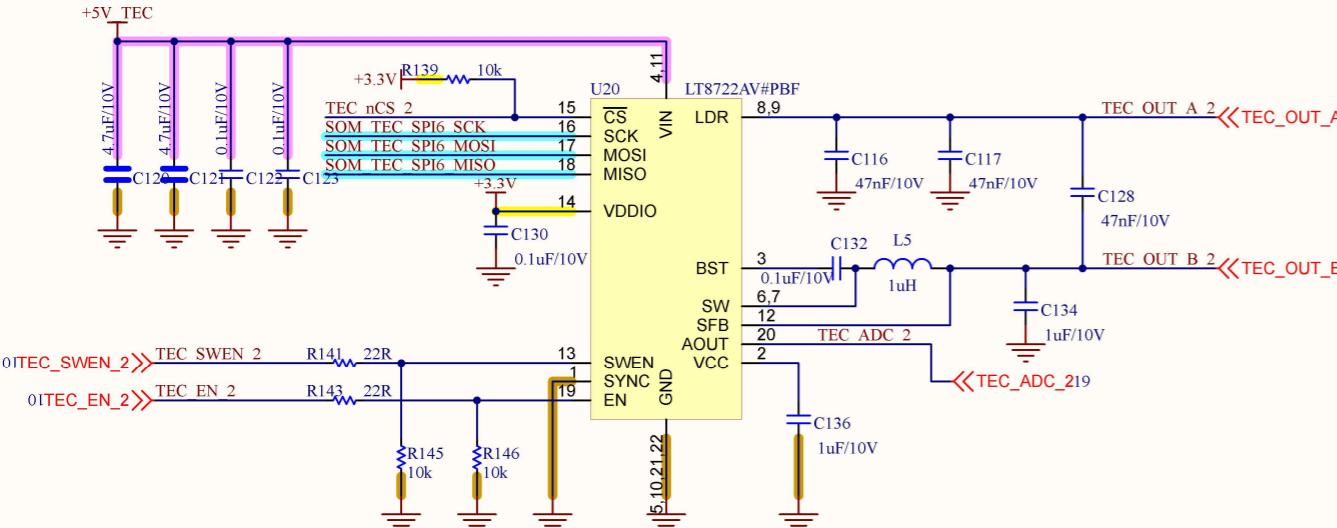
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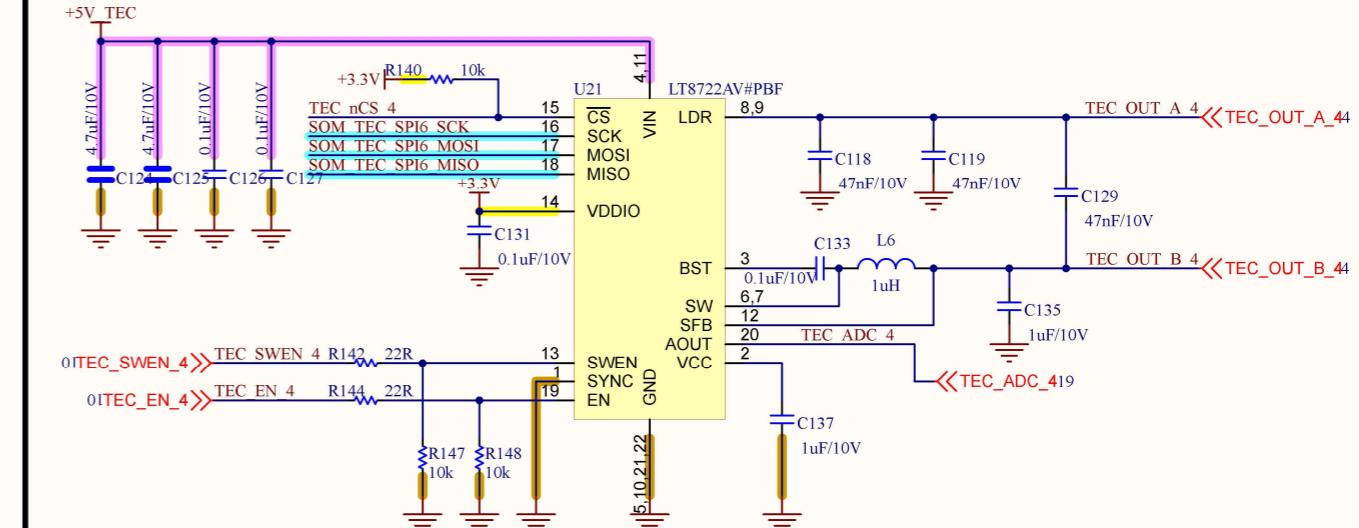
TEC 3



TEC 2



TEC 4



Project: SLT 6U Satellite

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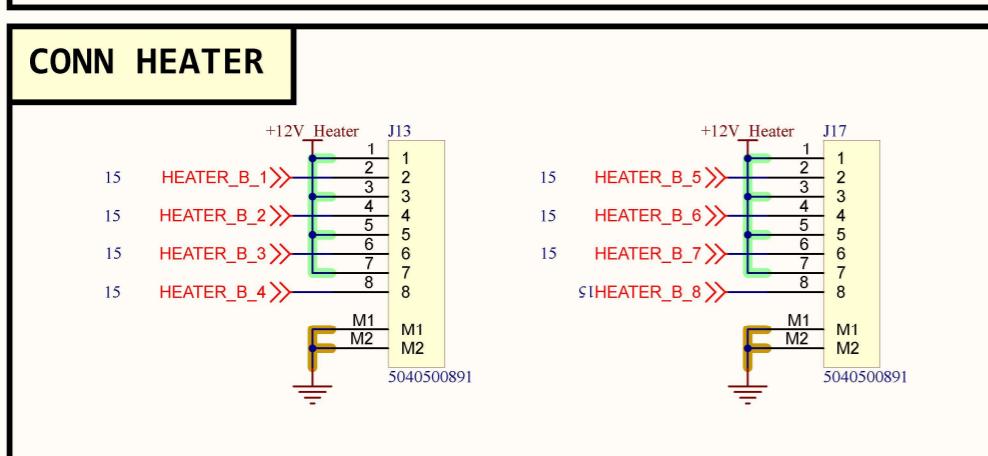
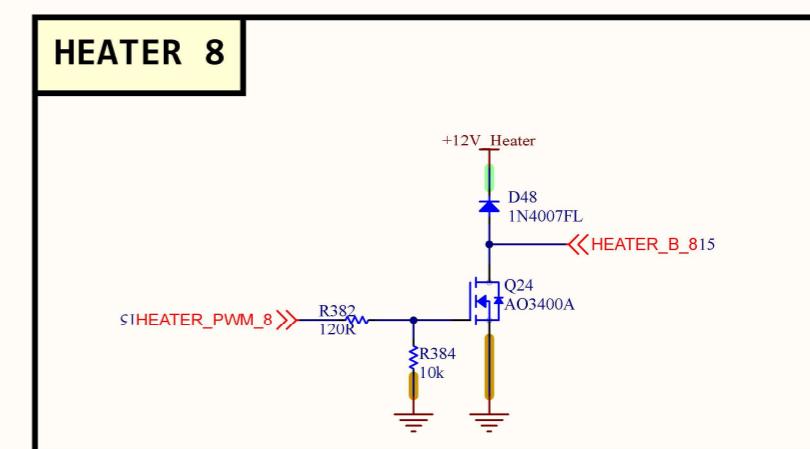
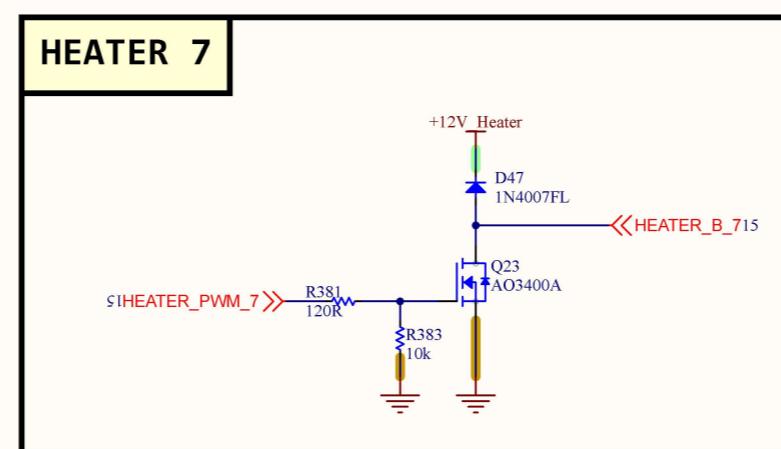
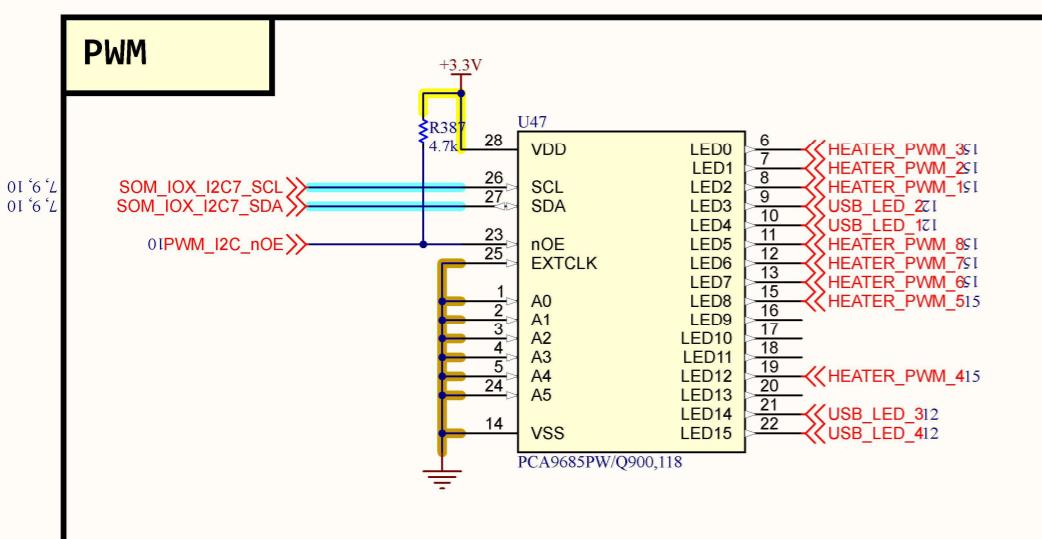
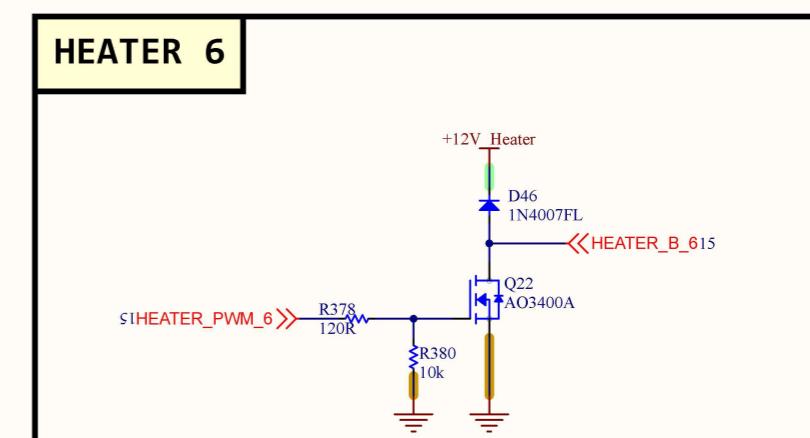
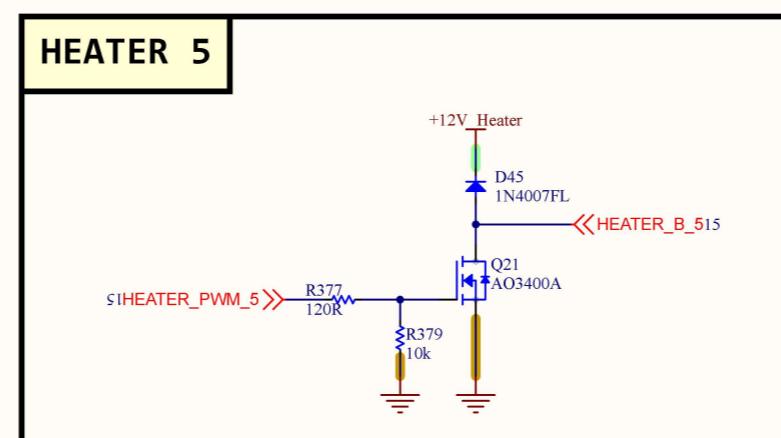
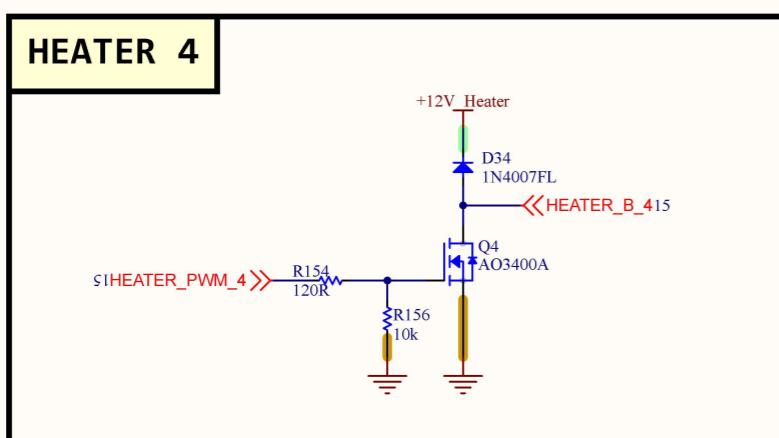
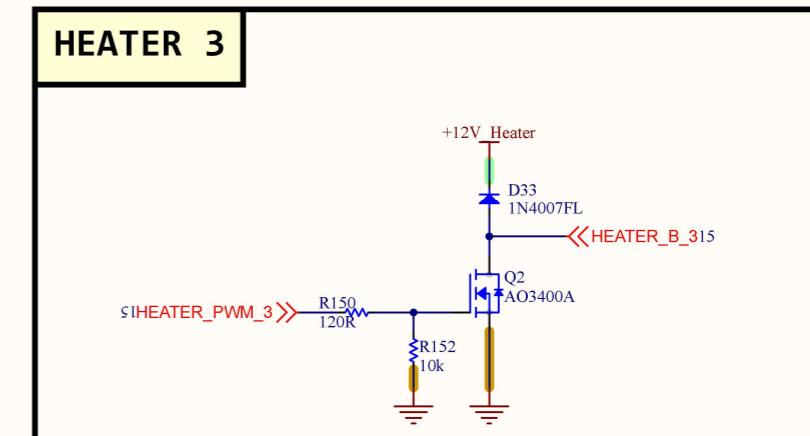
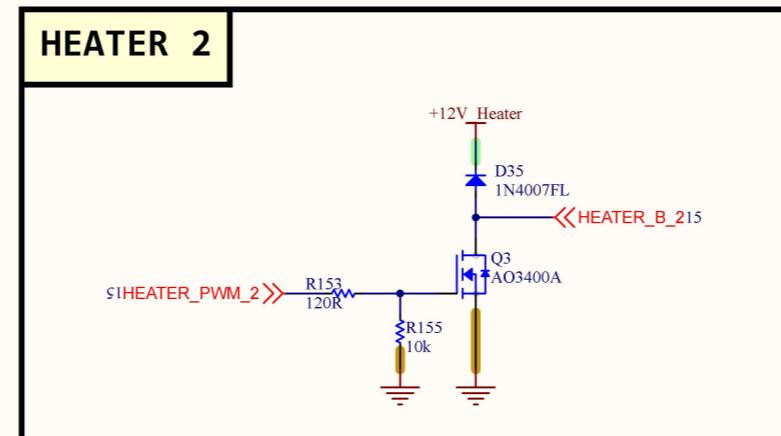
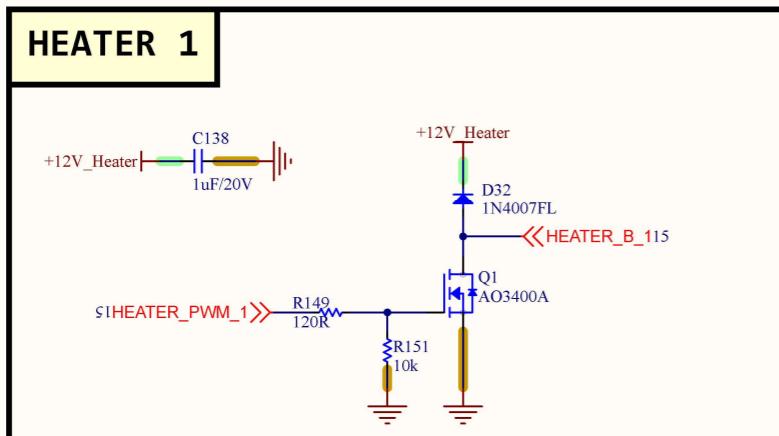
Size: A3

Approved by: Vu Pham

Revision: 1.1.0

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Check lại, coi lại điện áp 12V được không



Project: SLT 6U Satellite

Sub-system: SLT 6U Satellite

Design by: Cam Doan

Sheet title: HEATER.SchDoc

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Checked by: Bao Bui

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Size: A3

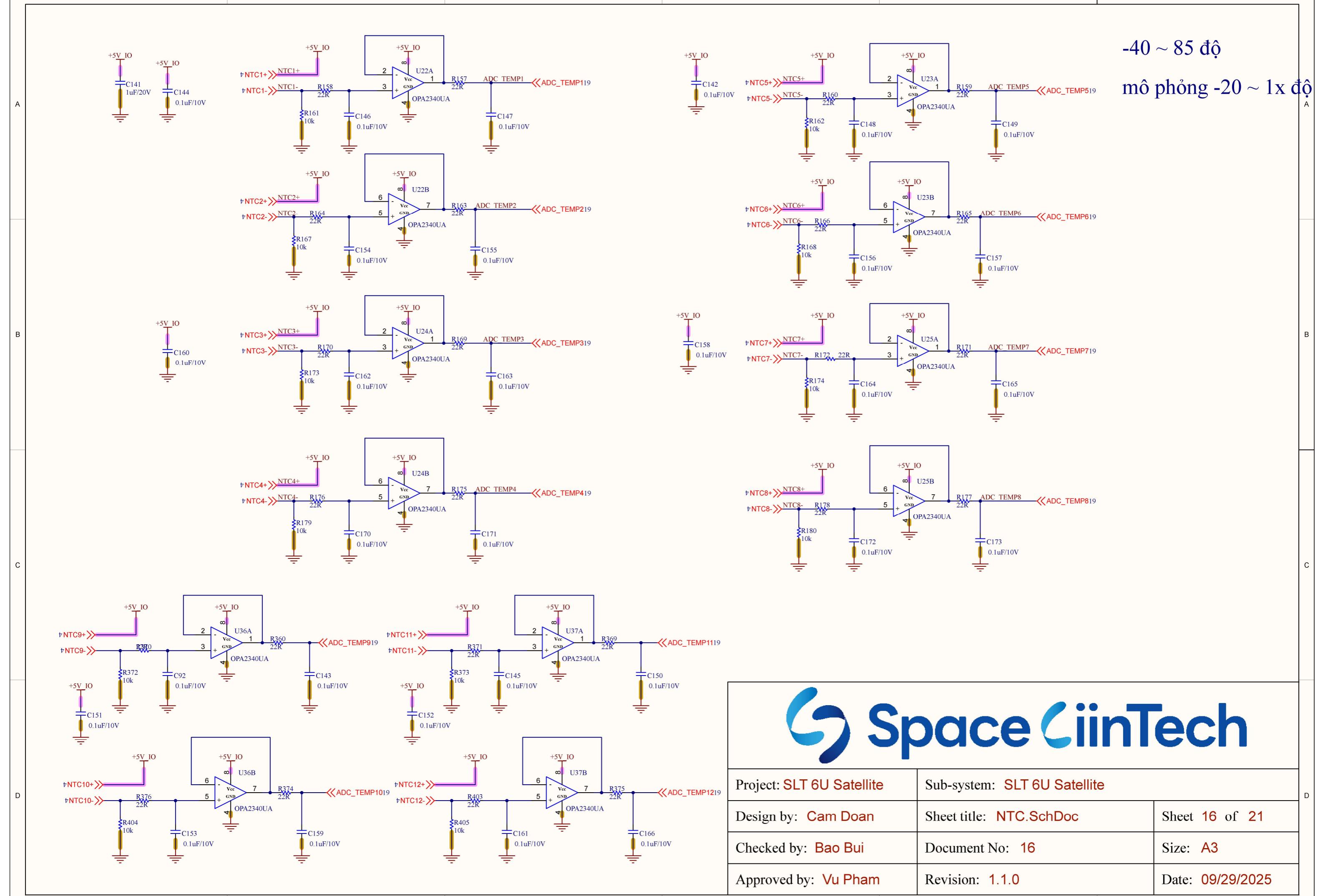
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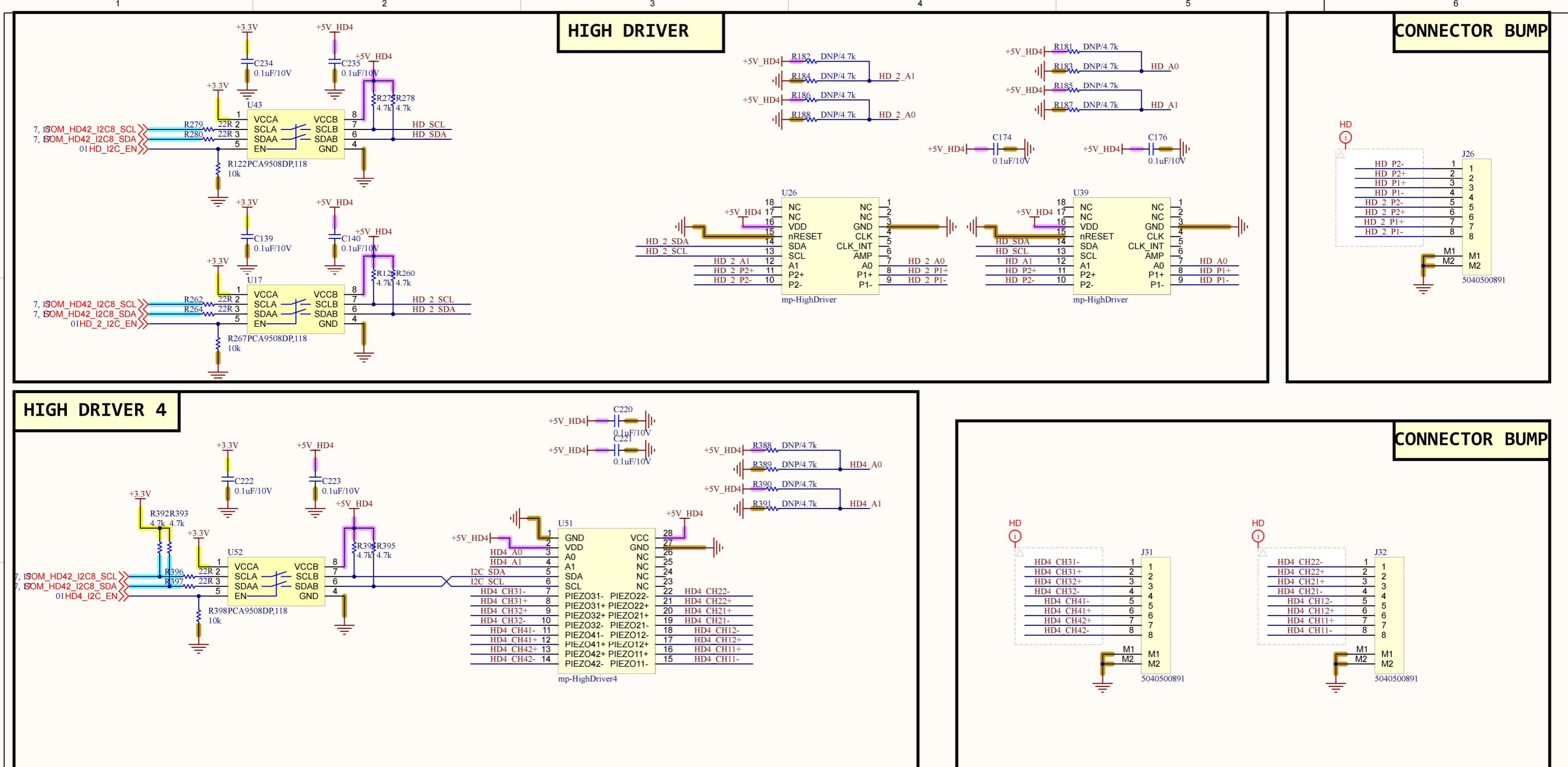
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-40 ~ 85 độ

mô phỏng -20 ~ 1x độ





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Design by: Cam Doan	Sheet title: FLUIDIC_CTRL.SchDoc	Sheet 17 of 21	
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Approved by: Vu Pham	Revision: 1.1.0		Date: 09/29/2025

SOLENOID x12

A

B

C

D

1

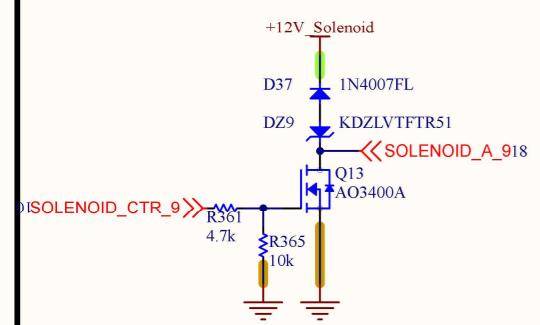
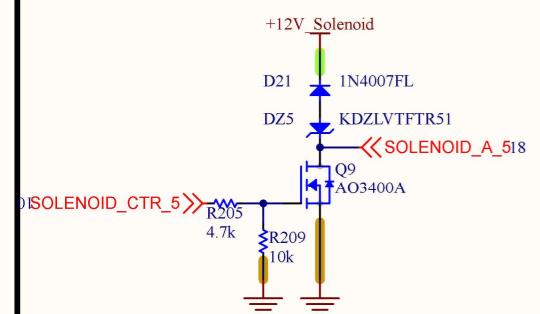
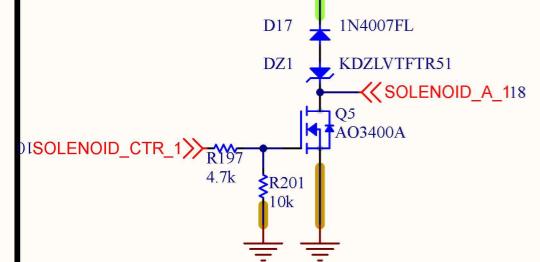
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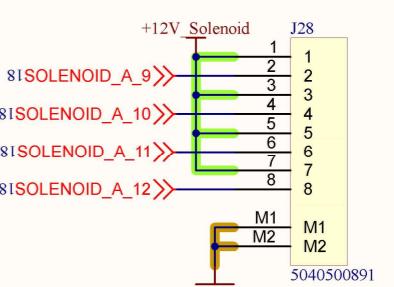
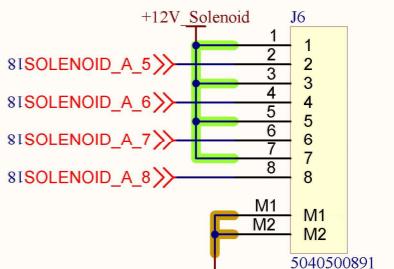
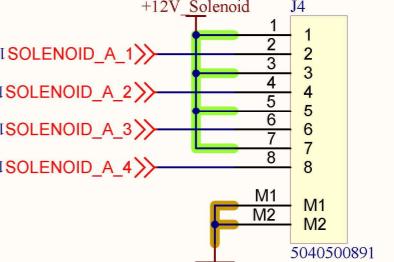
**CONN SOLENOID**

A

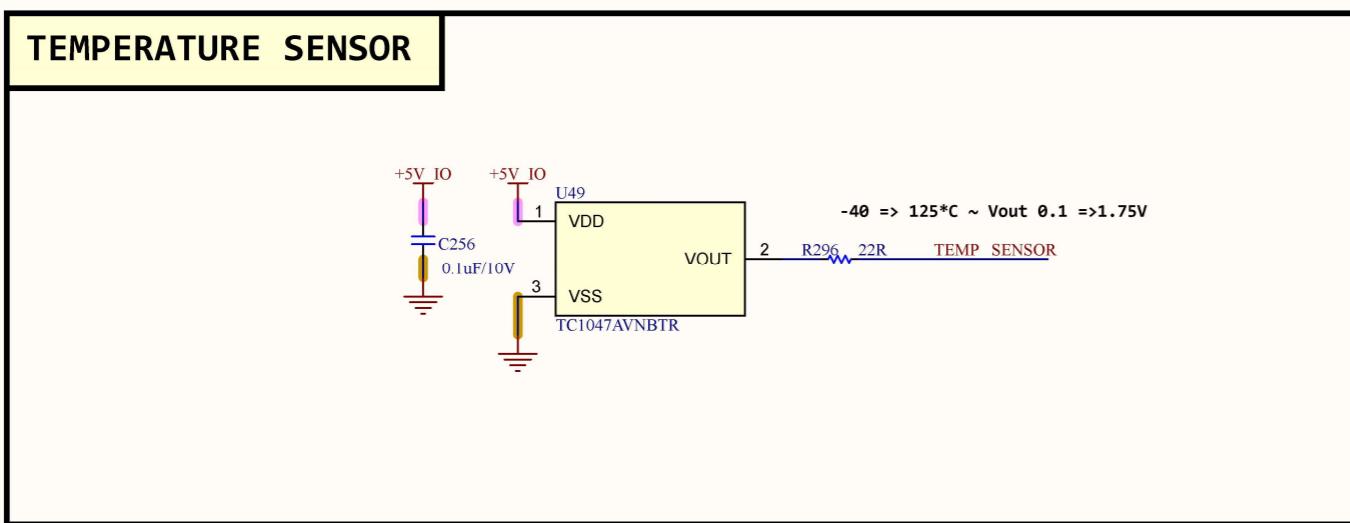
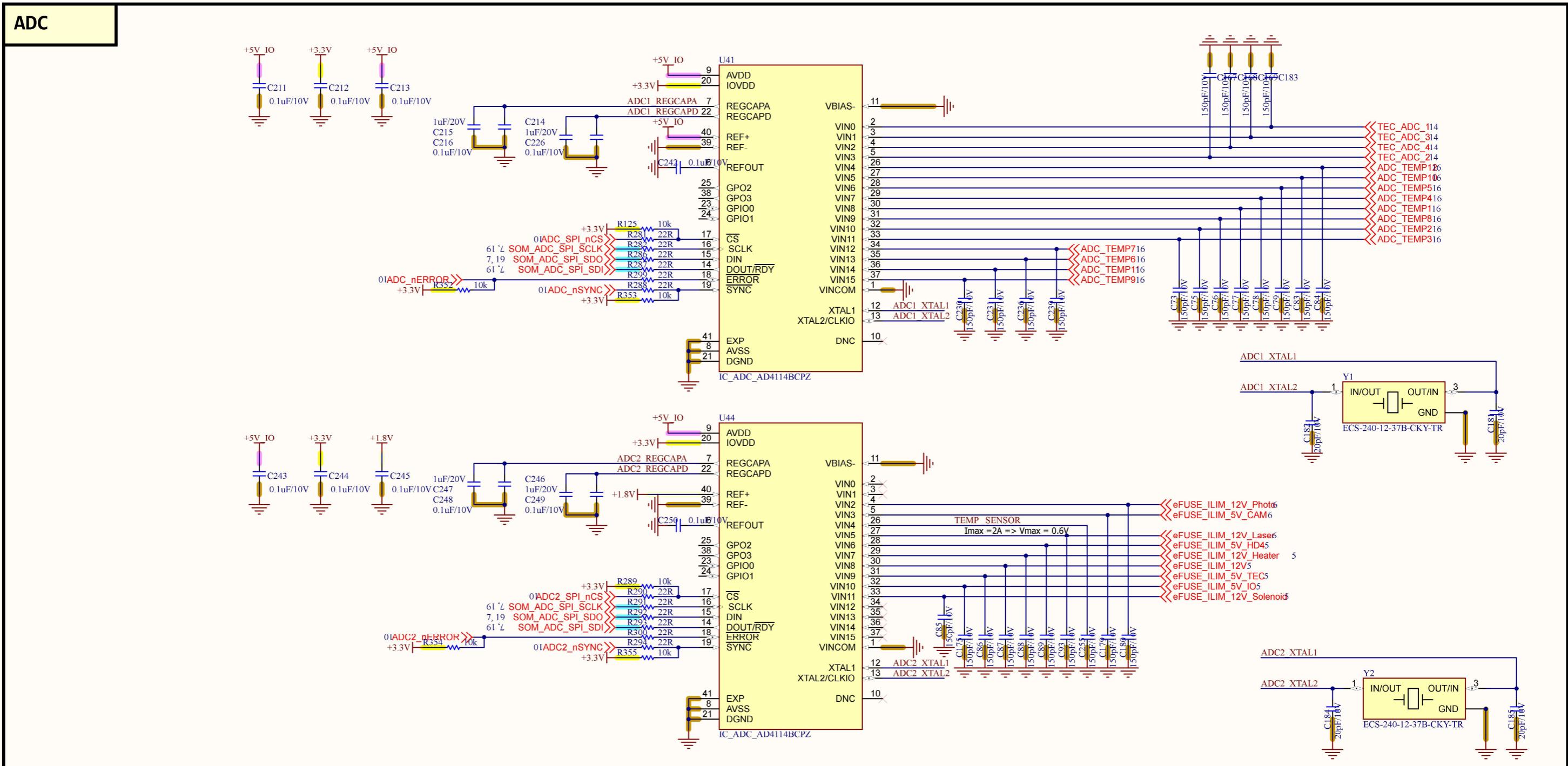
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C

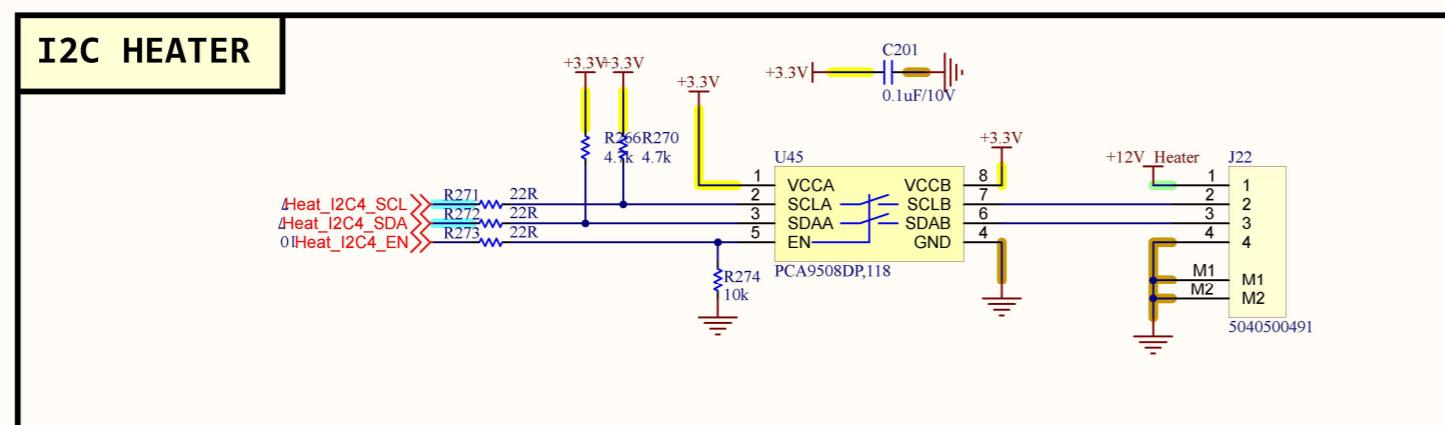
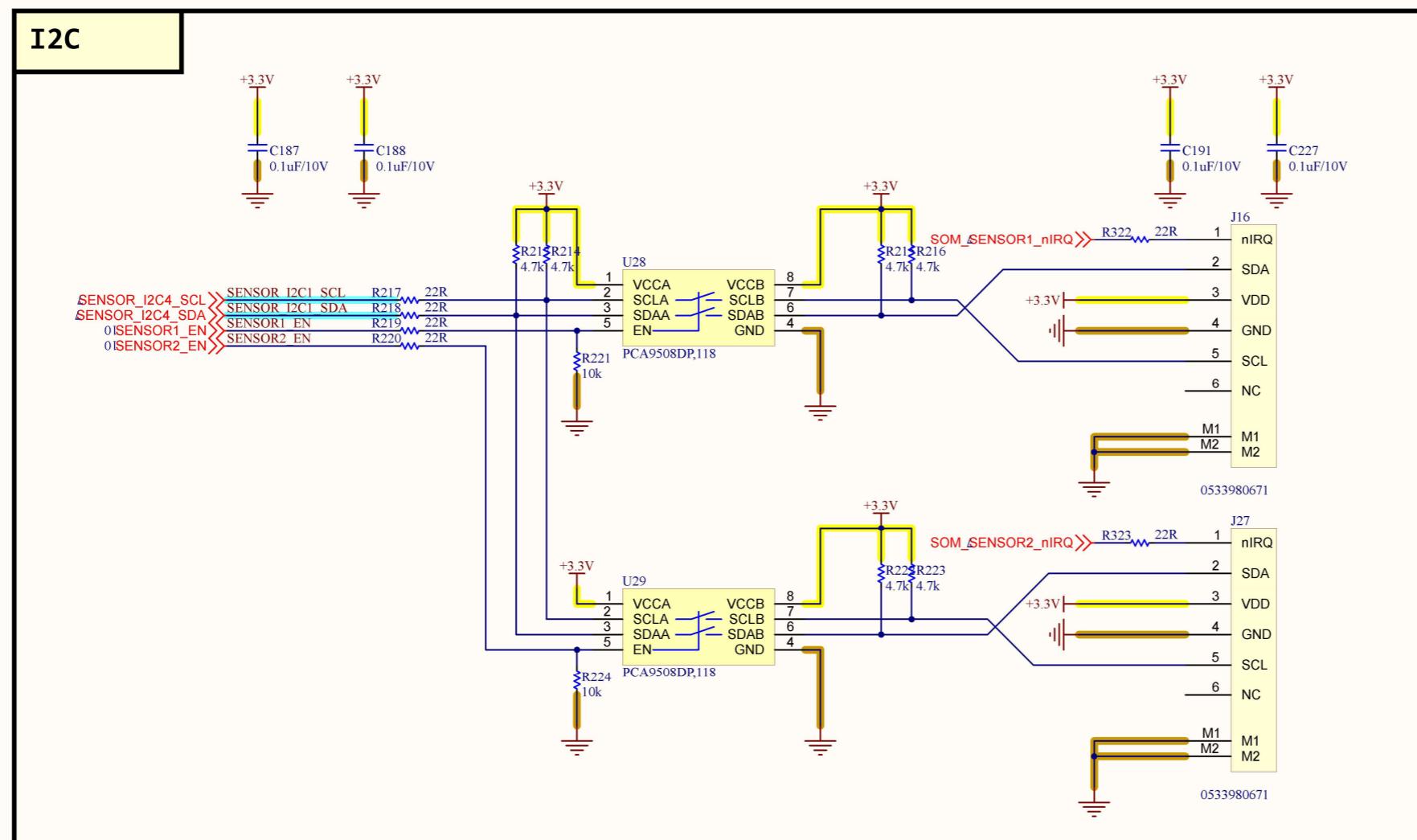
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Approved by: VU PHAM	Revision: 1.1.0	Date: 09/29/2025



Project: SLT 6U Satellite	Sub-system: SLT 6U Satellite	
Design by: Cam Doan	Sheet title: ADC.SchDoc	Sheet 19 of 21
Checked by: Bao Bui	Document No: 19	Size: A3
Approved by: Vu Pham	Revision: 1.1.0	Date: 09/29/2025

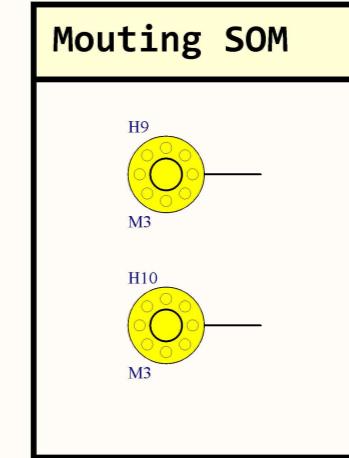
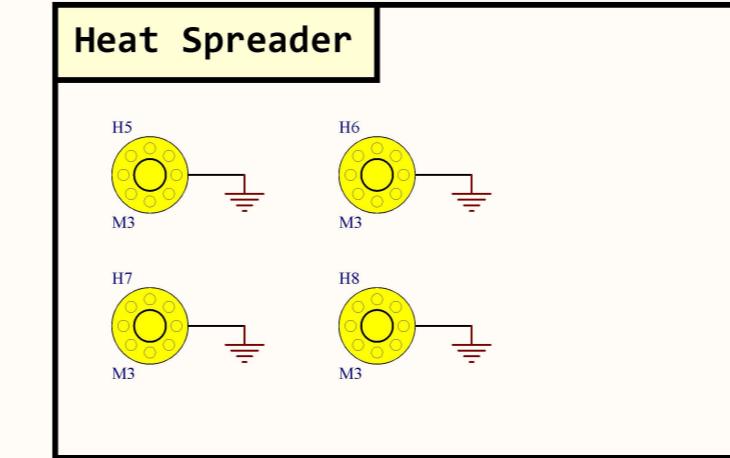
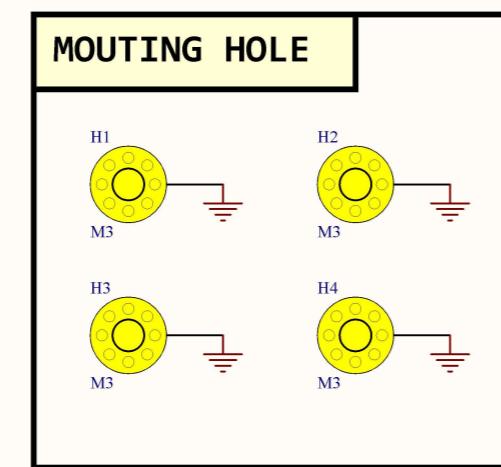


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Project: SLT 6U Satellite	Sub-system: SLT 6U Satellite
Design by: Cam Doan	Sheet title: I2C_Sensor.SchDoc
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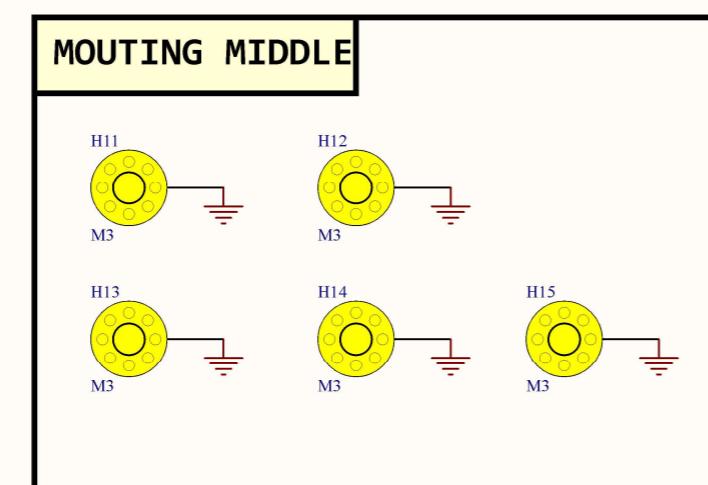
A

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Project: SLT_110.PrjPcb	Sub-system: *	
Design by: Cam Doan	Sheet title: *	Sheet 21 of 21
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