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# NANORACK PHOTODIODE

## Schematic V1.2.1

### History

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001	INDEX
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VERSION	DISCRIPTION
1.0.0	INITIAL PROJECT
1.1.0	REPLACE SWITCHING OBJECT
1.1.1	CHANGE SUPPLY POWER SWITCHING
1.1.2	CHANGE EPAD PIN OF SWITCH TO VSS
1.2.0	ADD HOLES (CENTER OF SIDES)
1.2.1	CHANGE ADG1414 CATEGORY

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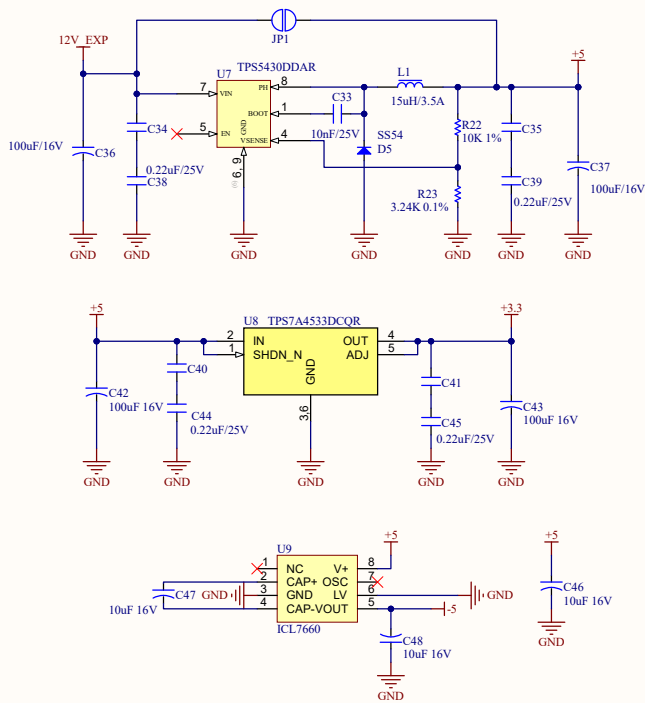
Project: BEE-PC1	Sub-system: NANORACK_PHOTODIODE.PrjPcb	
Design by: Hung Nguyen D.	Sheet title: INDEX.SchDoc	Sheet 1 of 5
Checked by: Bao Bui Q.	Document No: 1	Size: A3
Approved by: Vu Pham	Revision: V1.2.1	Date: 25/5/25

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1	2	3	4	5	6
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D	Cannot open file C:\Users\Public\Documents\Altium\AD21\Templates\SLT C1\SLT C1 좌우.png. File does not exist.				
	Project: BEE-PC1		Sub-system: NANORACK_PHOTODIODE.PrjPcb		
	Design by: Hung Nguyen D.		Sheet title: BLOCK_DIAGRAM.SchDoc		Sheet 2 of 5
	Checked by: Bao Bui Q.		Document No: 2		Size: A3
	Approved by: Vu Pham		Revision: V1.2.1		Date: 25/5/25
1	2	3	4	5	6

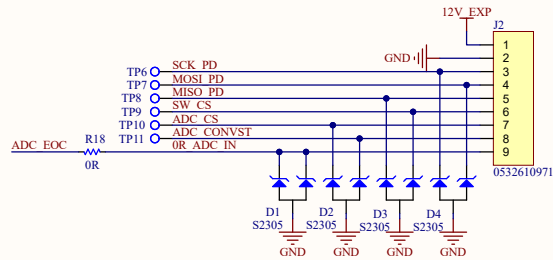
# POWER SWITCH AND LDO



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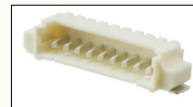
Project: BEE-PC1	Sub-system: NANORACK_PHOTODIODE.PrjPcb	
Design by: Hung Nguyen D.	Sheet title: POWER SWITCH AND LDO.SchDoc	Sheet 3 of 5
Checked by: Bao Bui Q.	Document No: 3	Size: A3
Approved by: Vu Pham	Revision: V1.2.1	Date: 4/9/25

## CONNECTOR



### Molex - 0532610971

- ELECTRICAL:**  
CURRENT RATING: 1A/CONTACT  
VOLTAGE RATING: 125V/CONTACT
- ENVIRONMENTAL:**  
TEMPERATURE RANGE: -40°C TO +105°C
- PHYSICAL:**  
PITCH-MATING: 0.049" (1.25mm)
- WIRE:**  
WIRE SIZE: 24 AWG  
MAX CURRENT: 1A per Wire  
ISULATION MATERIAL: PTFE
- MATING:**  
Molex - 0510210900

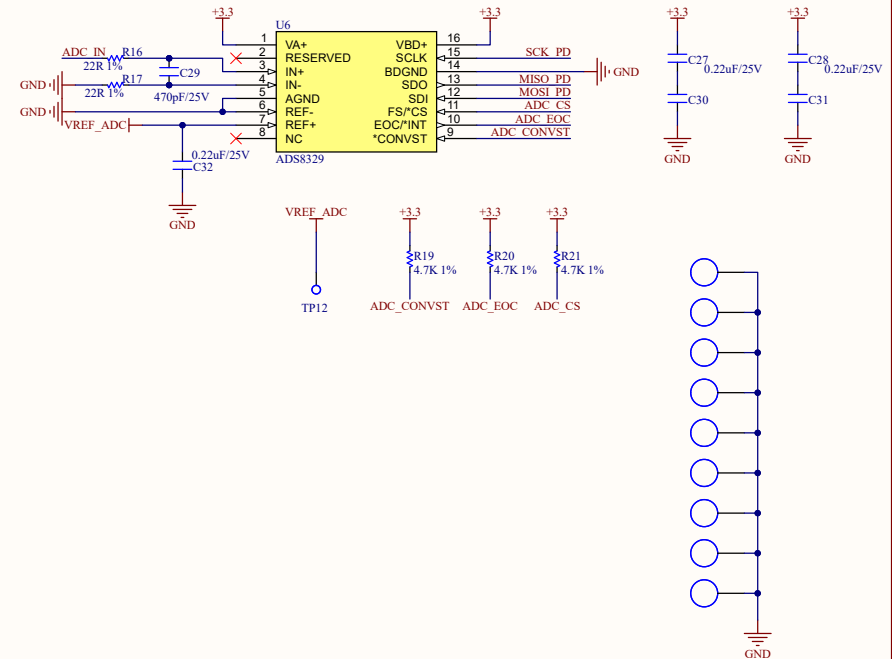


- MATING -



0510210900

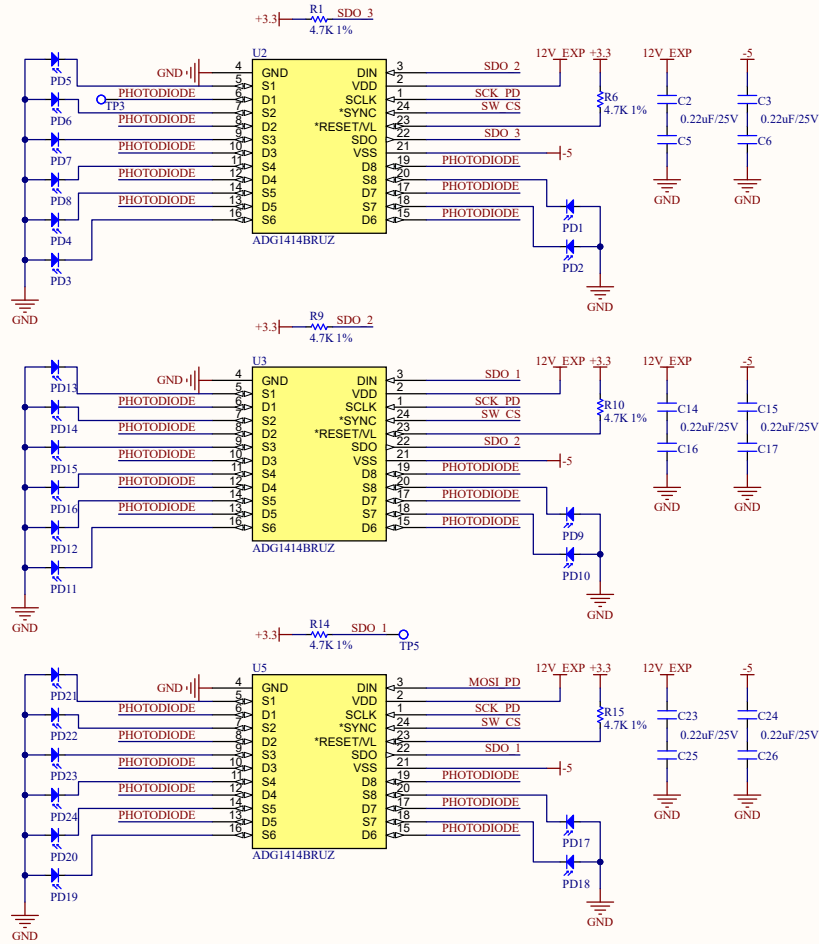
## ADC



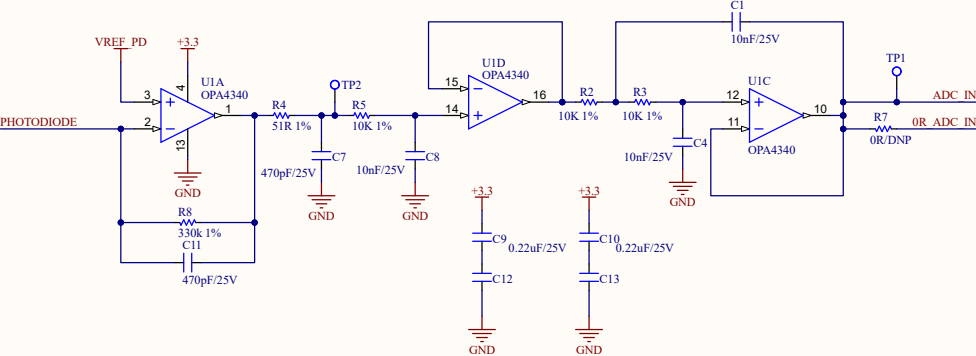
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Project: BEE-PC1	Sub-system: NANORACK_PHOTODIODE.PrjPcb	
Design by: Hung Nguyen D.	Sheet title: COMMUNICATION.SchDoc	Sheet 4 of 5
Checked by: Bao Bui Q.	Document No: 4	Size: A3
Approved by: Vu Pham	Revision: V1.2.1	Date: 14/9/25

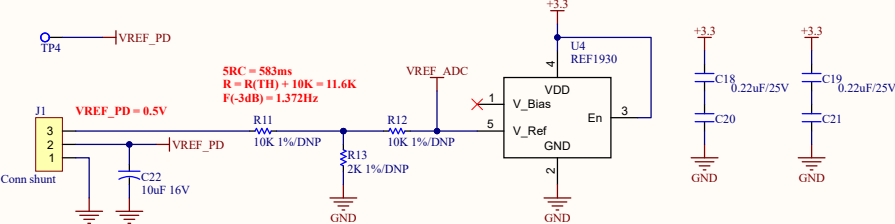
SWITCHING



TRANSIMPEDANCE, ANTI ALIAS



VREF



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Project: BEE-PC1	Sub-system: NANORACK_PHOTODIODE.PrjPcb	
Design by: Hung Nguyen D.	Sheet title: PHOTODIODE.SchDoc	Sheet 5 of 5
Checked by: Bao Bui Q.	Document No: 5	Size: A3
Approved by: Vu Pham	Revision: V1.2.1	Date: 12/9/25