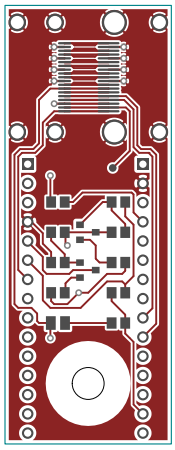
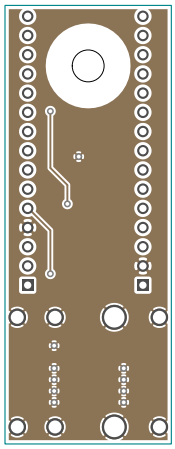
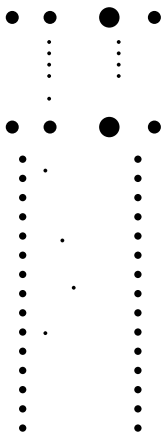
1. Table of Contents (This Page)
   Created on Sat Jun 25 14:44:30 2011
2. top
3. bottom
4. plated-drill
5. unplated-drill
6. componentmask
7. soldermask
8. topsilk
9. bottomsilk
10. toppaste
11. topassembly
12. bottomassembly
13. fab



top, scale = 1:1.000
cec.pcb

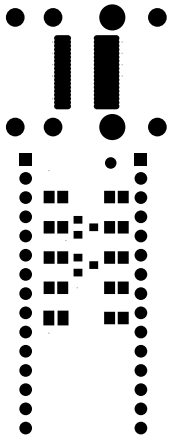


bottom (mirrored), scale = 1:1.000
cec.pcb

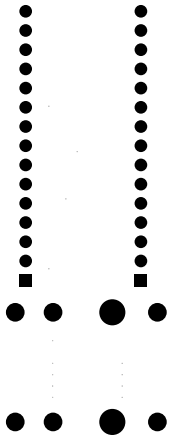


plated-drill, scale = 1:1.000
cec.pcb

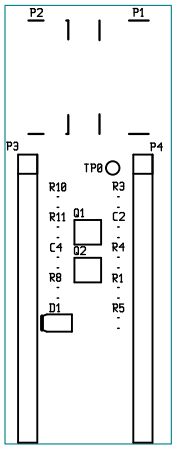
unplated-drill, scale = 1:1.000
cec.pcb



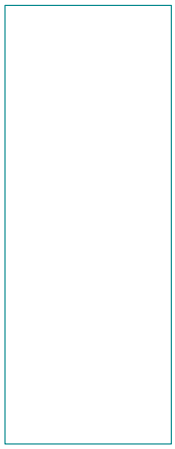
topmask, scale = 1:1.000
cec.pcb



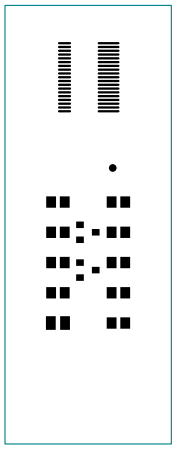
bottommask (mirrored), scale = 1:1.000
cec.pcb



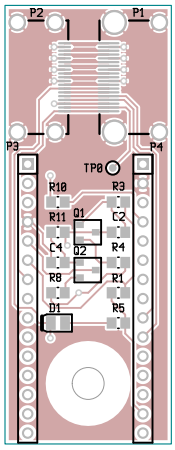
topsilk, scale = 1:1.000
cec.pcb



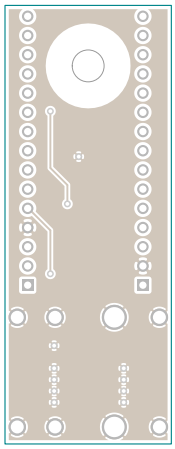
bottomsilk (mirrored), scale = 1:1.000
cec.pcb



toppaste, scale = 1:1.000
cec.pcb



topassembly, scale = 1:1.000
cec.pcb

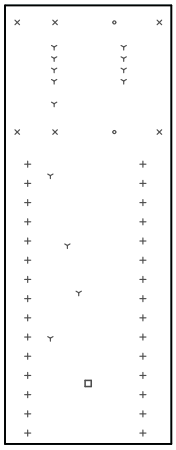


bottomassembly (mirrored), scale = 1:1.000
cec.pcb

There are 5 different drill sizes used in this layout, 52 holes total

Symbol	Diam. (Inch)	Count	Plated?
˅	0.020	13	YES
+	0.038	30	YES
×	0.068	6	YES
°	0.106	2	YES
□	0.166	1	NO

Title: (unknown) - Fabrication Drawing
Author: Russ Dill
Date: Sat 25 Jun 2011 09:44:30 PM GMT UTC
Maximum Dimensions: 866.140000 mils wide, 2283.460000 mi



Board outline is the centerline of this 8.000000 mil rectangle - 0,0 to 866.140000,2283.460000 mils

fab, scale = 1:1.000
cec.pcb