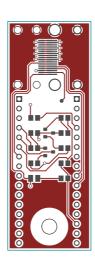
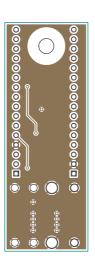
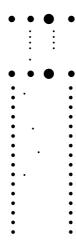
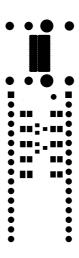
- 1. Table of Contents (This Page) Created on Fri Jun 24 01:50:32 2011
- 2. top
- 3. bottom
- 4. plated-drill
- 5. unplated-drill
- 6. componentmask
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- 9. bottomsilk
- 10. toppaste
- 11. topassembly
- 12. bottomassembly
- 13. fab

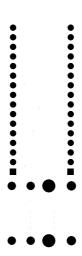


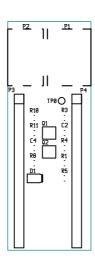


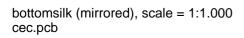


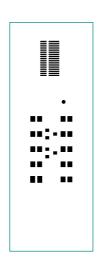
unplated-drill, scale = 1:1.000 cec.pcb

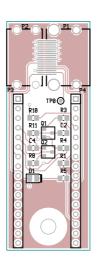


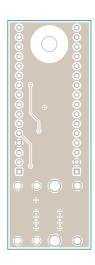






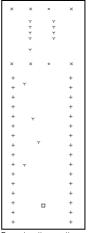






There are 5 different drill sizes used in this layout, 54 holes total

Symbol Diam.(Inch) Count Plated? 0.020 YES 13 Title: (unknown) - Fabrication Drawing 0.038 32 YES 0.068 YES 6 Author: Russ Dill 0.106 YES Date: Fri 24 Jun 2011 08:50:32 AM GMT UTC 0.158 NO Maximum Dimensions: 866.140000 mils wide, 2381.880000 mil



Board outline is the centerline of this 8.000000 mil rectangle - 0,0 to 866.140000,2381.880000 mils