Faculty of Engineering and Technology
Electrical and Computer Engineering Department
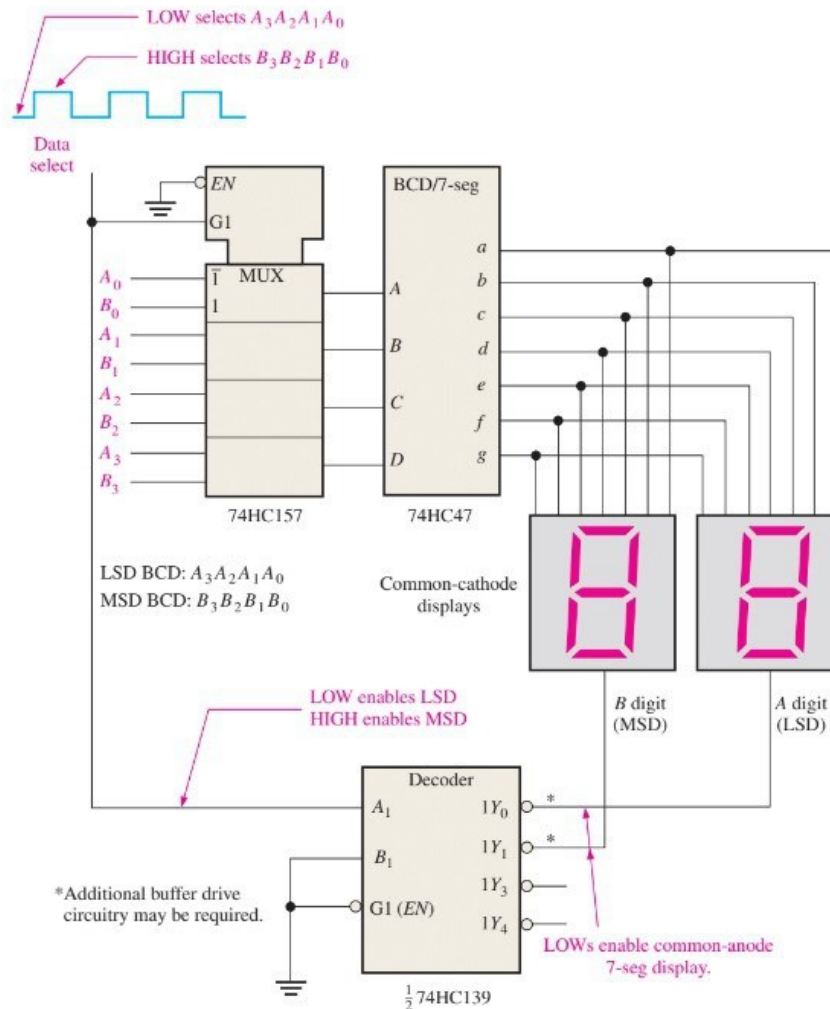Advanced Digital Design ENCS3310

Homework#1

For the following circuit
  a. Write a Verilog description for the Quad_Mux2x1.
  b. Write a Verilog description for the BCD_7Segment_driver.
  c. Write Verilog Description for the Decoder2x4
  d. Build the circuit structurally from the above components.
  e. Write a simple test bench for the circuit.

FIGURE 6–49 Simplified 7-segment display multiplexing logic.