

Programming the 8085: 8085 Microprocessor, Bus Structure and Timings, Demultiplexing the bus, Addressing Modes and Instruction set, 8085 Interrupts, Programming the 8086: Software Model of 8086, Memory address Space and Data Organization, Segment Registers and Memory Segmentation, Dedicated and General Use of Memory, Addressing Modes, Instruction Set, Interrupt Processing, Minimum/ Maximum Mode, Interfacing Peripherals and Applications: Digital to Analog Converters, Analog to Digital Converters, 8255 PPI, 8279 Keyboard Display Interface, 8253 Programmable Interval Timer, 8259 Programmable Interrupt Controller, 8237 DMA Controller, Introduction to 80186, 80286, 80386 and 80486 Microprocessors, Registers, Memory Management.

References:

1. Ramesh Gaonkar, Microprocessor Architecture, Programming and Applications with the 8085, Wiley Eastern Limited, 2002
2. Barry B. Brey, C.R.Sharma, The Intel Microprocessors: Architecture, Programming and Interfacing, Pearson Education, 2005
3. Sunil Mathur, Microprocessor 8086: Architecture, Programming and Interfacing, PHI, 2011
4. Douglas V. Hall, Microprocessor and Interfacing, McGraw Hill Company, 2005.