LT8648S Supply Design Summary Report

Vin: 9V (min.), 12V (nom.), 15V (max.)

Output Rails : Vout1 = 4.98V / 6A (max.)

Project Name: SURFv6

Project Date : 1/31/22

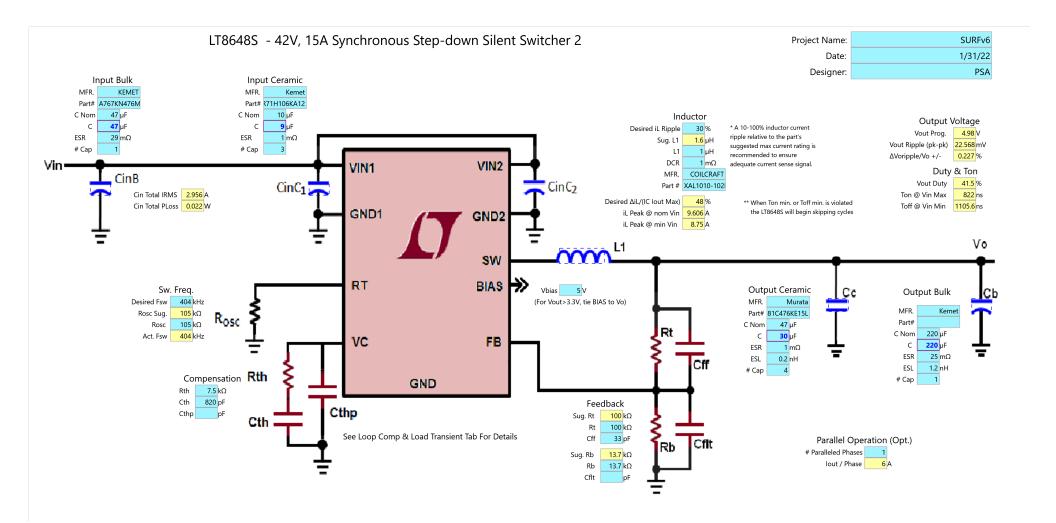
Designer: PSA





LT8648S Solution - Simplified Schematic

Vin: 9V (min.), 12V (nom.), 15V (max.) Output Rails: Vout1 = 4.98V / 6A (max.)

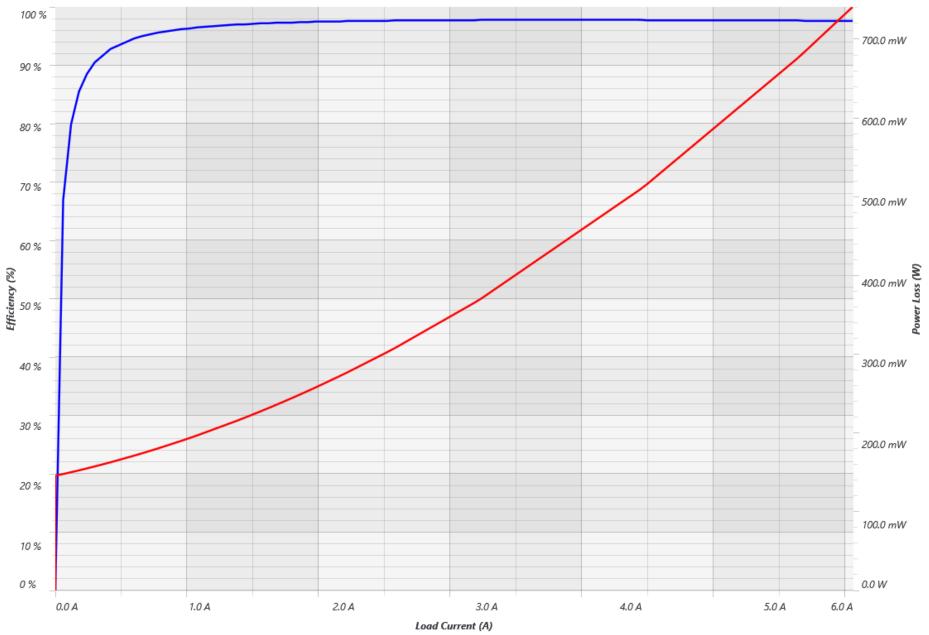


LT8648S Solution - Efficiency & Loss Estimations

Rail # 1 : Vin = 12V, Vout1 = 4.98V

* Estimations For CCM Mode Only. Inductor AC Losses Entered by User

Rail #1 (4.98V) Efficiency & Power Loss



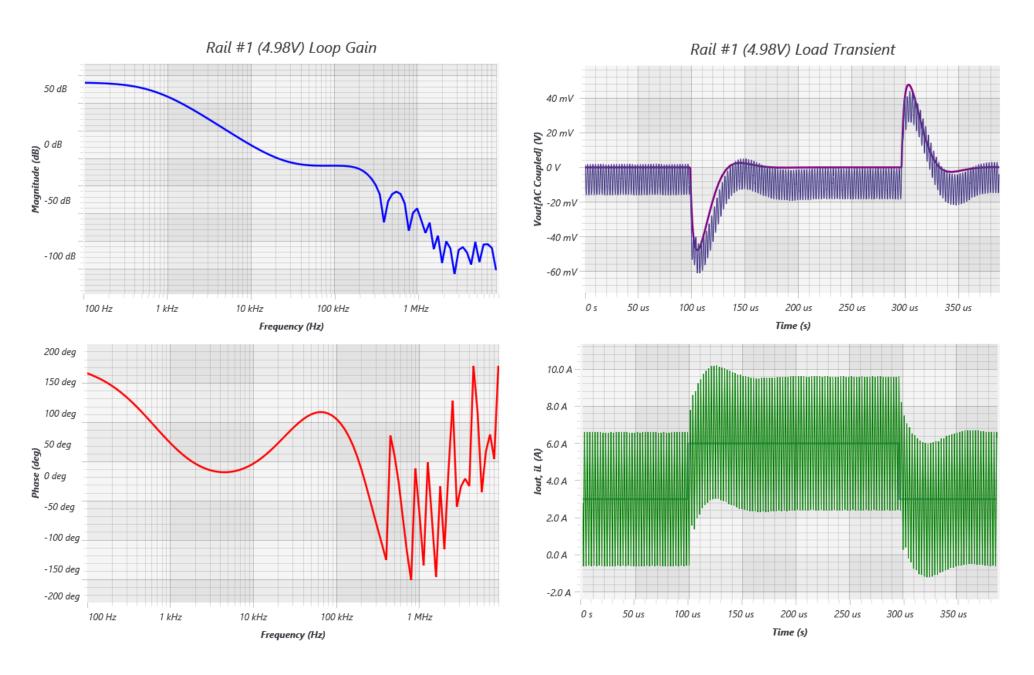
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LT8648S Solution - Loop Gain & Load Transient Estimations

Rail # 1 : Vin = 12V, Vout1 = 4.98V, Iout1 = 6A

* Estimations For CCM Mode Only. Estimations Based On Small Signal Avg. Model



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LT8648S Solution - Summary

LT8648S Supply Design Summary **Project Info:** SURFv6, 1/31/22, PSA **Design Specifications** Steady State: Rail # ΔVo Vin Min. Vin Nom. Vin Max. Fsw Vo ΔVo% lo Max ΔiLp-p ΔiL% iLpk Duty Ton min. Toff min. 41.5 % 12 V 404 kHz 4.98 V 22.57 mV 0.2 % 6 A 7.21 A 48 % 9.61 A 822 ns 1106 ns **Efficiency and Loop:** Loop BW Rail # Vo Iomax Eff.@lomax PLoss@lomax Loop PM Step Low Step High Step Slew ΔVo@Step ΔVo@Step % 4.98 V 6 A 97.61 % 0.731 W 22.39 kHz 79.31 dea 3 A 6 A 100 A/µs 60.990848521 +/-1.2 % **Recommendations and Warnings:** Rail #1 Capacitance value is not de-rated. Inadequate information provided to estimate de-rated value. Please check the capacitor's entries in library. Rail #1 Capacitance value is not de-rated. Inadequate information provided to estimate de-rated value. Please check the capacitor's entries in library. Rail #1 Capacitance value is not de-rated. Inadequate information provided to estimate de-rated value. Please check the capacitor's entries in library. Rail #1 Capacitance value is not de-rated. Inadequate information provided to estimate de-rated value. Please check the capacitor's entries in library. **Power Components** Export BOM **Power Components Bill Of Materials:** Ref. Des. Value Quantity Description Mfr. Name Mfr. Part # Pkg. (Imperial) L(mm) W(mm) H(mm) User Note U1 LINEAR TECH LT8648S 0.94 Lo1 1µH IND COILCRAFT XAL1010-102MEB 11.3 10 10 Cinb1 47µF CAP KEMET A767KN476M1HLAE029 Cinc1 Cinc2 Cinc3 10µF CAP Kemet GRM32ER71H106KA12 v 0 Cob1 220µF CAP Kemet Coc1 Coc2 Coc3 Coc4 47µF CAP Murata GRM32EC81C476KE15L **Power Components Footprint:** 11 # Components Component Clearance (d Max. Height 10 mm Component Clearance (d) 1.5 mm

Part # 2

* Notes:

* Power Components Area (Excludes ICs)

* Power Components Area (Includes ICs)

1. The calculated power component area is only the simple sum of component footprint areas with given clearance, assuming all power components are on the same side of PCB. It is NOT the final PCB size with layout design.

Part #1

d/2

d

2. Component count should change with the number of paralleled phases.

147.2

0.228

194

0.301

mm^2

mm^2

in^2

in^2

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