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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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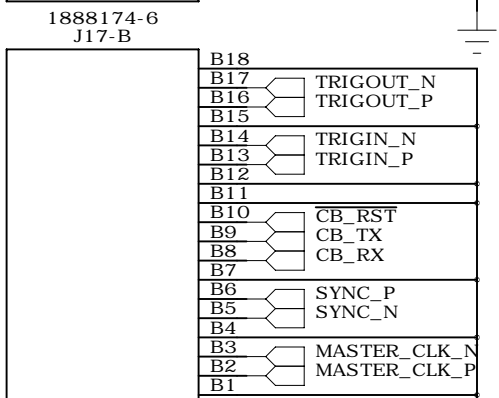
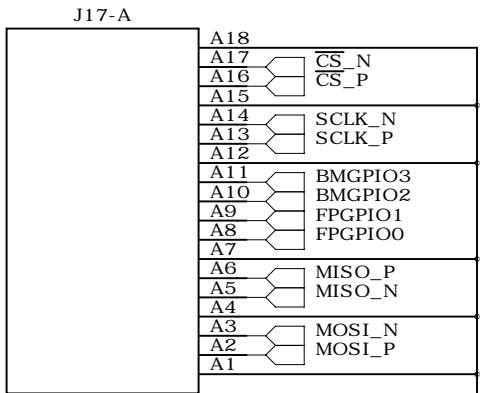
C

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SFF-8087 connector

Cable is designed with 8 differential pairs

TRIGIN+/-

TRIGOUT+/-

CLK+/-

SYNC+/-

MOSI/GND

MISO/GND (these are implemented as differential pairs!)

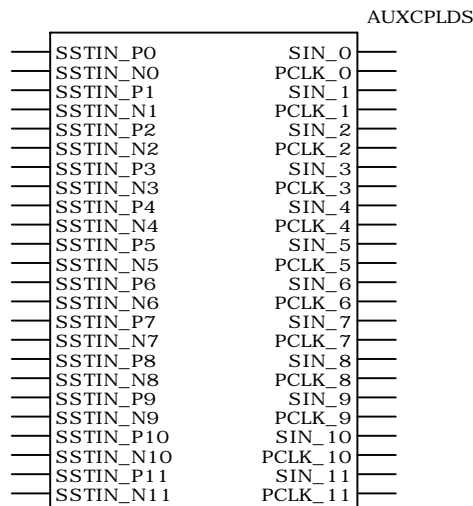
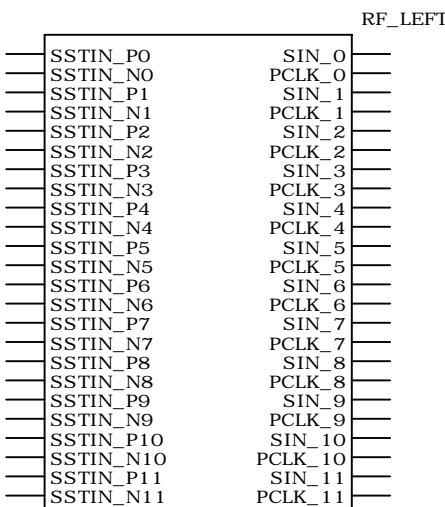
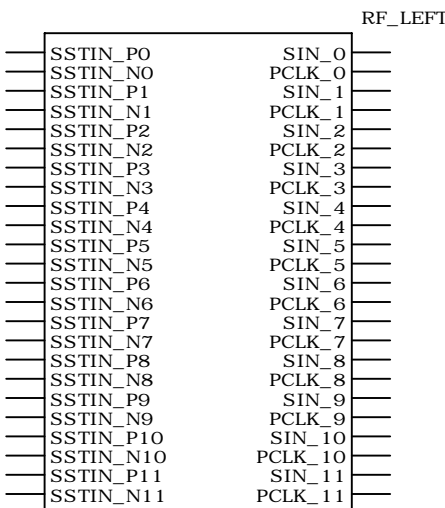
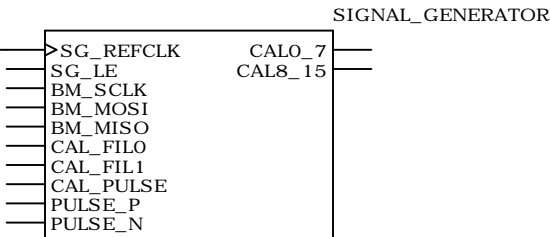
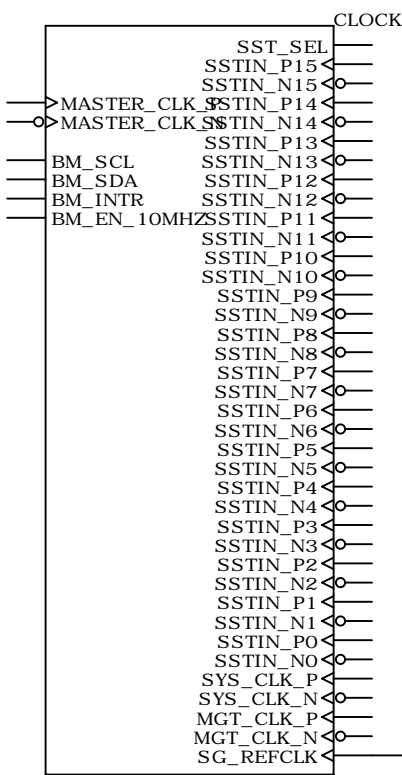
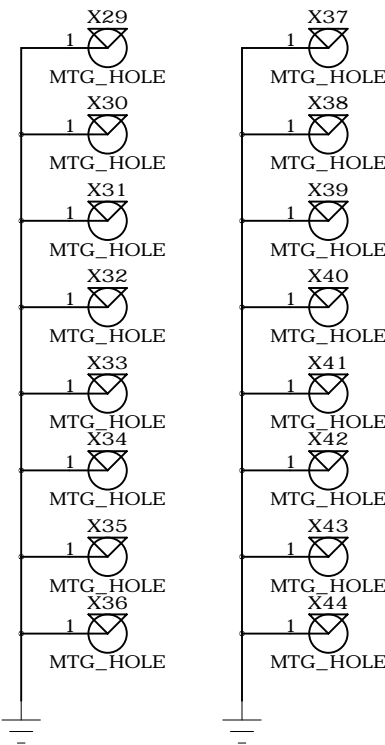
SCLK/GND (allows for a higher-speed FPGA-to-FPGA link)

CS/GND (if used single ended, just drive N side to ground in FPGA)

There are also 8 sideband signals

RX/TX/RST/GND/GPIO[3:0]

At some point I'll finish a top block diagram...

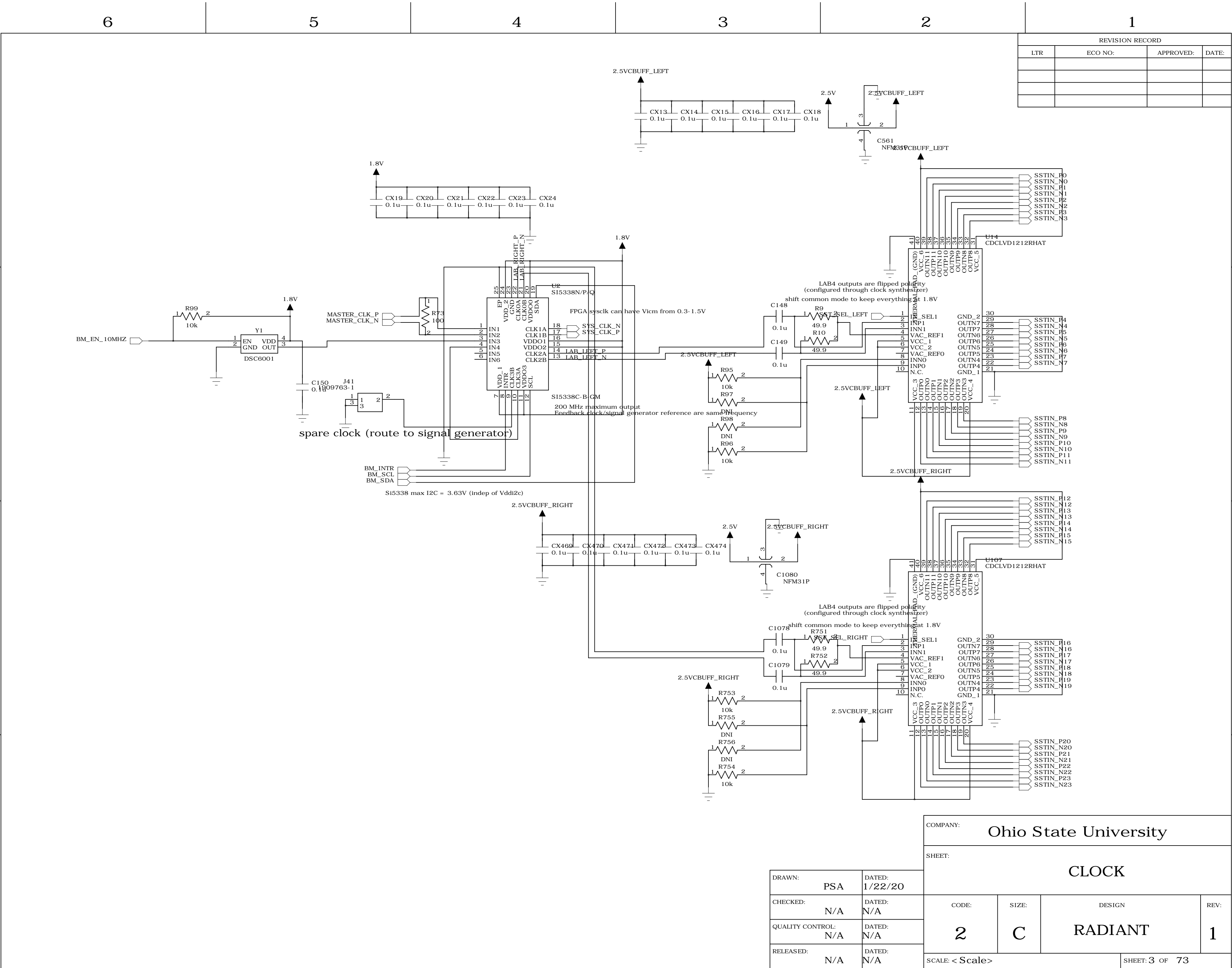


AUXCPLDS

SHEET-41

DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
QUALITY CONTROL:	N/A	DATED:	N/A
RELEASED:	N/A	DATED:	N/A

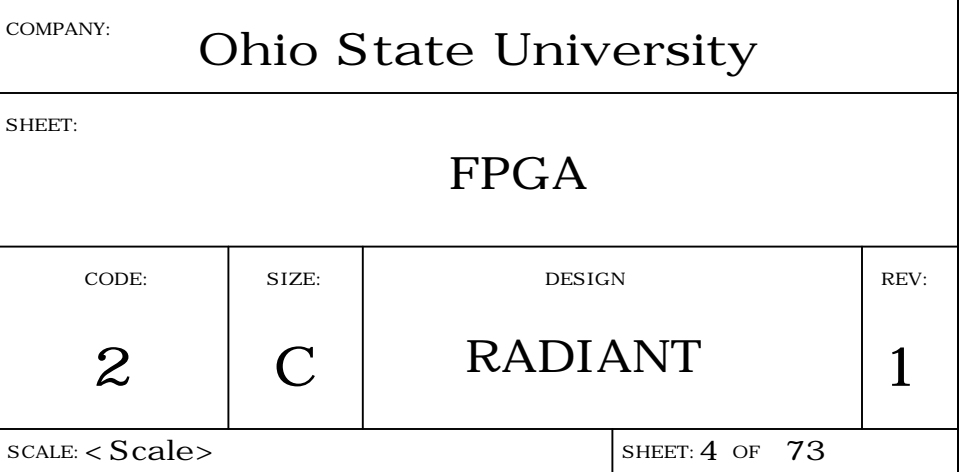
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SHEET: TOP			
CODE:	SIZE:	DESIGN:	REV:
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SCALE: < Scale >			SHEET: 1 OF 73

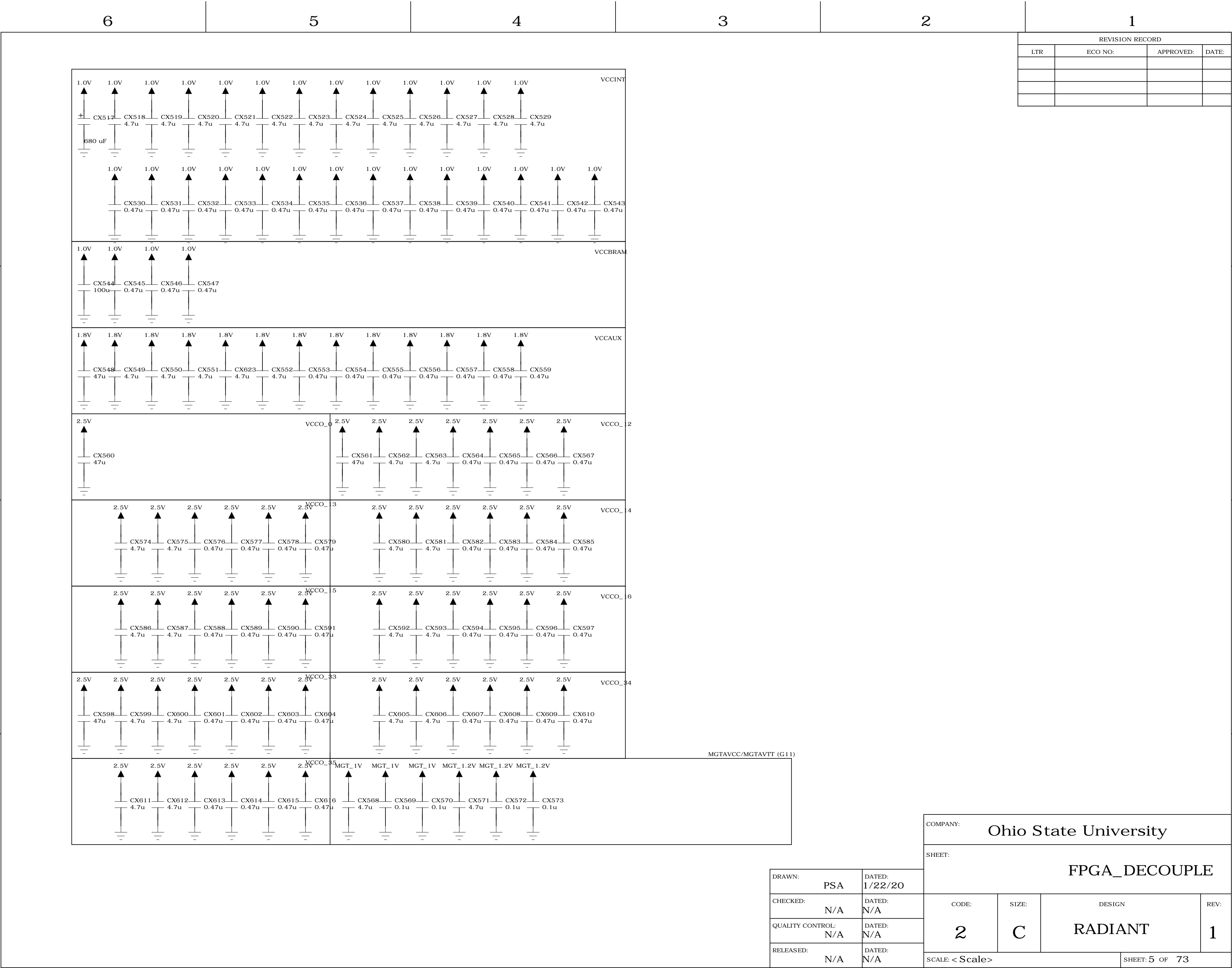


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Ohio State University			
SHEET: CLOCK			
CODE: 2	SIZE: C	DESIGN: RADIANT	REV: 1
SCALE: < Scale >			SHEET: 3 OF 73

DRAWN: PSA	DATED: 1/22/20
CHECKED: N/A	DATED: N/A
QUALITY CONTROL: N/A	DATED: N/A
RELEASED: N/A	DATED: N/A





2.5V

CX574

4.7u

2.5V

CX575

4.7u

2.5V

CX576

0.47u

2.5V

CX577

0.47u

2.5V

CX578

0.47u

2.5V

CX579

0.47u

VCCO_3

2.5V

CX580

4.7u

2.5V

CX581

4.7u

2.5V

CX582

0.47u

2.5V

CX583

0.47u

2.5V

CX584

0.47u

2.5V

CX585

0.47u

VCCO_4

2.5V

CX586

4.7u

2.5V

CX587

4.7u

2.5V

CX588

0.47u

2.5V

CX589

0.47u

2.5V

CX590

0.47u

2.5V

CX591

0.47u

VCCO_5

2.5V

CX592

4.7u

2.5V

CX593

4.7u

2.5V

CX594

0.47u

2.5V

CX595

0.47u

2.5V

CX596

0.47u

2.5V

CX597

0.47u

VCCO_6

2.5V

CX598

47u

2.5V

CX599

4.7u

2.5V

CX600

4.7u

2.5V

CX601

0.47u

2.5V

CX602

0.47u

2.5V

CX603

0.47u

2.5V

CX604

0.47u

VCCO_33

2.5V

CX605

4.7u

2.5V

CX606

4.7u

2.5V

CX607

0.47u

2.5V

CX608

0.47u

2.5V

CX609

0.47u

2.5V

CX610

0.47u

VCCO_34

2.5V

CX611

4.7u

2.5V

CX612

4.7u

2.5V

CX613

0.47u

2.5V

CX614

0.47u

2.5V

CX615

0.47u

2.5V

CX616

0.47u

VCCO_35

MGT_1V

CX568

4.7u

MGT_1V

CX569

0.1u

MGT_1V

CX570

0.1u

MGT_1.2V

CX571

4.7u

MGT_1.2V

CX572

0.1u

MGT_1.2V

CX573

0.1u

MGTAVCC/MGTAVTT (G11)

REVISION RECORD

LTR

ECO NO:

APPROVED:

DATE:

DRAWN:PSA

CHECKED:N/A

QUALITY CONTROL:N/A

RELEASED:N/A

DATED:1/22/20

DATED:N/A

DATED:N/A

DATED:N/A

COMPANY:Ohio State University

SHEET:FPGA_DECOUPLE

CODE:2

SIZE:C

DESIGNRADIANT

REV:1

SCALE: < Scale>

SHEET: 5 OF 73

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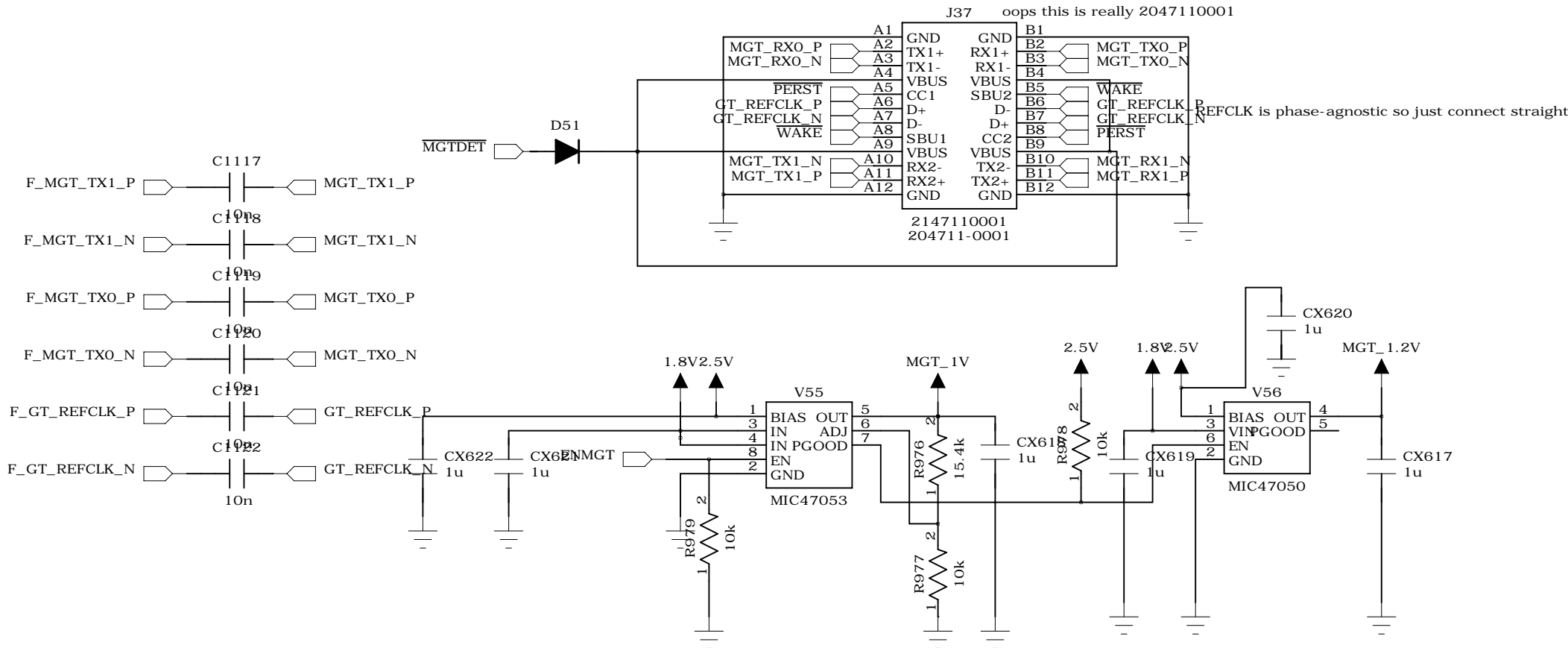
3

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

This is NOT USB-C!!
MUST use custom cable or adapter board to go to PCIe
Note that a USB-A to USB-C cable likely WON'T work
since the cheapo adapters use USB VCC/GND weirdly
Tie VBUS to ground on custom cable/adapter to enable MGT power.



MGT power is off by default. If board manager detects plugged in cable/board, it enables in proper sequence

COMPANY: Ohio State University			
SHEET: FPGA-PCIE			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 6 OF 73

DRAWN:	DATED:
PSA	1/22/20
CHECKED:	DATED:
N/A	N/A
QUALITY CONTROL:	DATED:
N/A	N/A
RELEASED:	DATED:
N/A	N/A

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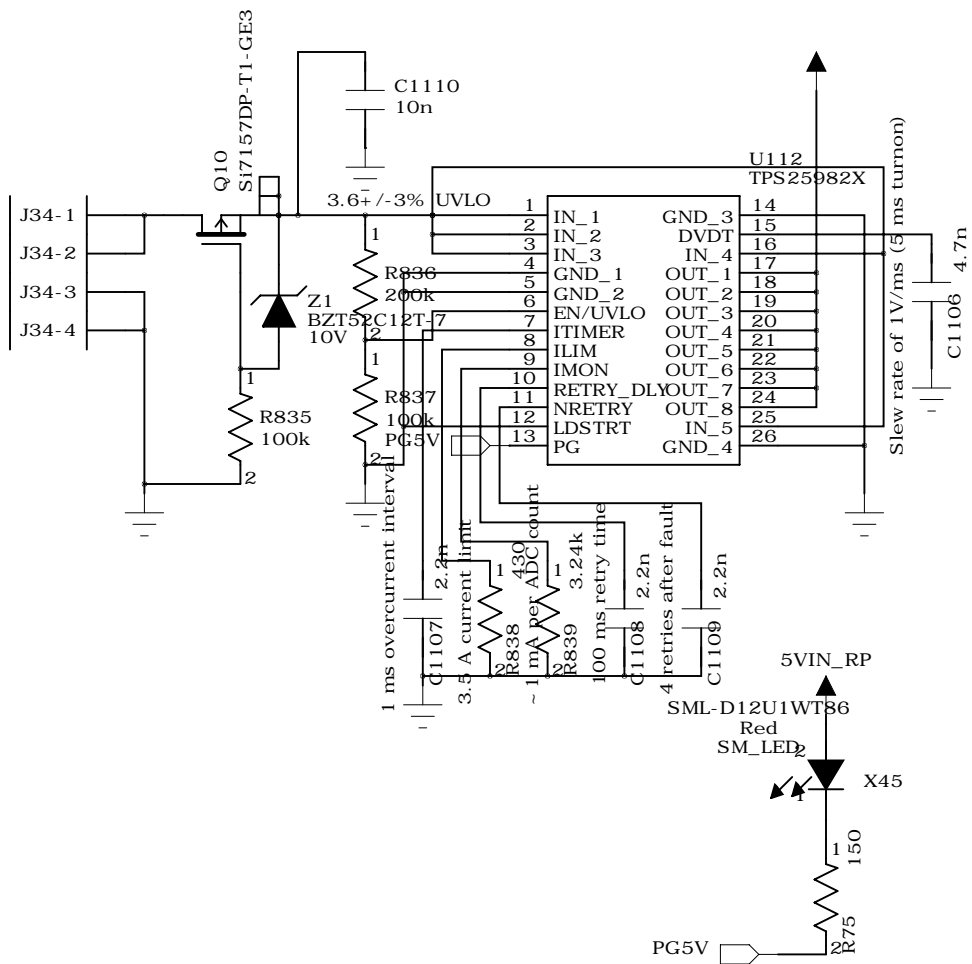
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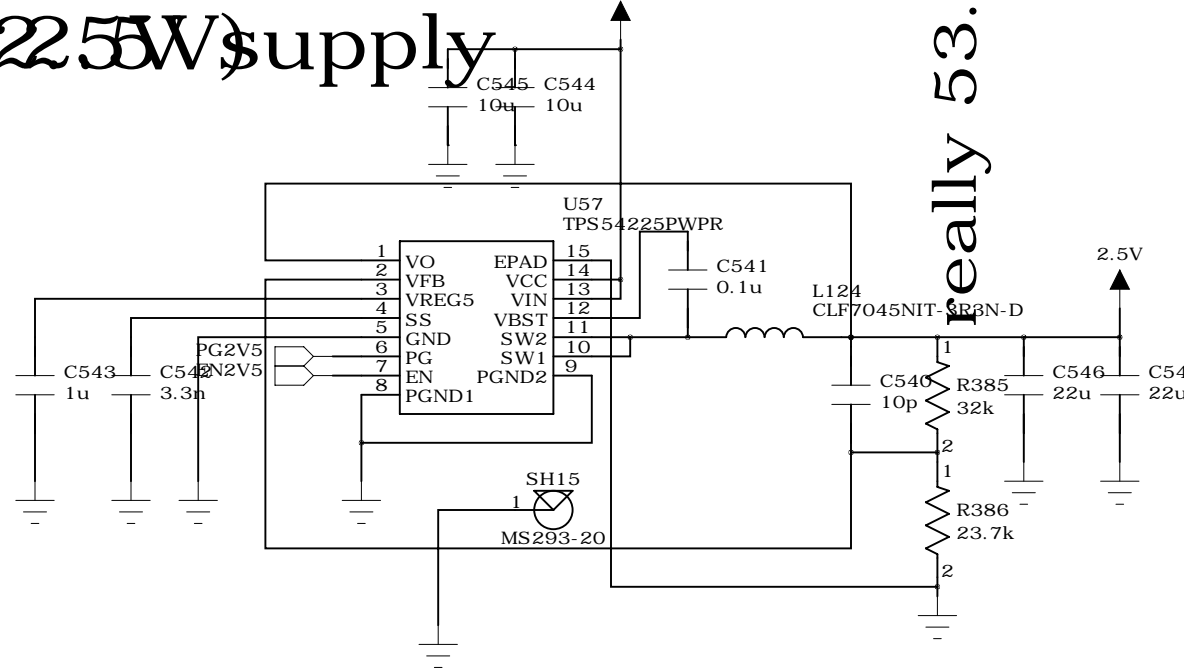
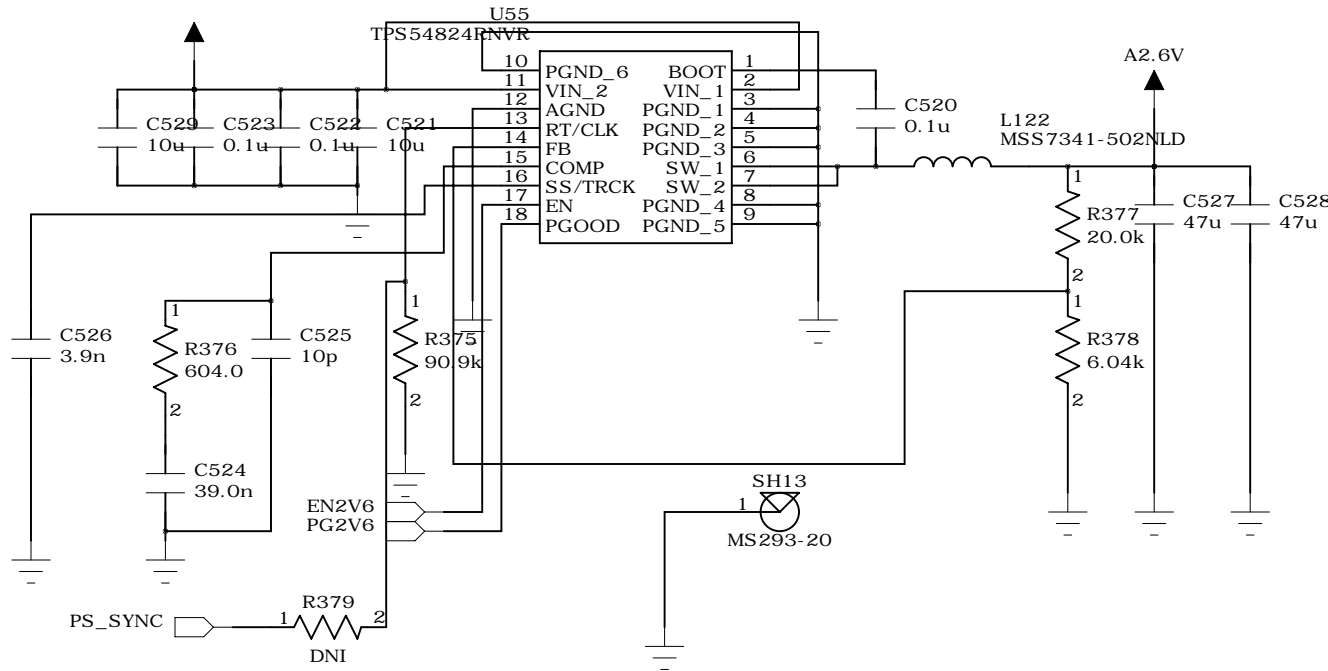
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

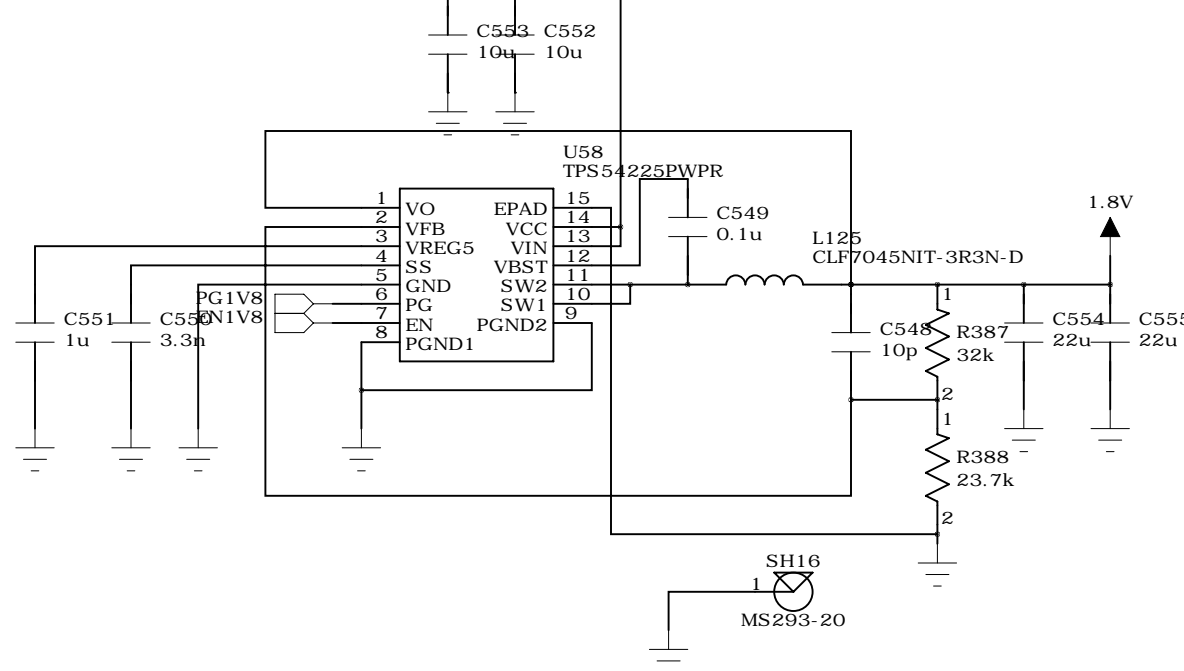
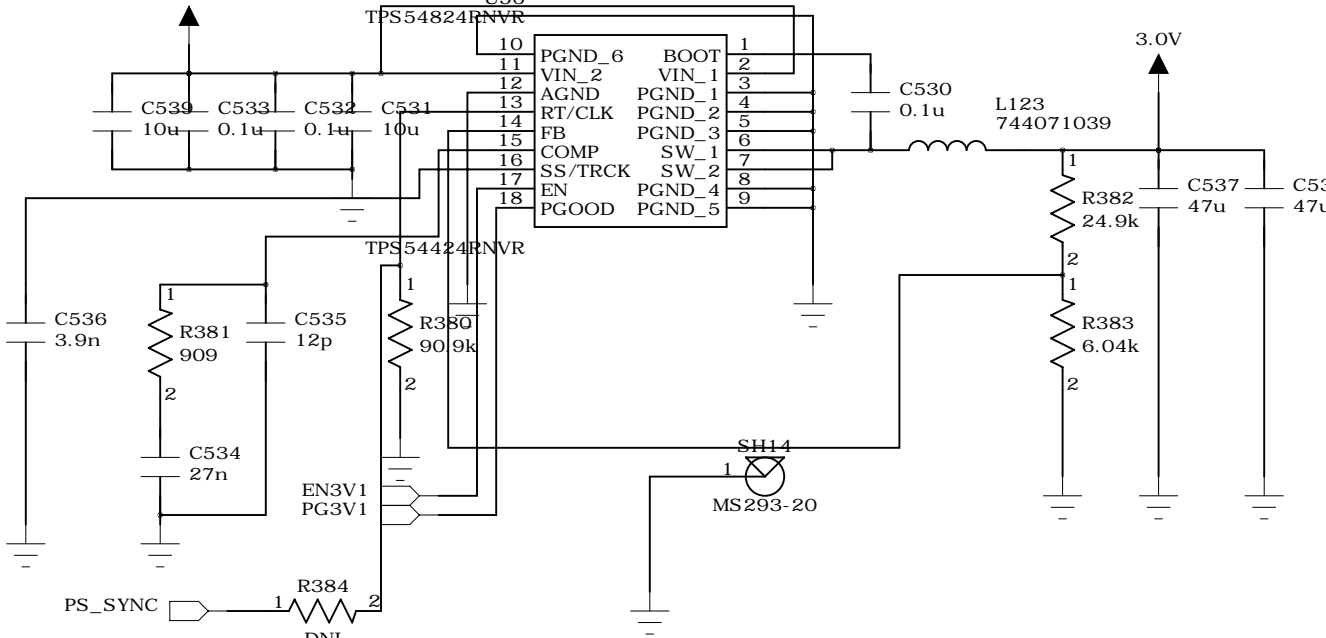
Input power protection



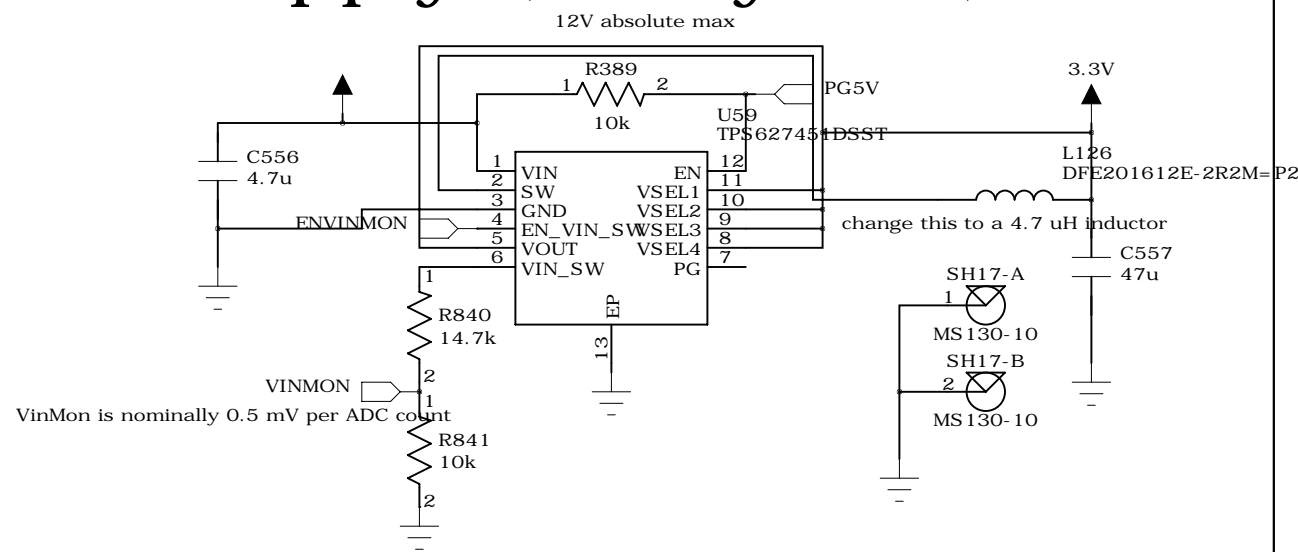
LAB4D supply (2.6V, regulated to 2.5V) supply



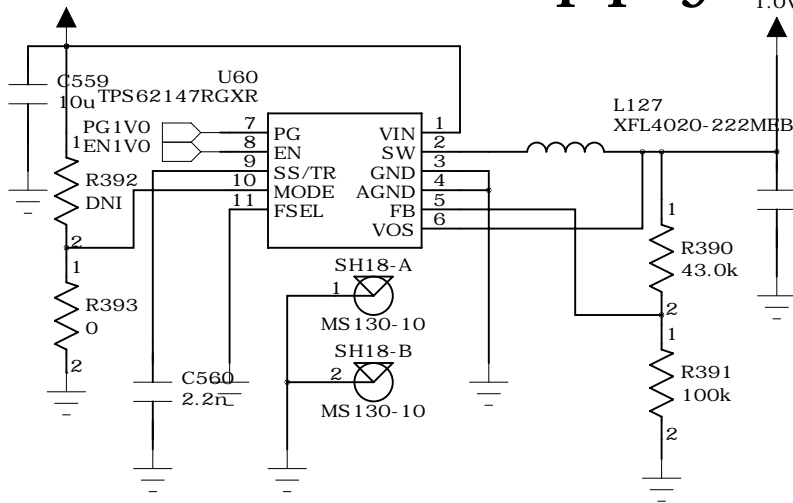
Trigger supply (3.07V, regulated to 1.8V) supply



3.3V supply (always on)



FPGA core supply (1.0V)



COMPANY: Ohio State University			
SHEET: POWER			
CODE: 2	SIZE: C	DESIGN: RADIANT	REV: 1
SCALE: < Scale >			SHEET: 8 OF 73

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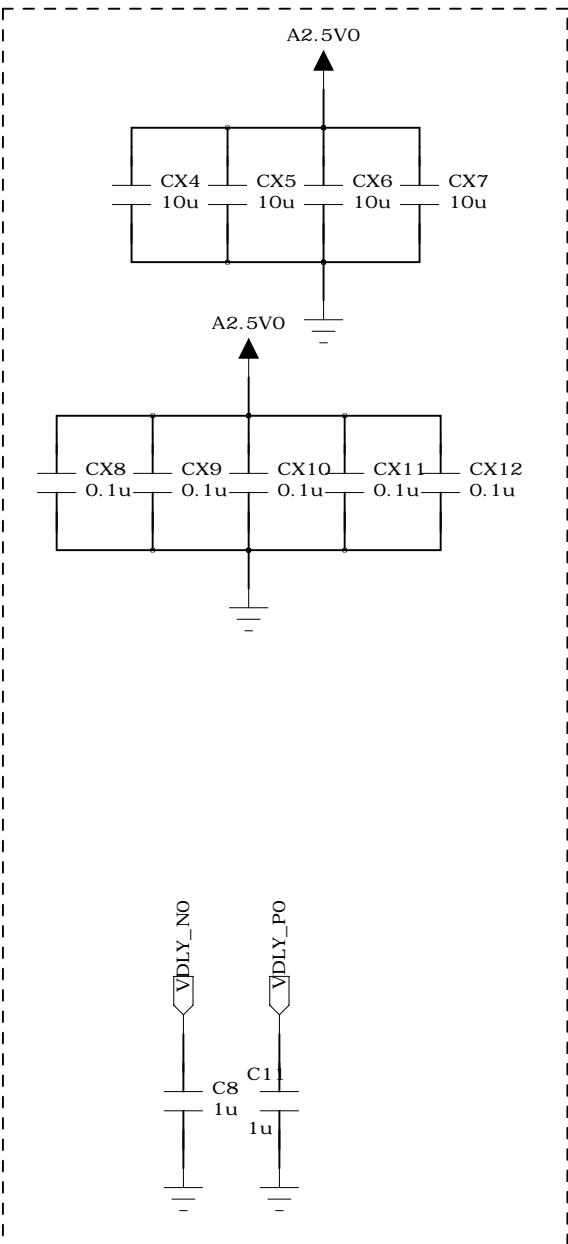
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
Filtering/bypass capacitors			



Filtering/bypass capacitors

Do not install

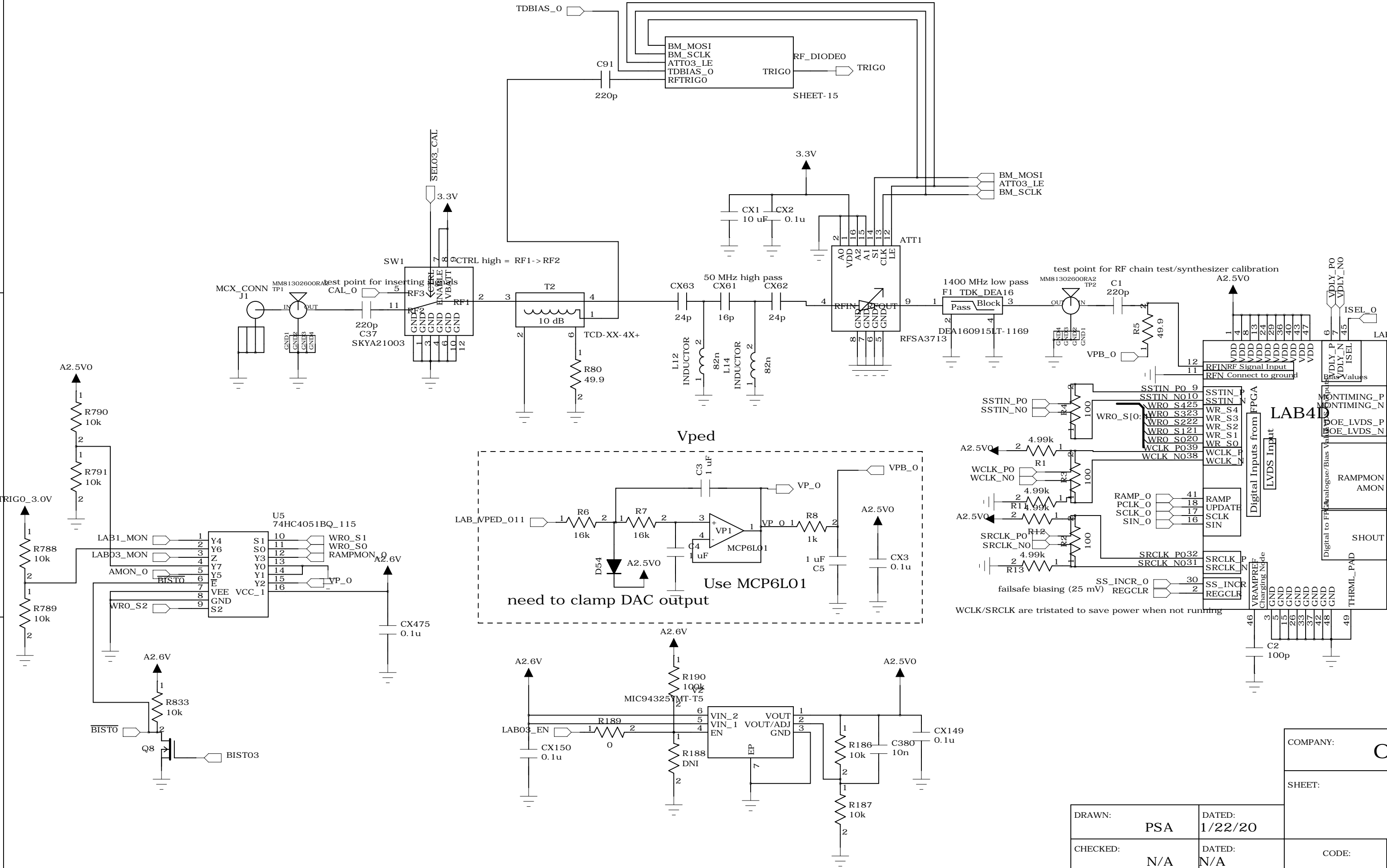
Do not install

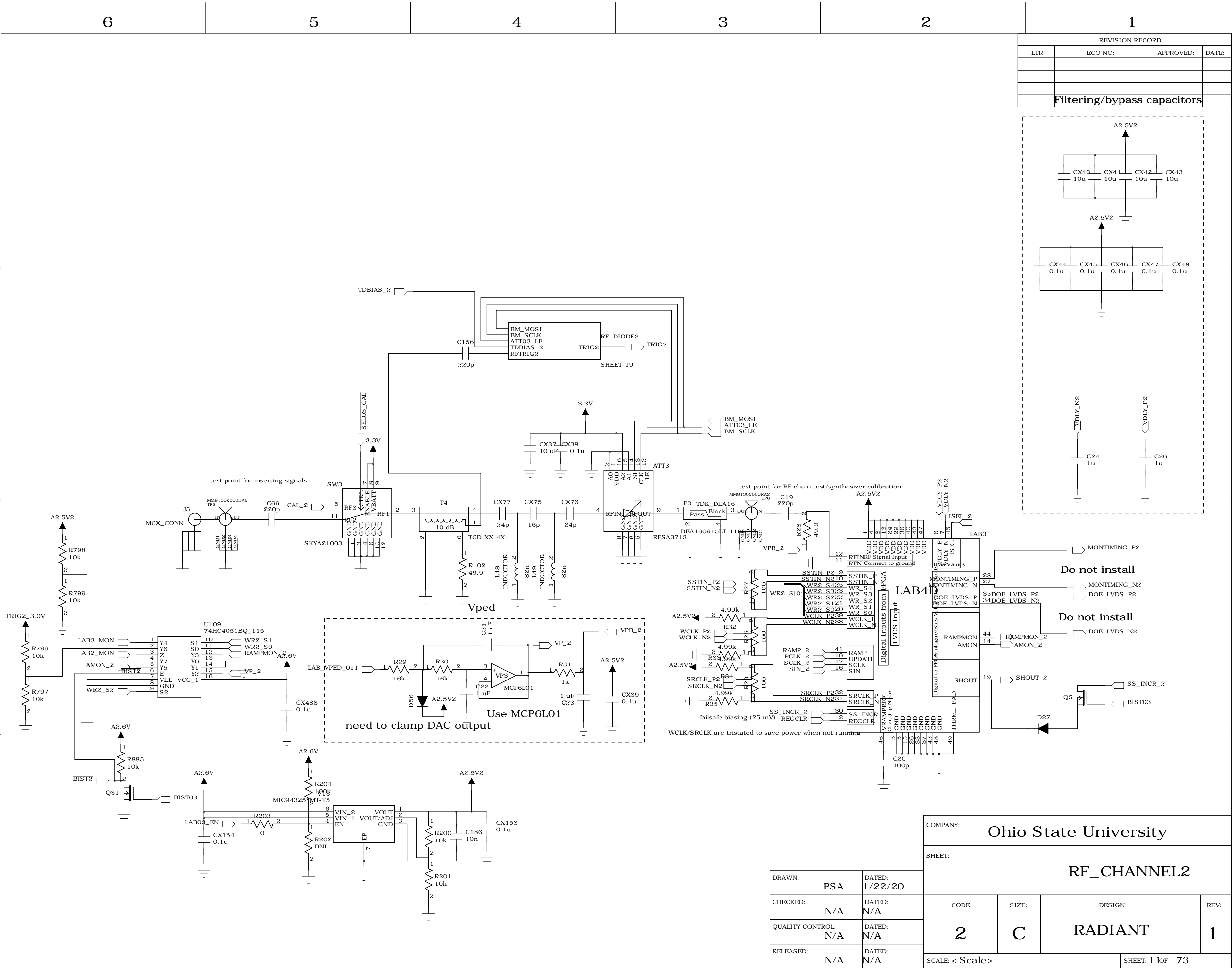
Do not install

Do not install

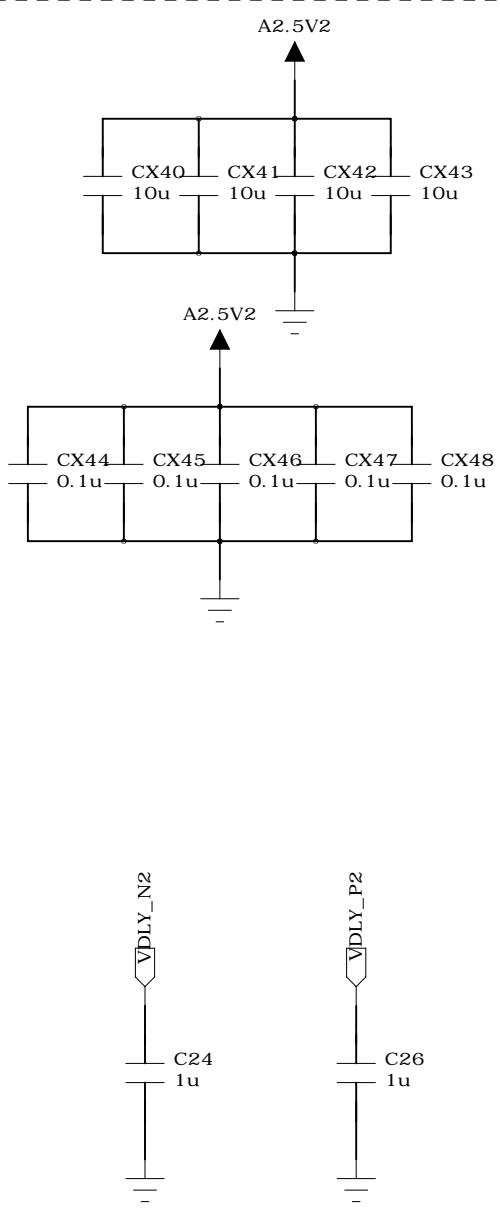
COMPANY: Ohio State University			
SHEET: RF_CHANNELO			
CODE: 2	SIZE: C	DESIGN: RADIANT	REV: 1
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DRAWN: PSA	DATED: 1/22/20
CHECKED: N/A	DATED: N/A
QUALITY CONTROL: N/A	DATED: N/A
RELEASED: N/A	DATED: N/A



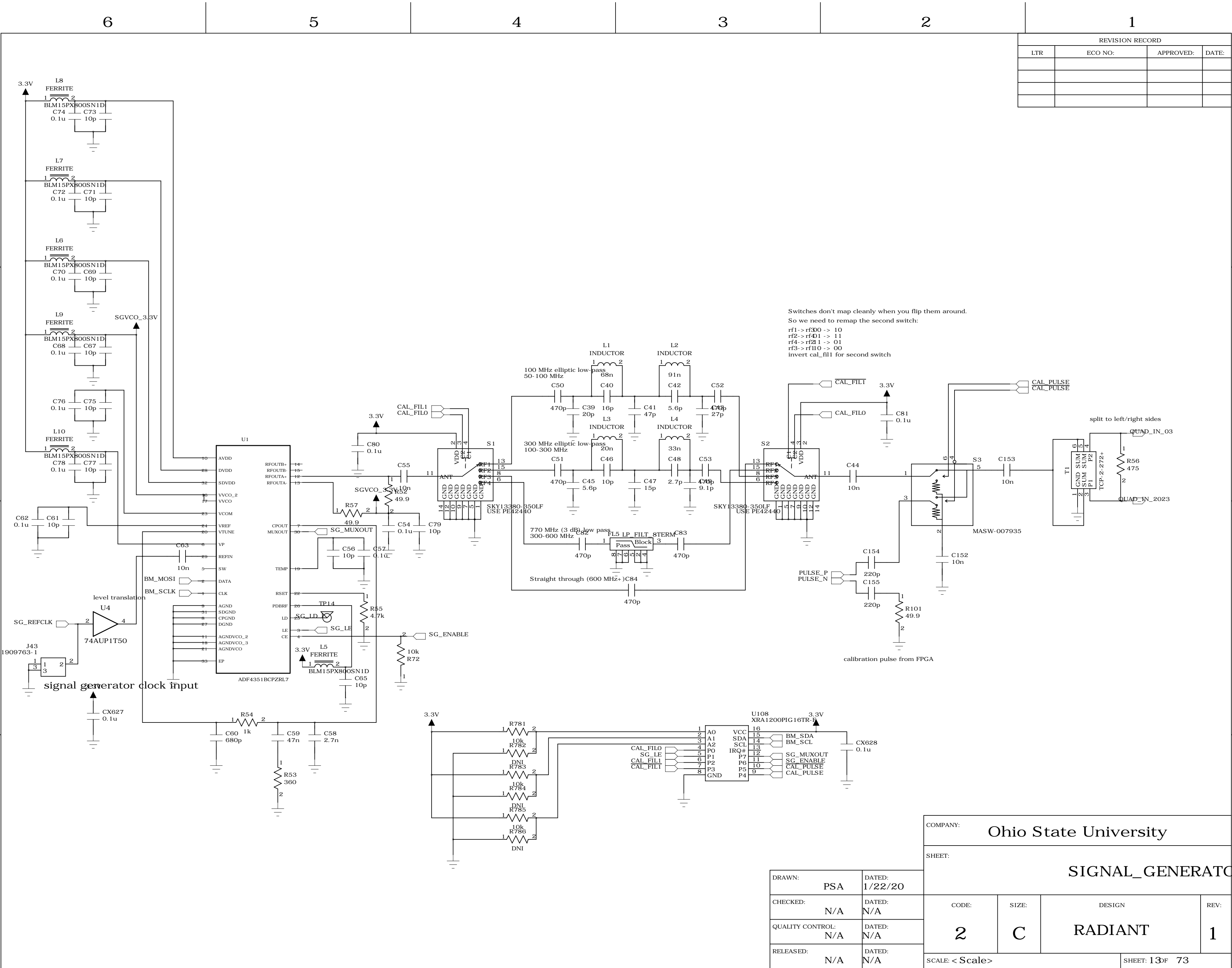


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
Filtering/bypass capacitors			



COMPANY: Ohio State University			
SHEET: RF_CHANNEL2			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale >			SHEET: 1 of 73

DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
QUALITY CONTROL:	N/A	DATED:	N/A
RELEASED:	N/A	DATED:	N/A



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

Switches don't map cleanly when you flip them around.
So we need to remap the second switch:
rf1->rf300 -> 10
rf2->rf401 -> 11
rf4->rf211 -> 01
rf3->rf110 -> 00
invert cal_fil1 for second switch

COMPANY: Ohio State University			
SHEET: SIGNAL_GENERATOR			
DRAWN: PSA	DATED: 1/22/20	CODE: 2	SIZE: C
CHECKED: N/A	DATED: N/A	DESIGN: RADIANT	REV: 1
QUALITY CONTROL: N/A	DATED: N/A		
RELEASED: N/A	DATED: N/A		
SCALE: < Scale>			SHEET: 13 of 73

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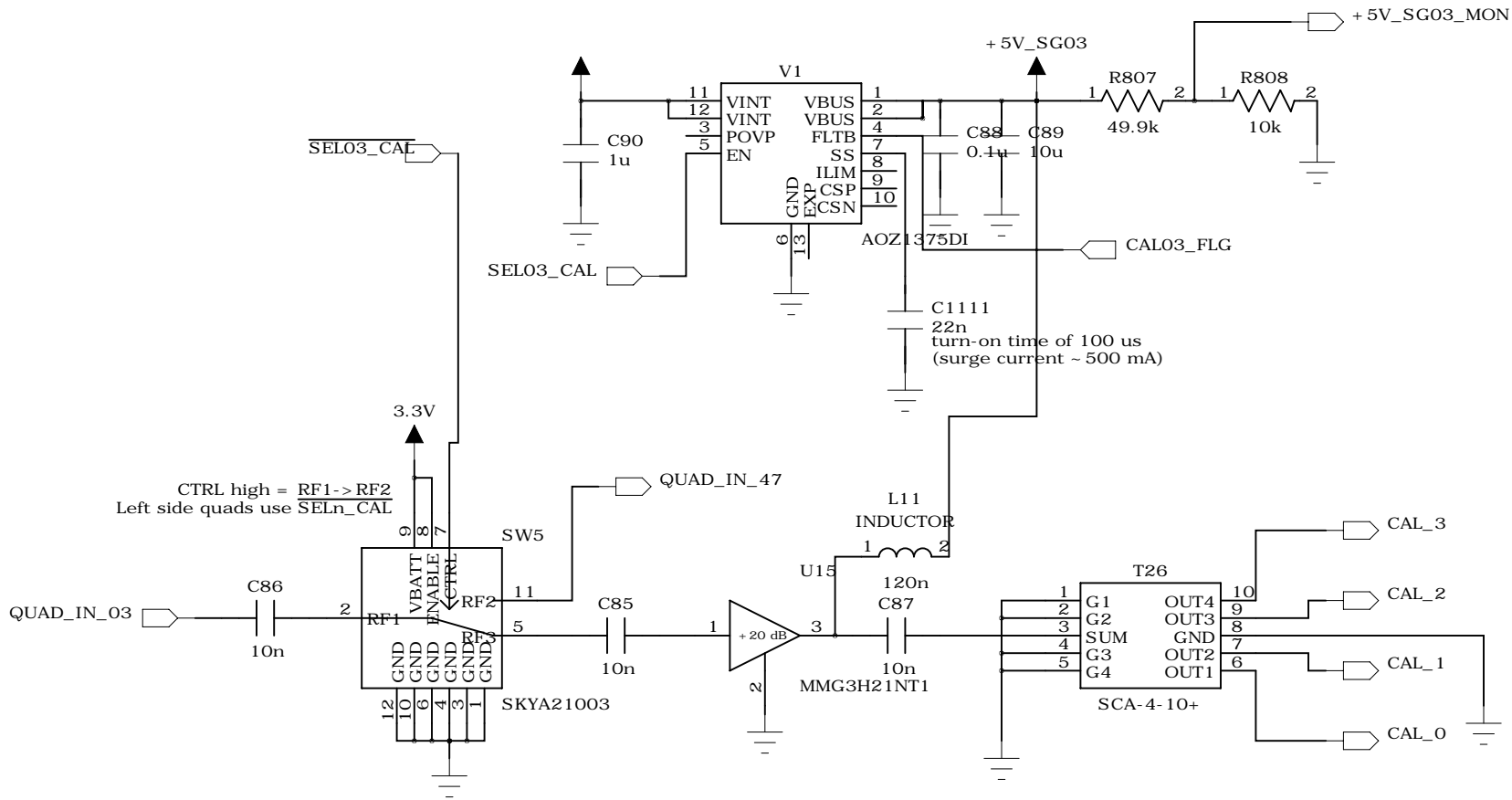
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: Ohio State University			
SHEET: QUAD_SIG_GENERATOR_C			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 14 of 73

DRAWN: PSA	DATED: 1/22/20
CHECKED: N/A	DATED: N/A
QUALITY CONTROL: N/A	DATED: N/A
RELEASED: N/A	DATED: N/A

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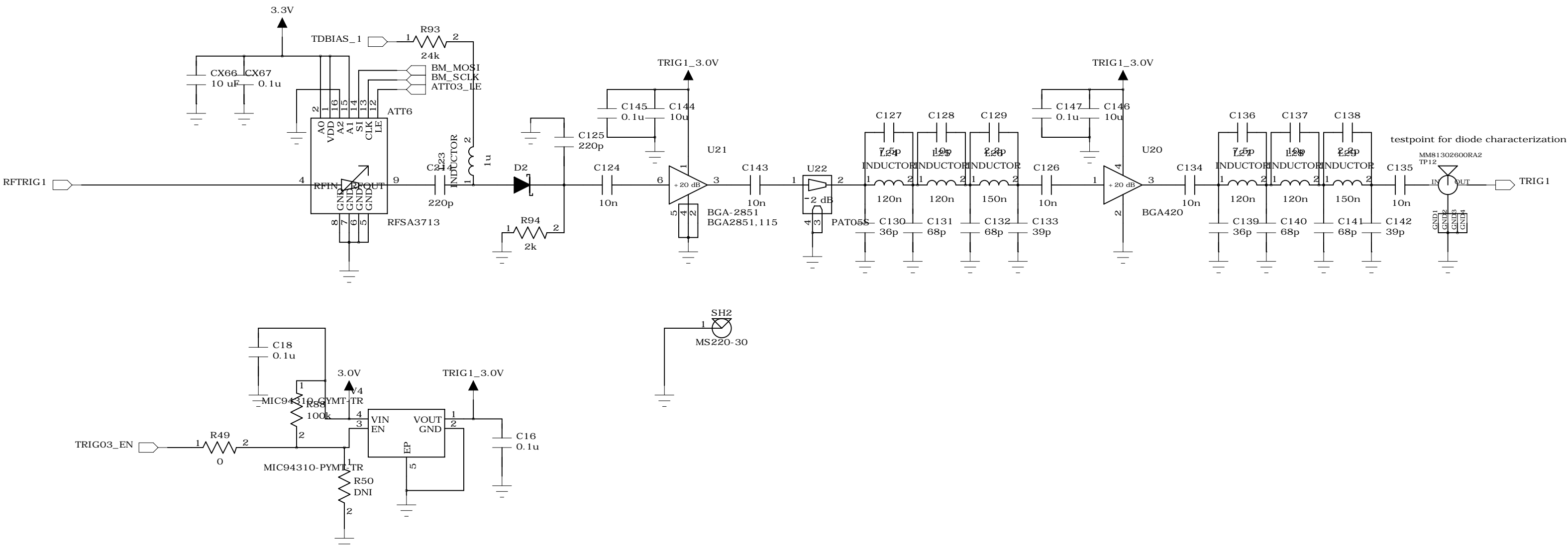
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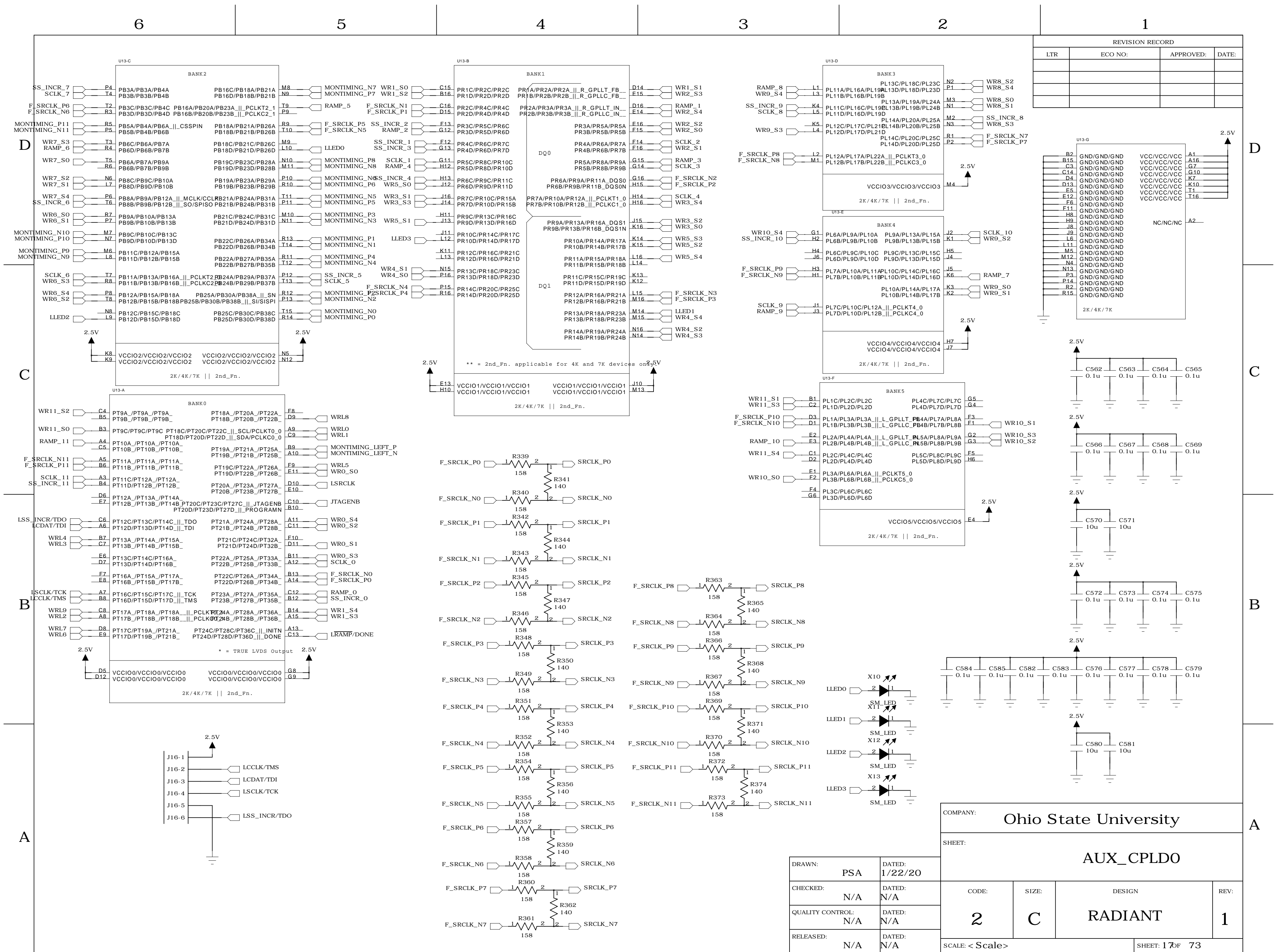
1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: Ohio State University			
SHEET: RF_DIODE1			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 16 of 73

DRAWN: PSA	DATED: 1/22/20
CHECKED: N/A	DATED: N/A
QUALITY CONTROL: N/A	DATED: N/A
RELEASED: N/A	DATED: N/A



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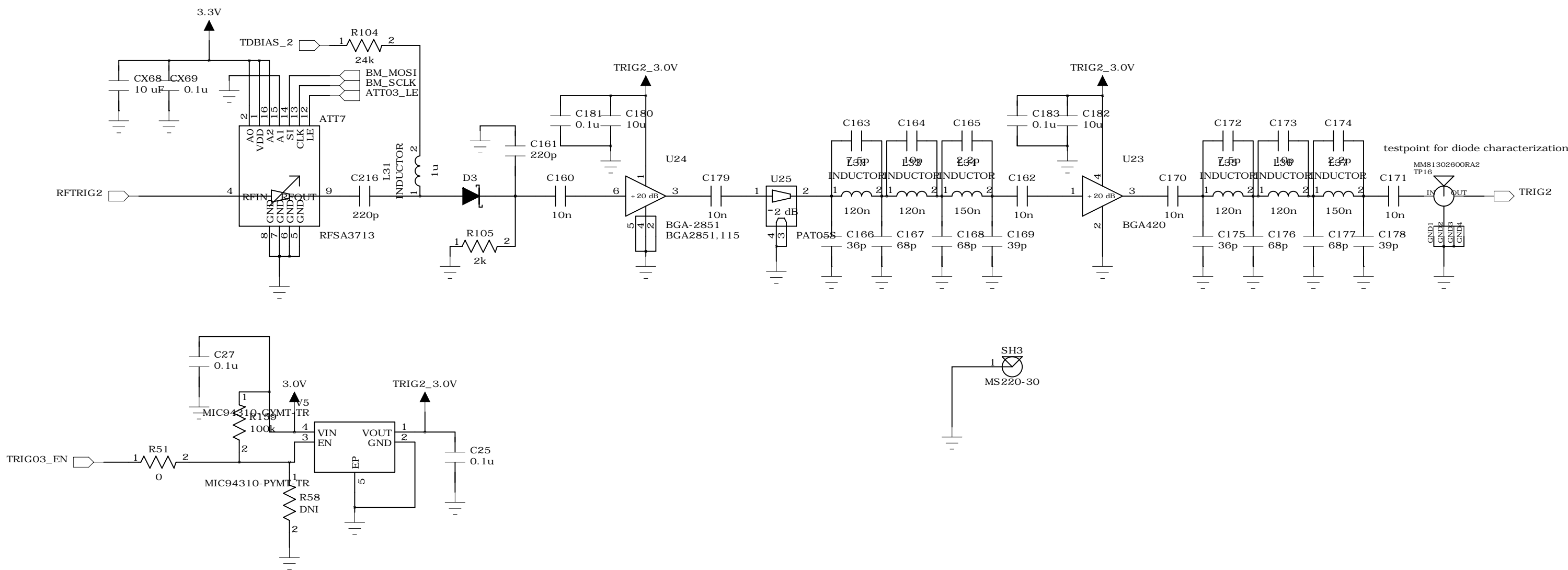
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REVISION RECORD

LTR	ECO NO:	APPROVED:	DATE:



COMPANY: Ohio State University			
SHEET: RF_DIODE2			
CODE:	SIZE:	DESIGN	REV:
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DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
QUALITY CONTROL:	N/A	DATED:	N/A
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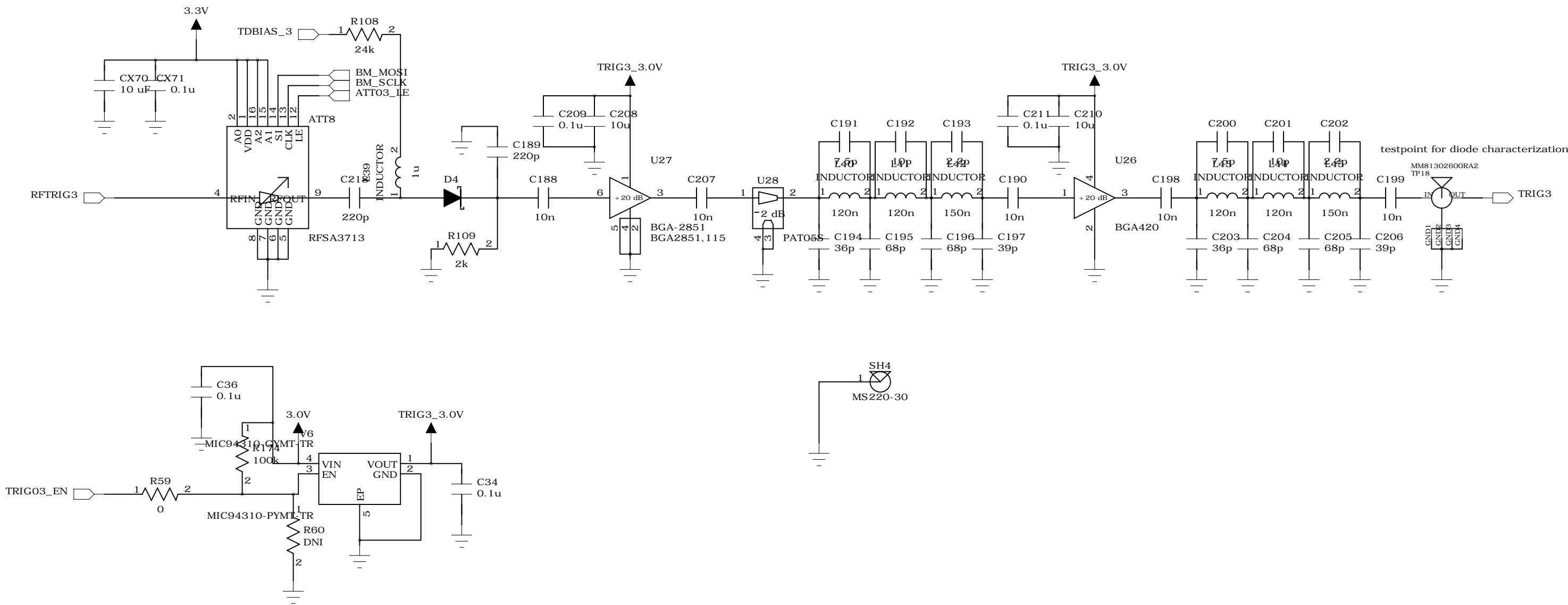
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



		COMPANY: Ohio State University			
		SHEET: RF_DIODE3			
DRAWN: PSA	DATED: 1/22/20	CODE: 2	SIZE: C	DESIGN RADIANT	REV: 1
CHECKED: N/A	DATED: N/A				
QUALITY CONTROL: N/A	DATED: N/A				
RELEASED: N/A	DATED: N/A				
		SCALE: < Scale>			SHEET: 20 of 73

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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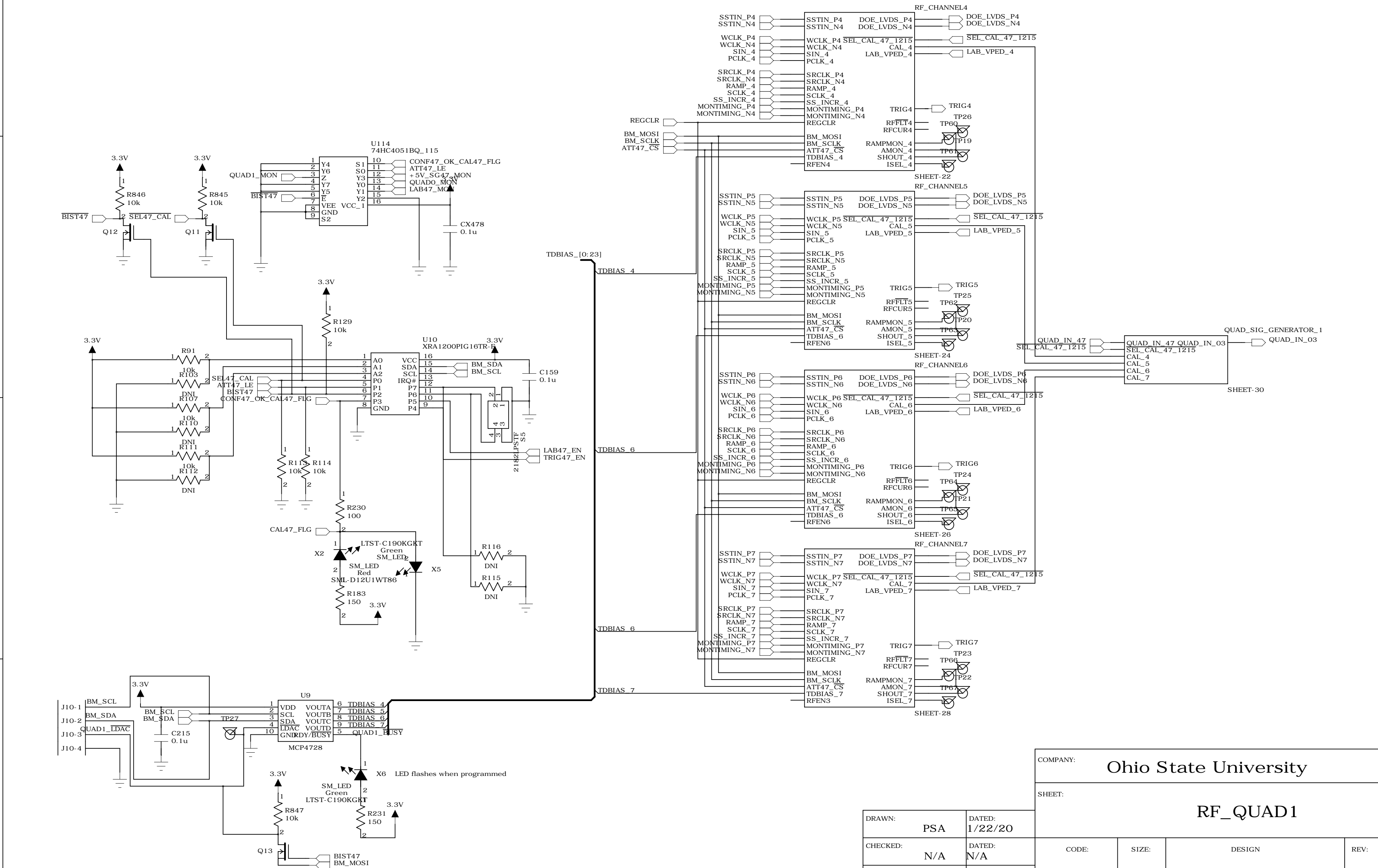
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DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
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RELEASED:	N/A	DATED:	N/A

COMPANY: Ohio State University			
SHEET: RF_Quad1			
CODE:	SIZE:	DESIGN:	REV:
2	C	RADIANT	1
SCALE: < Scale >			SHEET: 2 of 73

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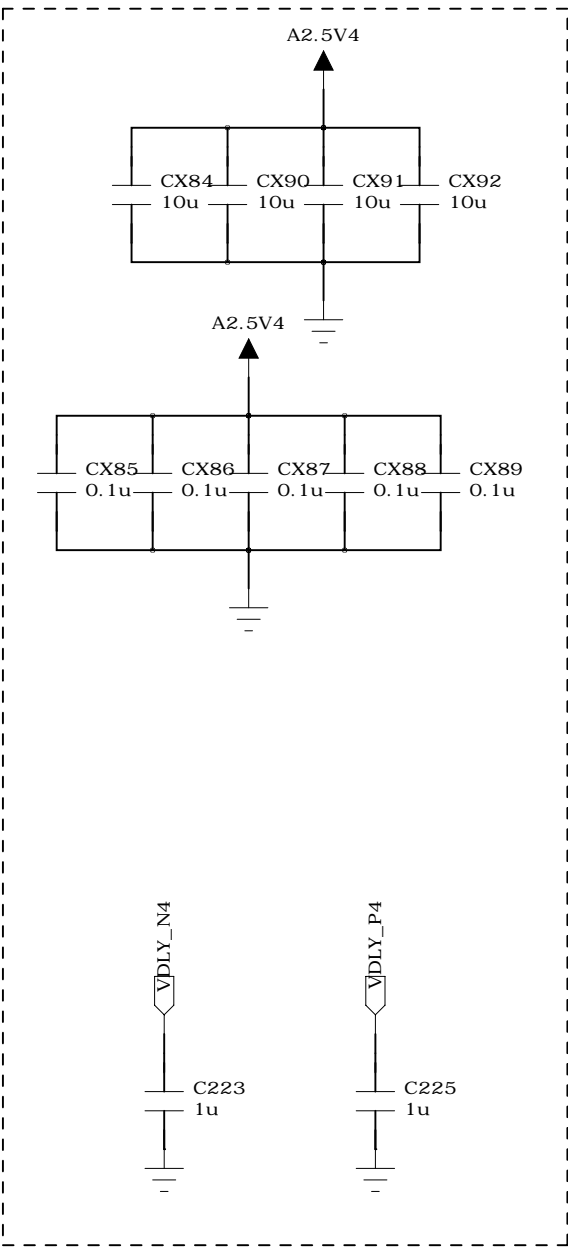
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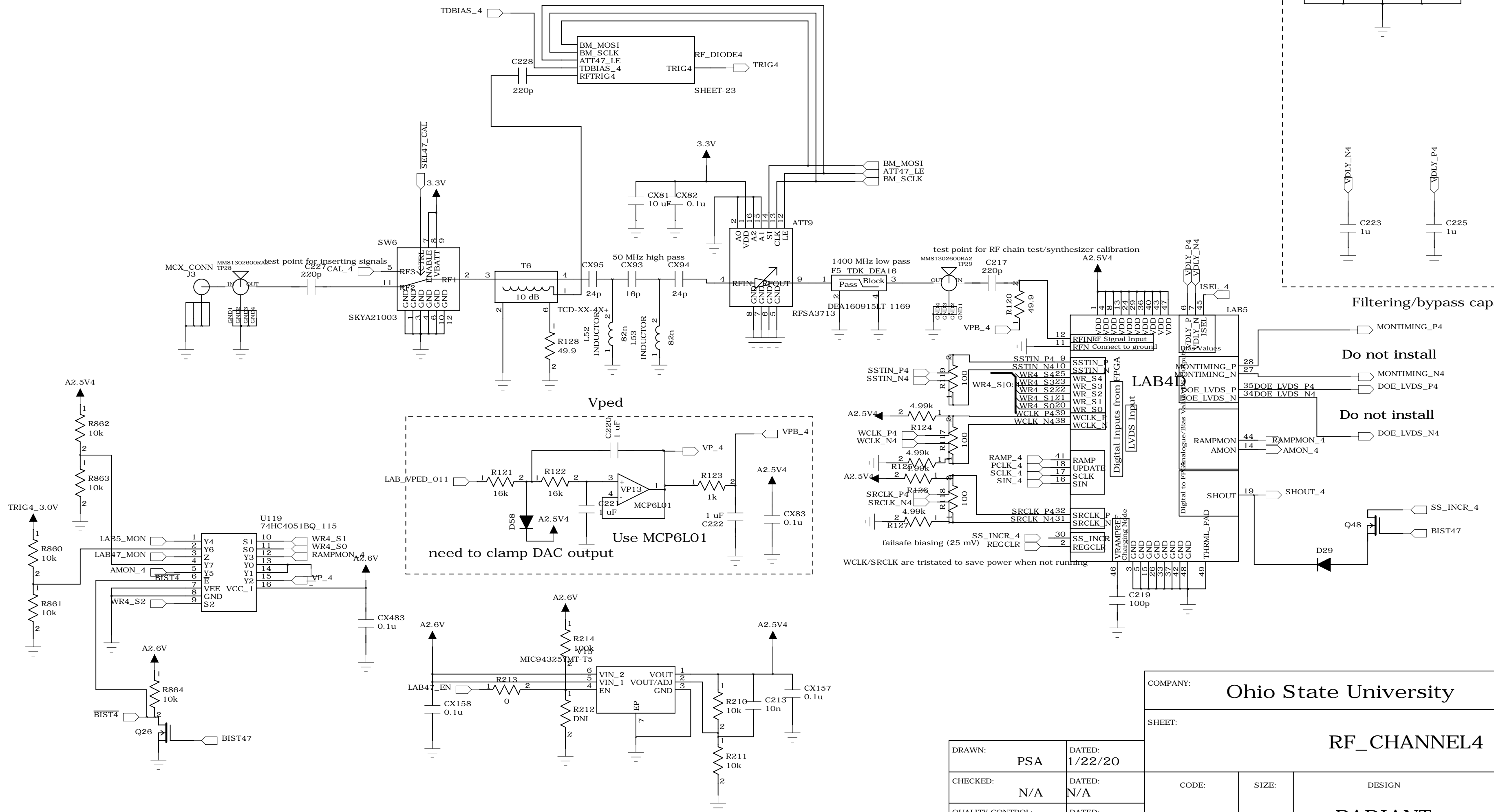
B

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
Filtering/bypass capacitors			



Filtering/bypass capacitors



COMPANY: Ohio State University			
SHEET: RF_CHANNEL4			
DRAWN: PSA	DATED: 1/22/20	CODE: 2	SIZE: C
CHECKED: N/A	DATED: N/A	DESIGN: RADIANT	REV: 1
QUALITY CONTROL: N/A	DATED: N/A		
RELEASED: N/A	DATED: N/A		
SCALE: < Scale >			SHEET: 22 OF 73

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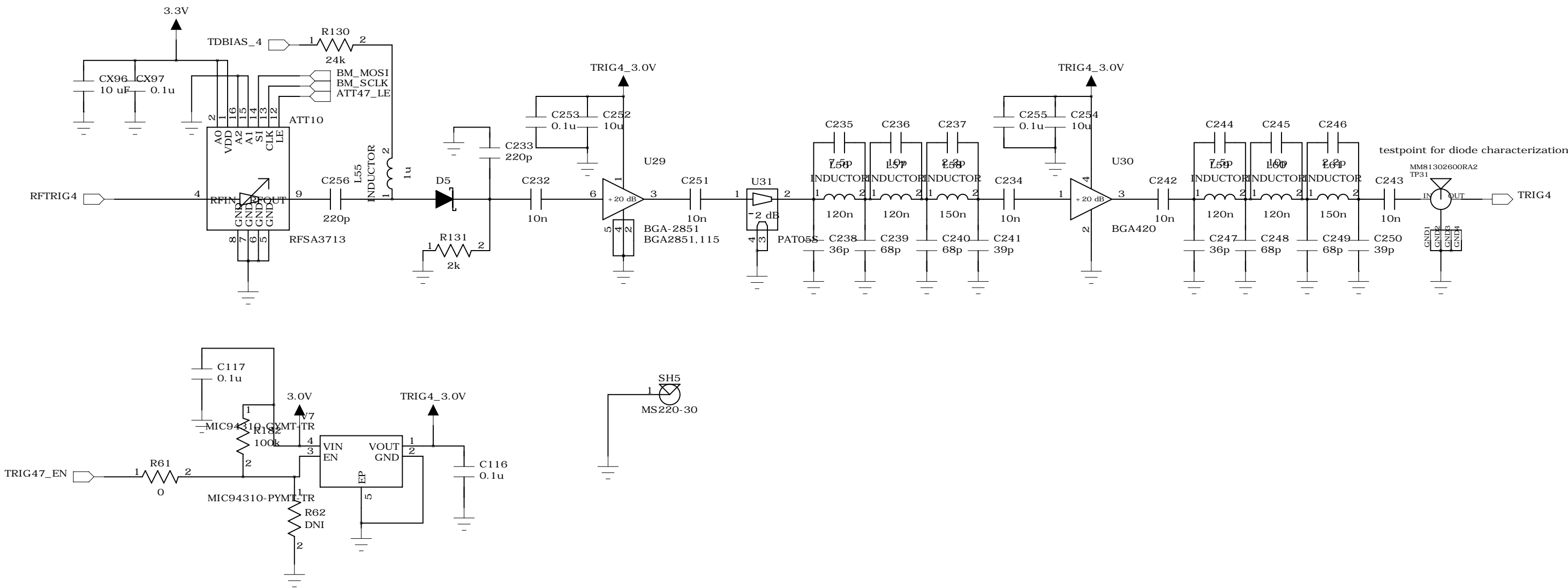
D

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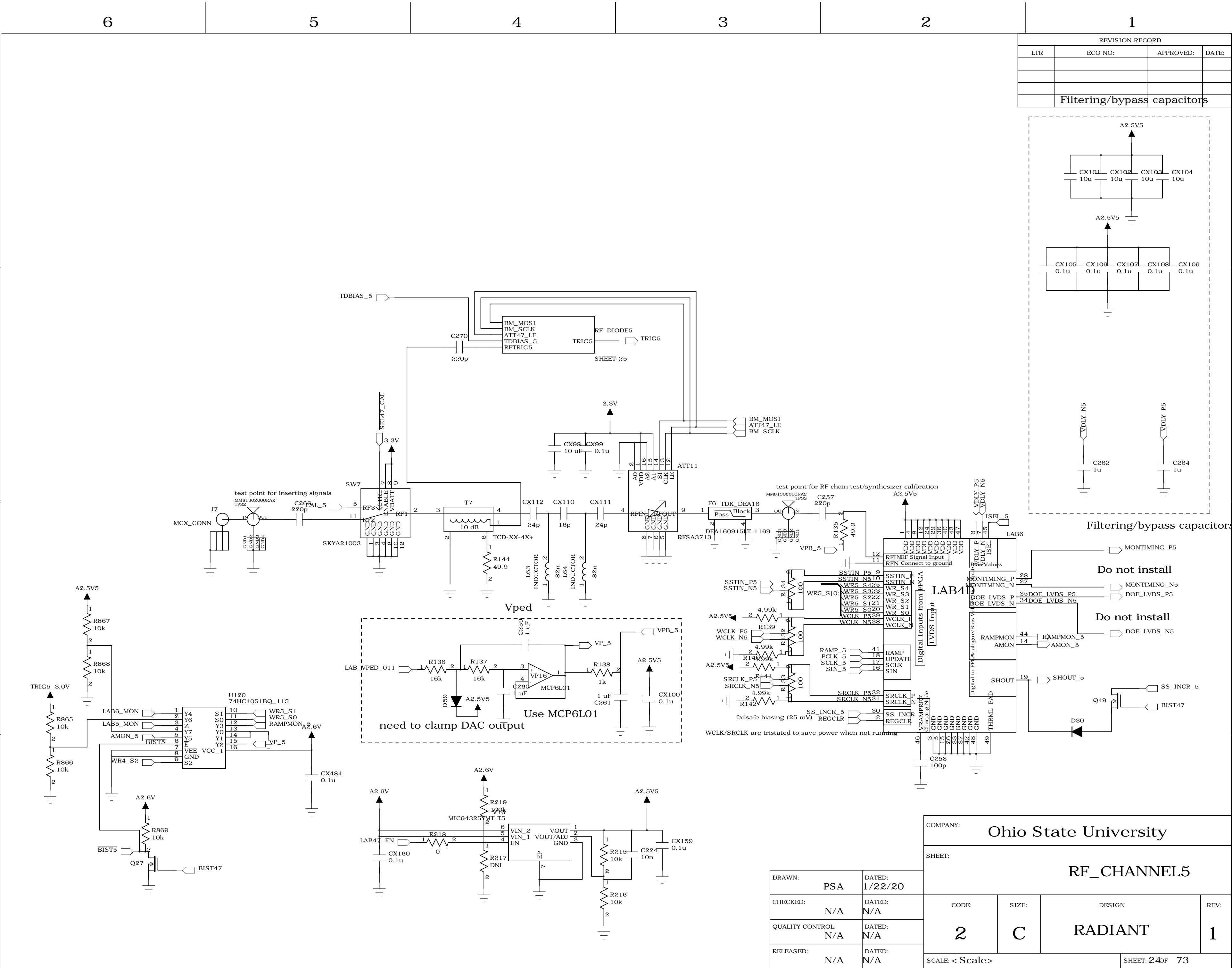
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

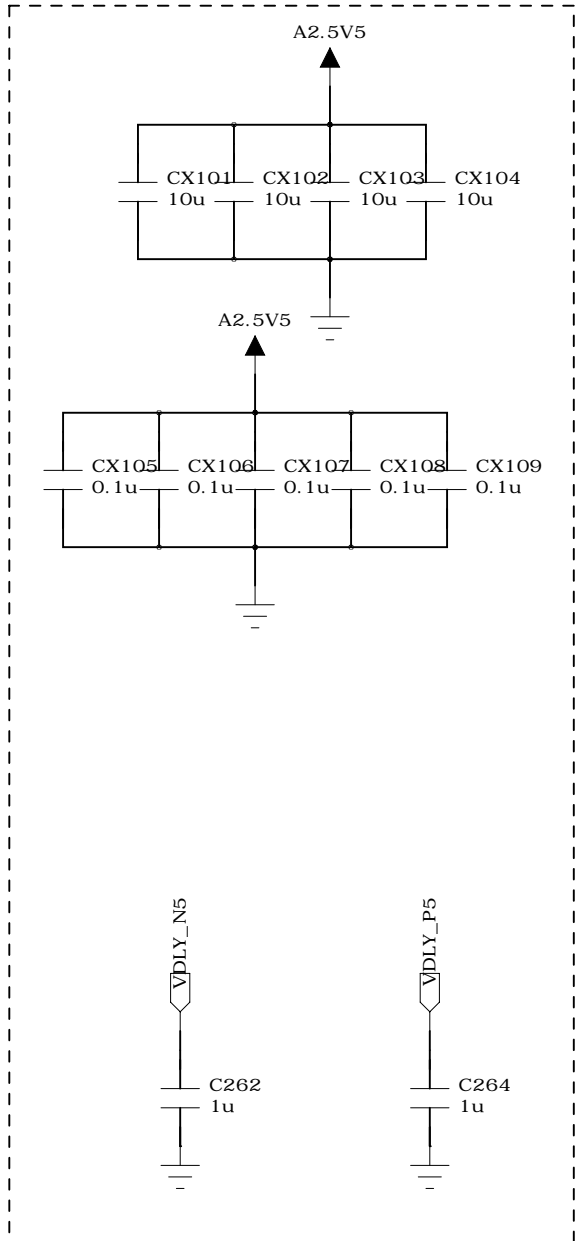


DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
QUALITY CONTROL:	N/A	DATED:	N/A
RELEASED:	N/A	DATED:	N/A

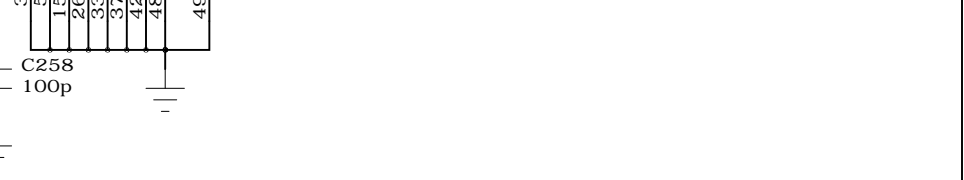
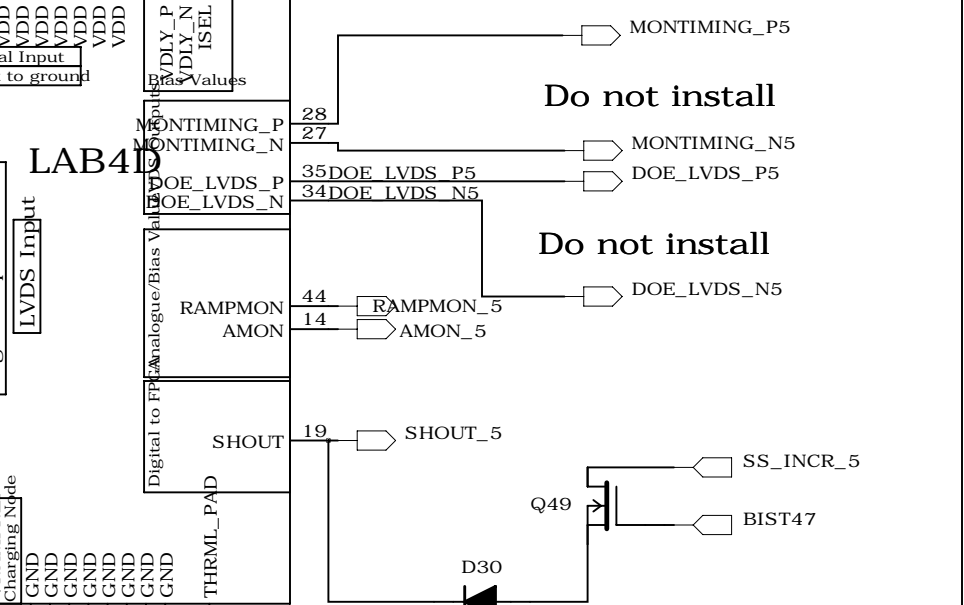
COMPANY: Ohio State University			
SHEET: RF_DIODE4			
CODE:	SIZE:	DESIGN:	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 23 OF 73



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
Filtering/bypass capacitors			

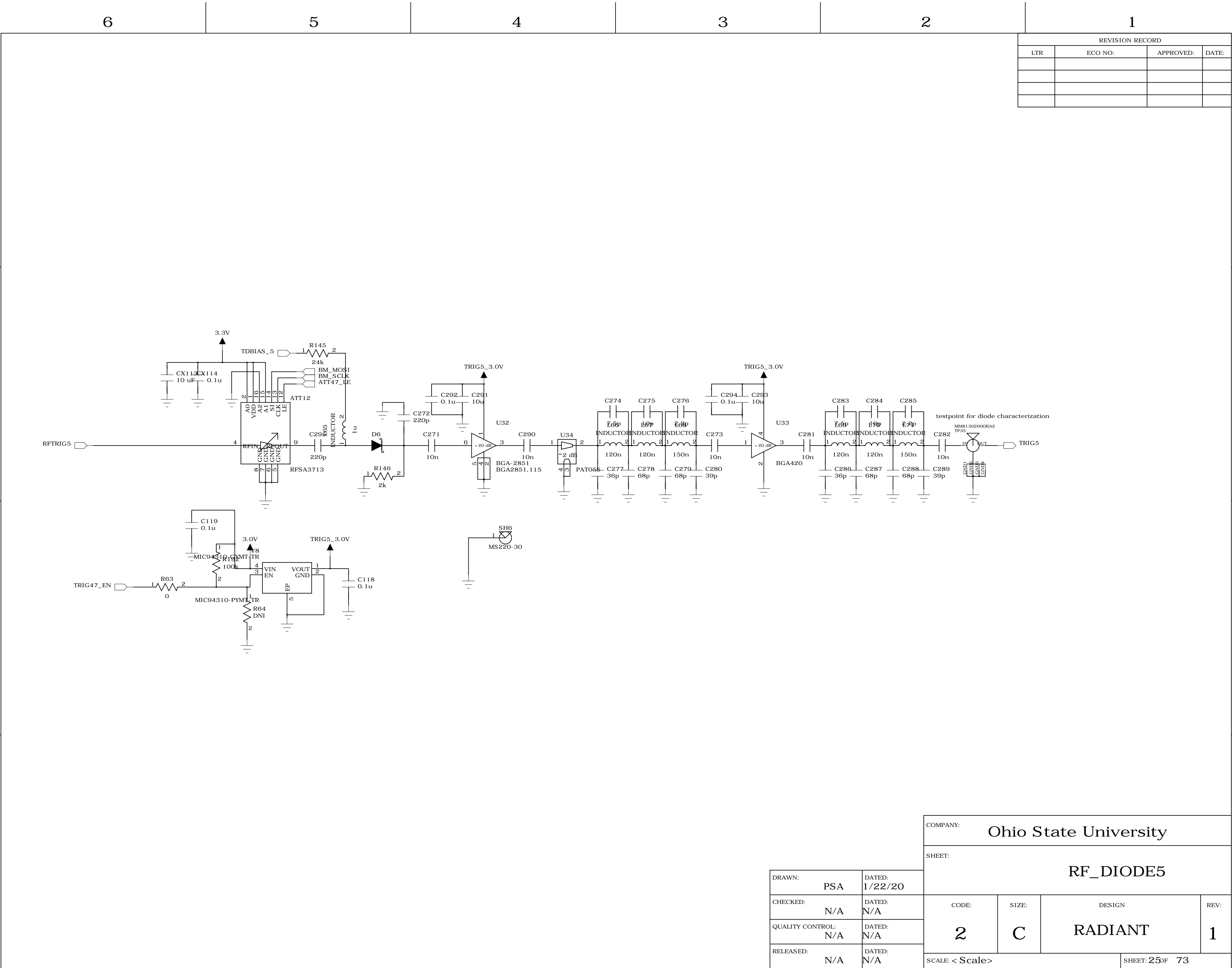


Filtering/bypass capacitors



COMPANY: Ohio State University			
SHEET: RF_CHANNEL5			
CODE: 2	SIZE: C	DESIGN: RADIANT	REV: 1
SCALE: < Scale >			SHEET: 24 OF 73

DRAWN: PSA	DATED: 1/22/20
CHECKED: N/A	DATED: N/A
QUALITY CONTROL: N/A	DATED: N/A
RELEASED: N/A	DATED: N/A



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Ohio State University			
SHEET: RF_DIODE5			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 25 of 73

DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
QUALITY CONTROL:	N/A	DATED:	N/A
RELEASED:	N/A	DATED:	N/A

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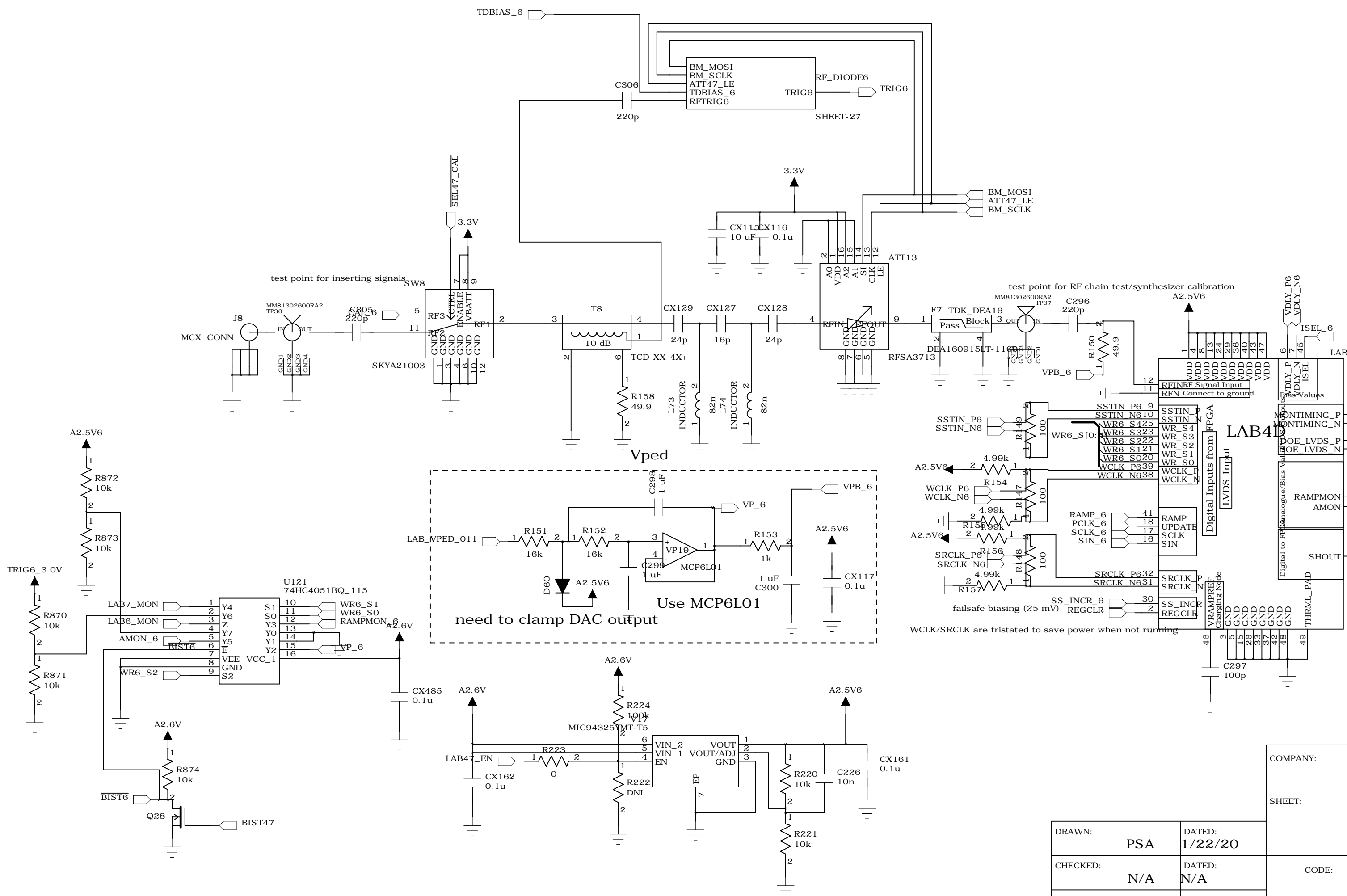
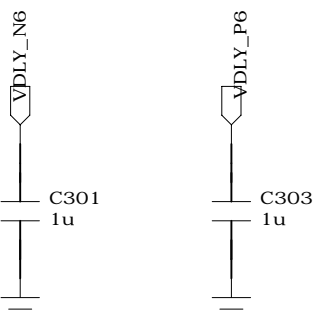
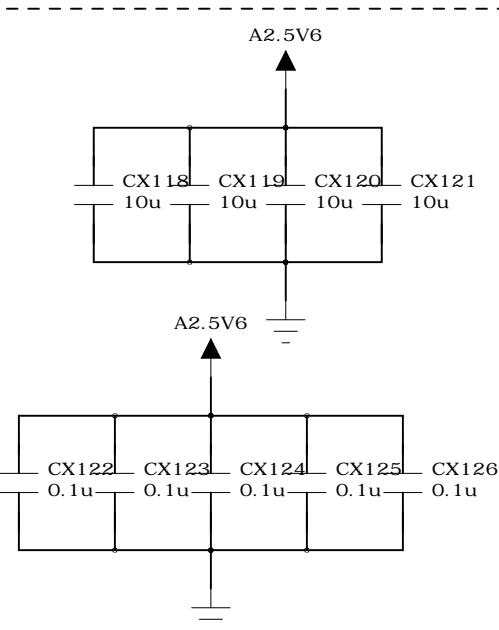
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
Filtering/bypass capacitors			



COMPANY: Ohio State University			
SHEET: RF_CHANNEL6			
CODE: 2	SIZE: C	DESIGN: RADIANT	REV: 1
SCALE: < Scale >			SHEET: 26 OF 73

DRAWN: PSA	DATED: 1/22/20
CHECKED: N/A	DATED: N/A
QUALITY CONTROL: N/A	DATED: N/A
RELEASED: N/A	DATED: N/A

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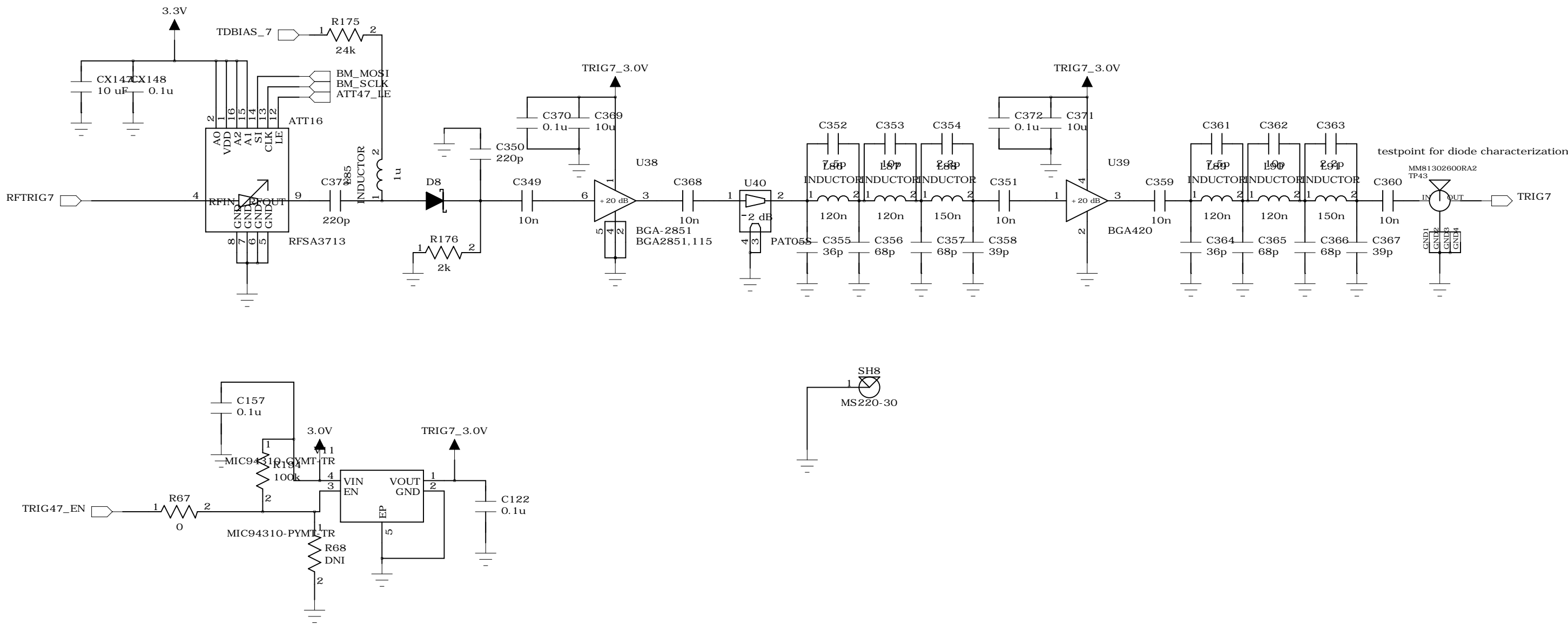
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REVISION RECORD

LTR	ECO NO:	APPROVED:	DATE:



COMPANY: Ohio State University			
SHEET: RF_DIODE7			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 29 of 73

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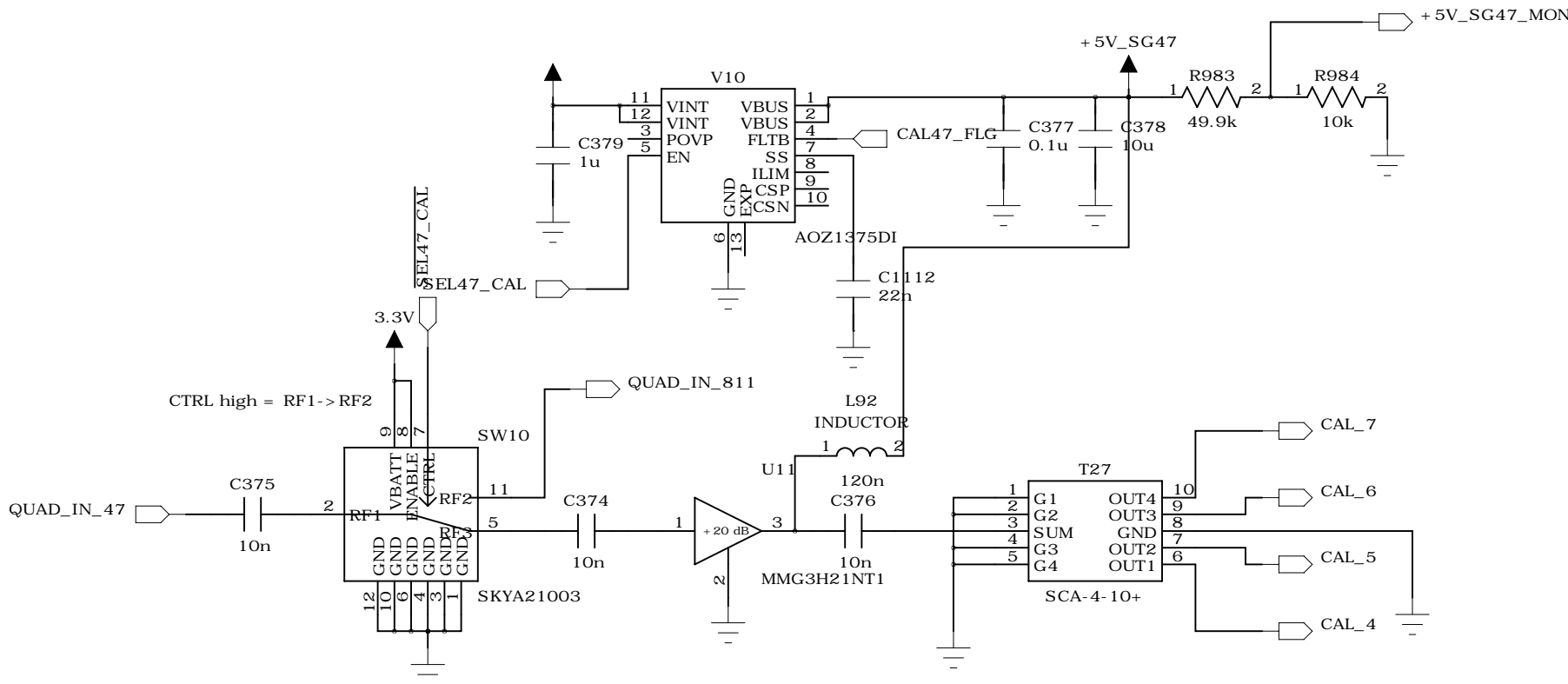
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REVISION RECORD

LTR	ECO NO:	APPROVED:	DATE:



COMPANY: Ohio State University			
SHEET: QUAD_SIG_GENERATOR_1			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 30 OF 73

DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
QUALITY CONTROL:	N/A	DATED:	N/A
RELEASED:	N/A	DATED:	N/A

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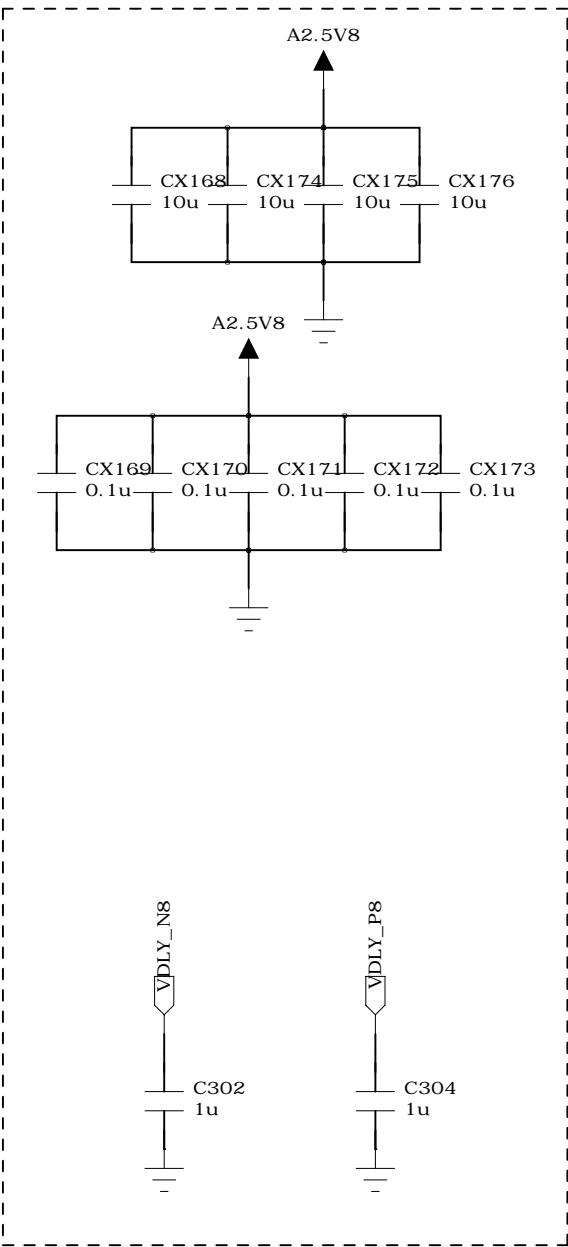
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
Filtering/bypass capacitors			



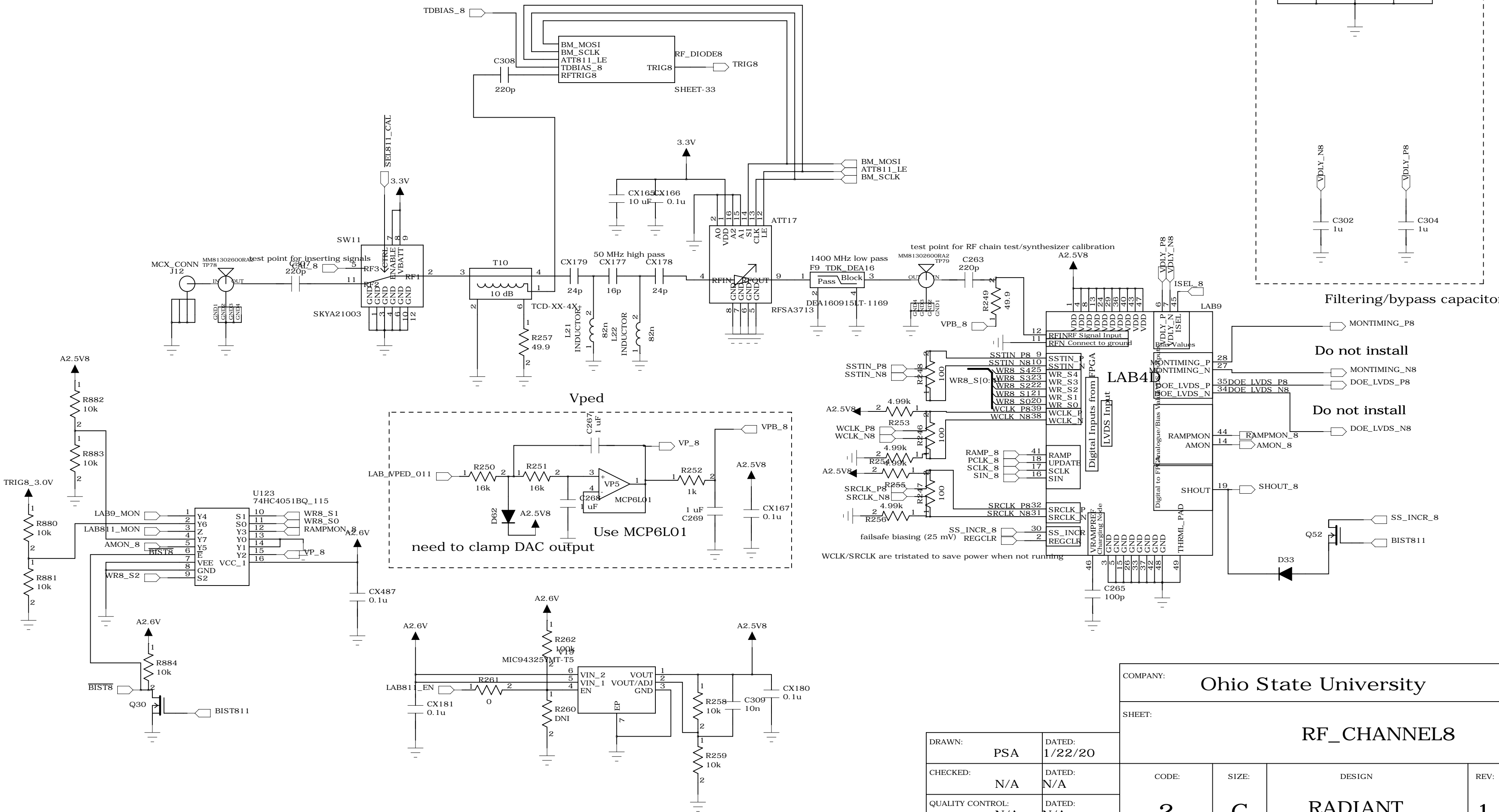
Filtering/bypass capacitors

Do not install

Do not install

COMPANY: Ohio State University			
SHEET: RF_CHANNEL8			
CODE: 2	SIZE: C	DESIGN: RADIANT	REV: 1
SCALE: < Scale >			SHEET: 32 OF 73

DRAWN: PSA	DATED: 1/22/20
CHECKED: N/A	DATED: N/A
QUALITY CONTROL: N/A	DATED: N/A
RELEASED: N/A	DATED: N/A



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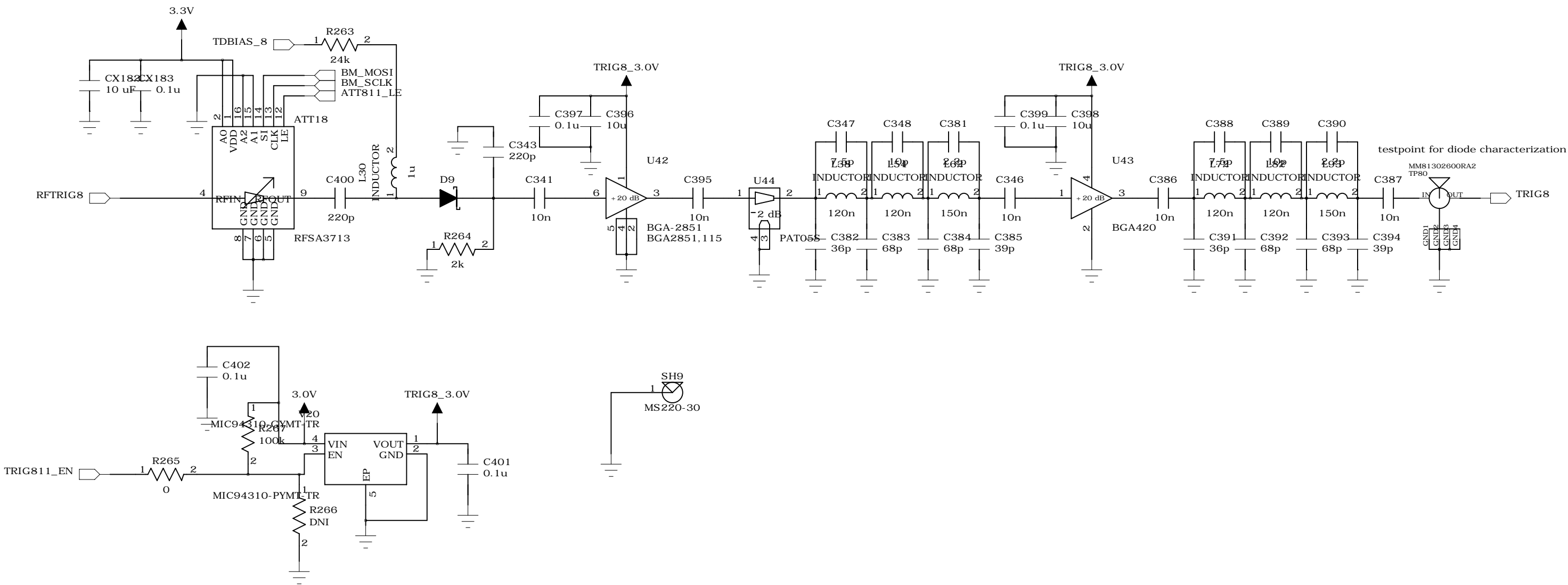
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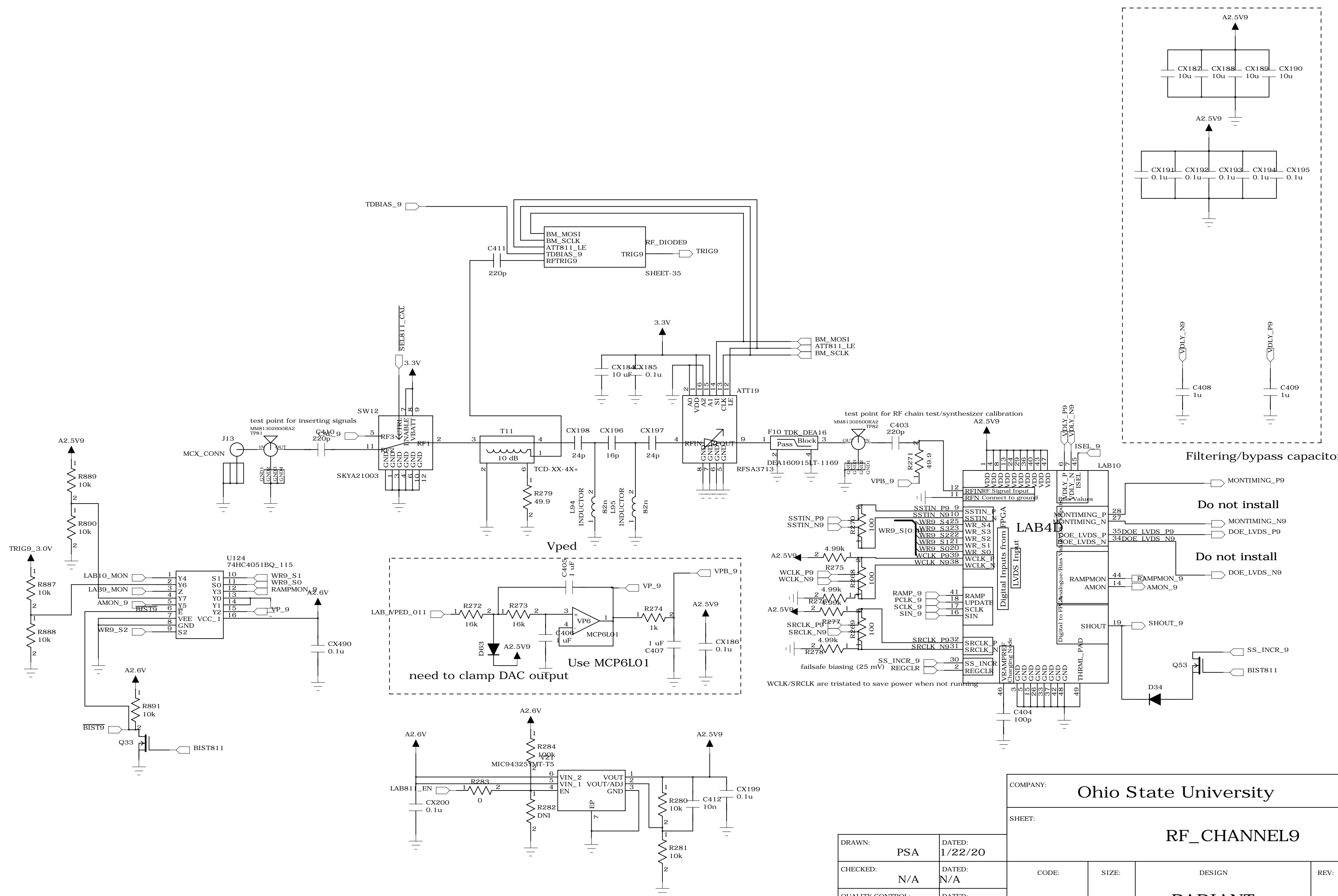
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



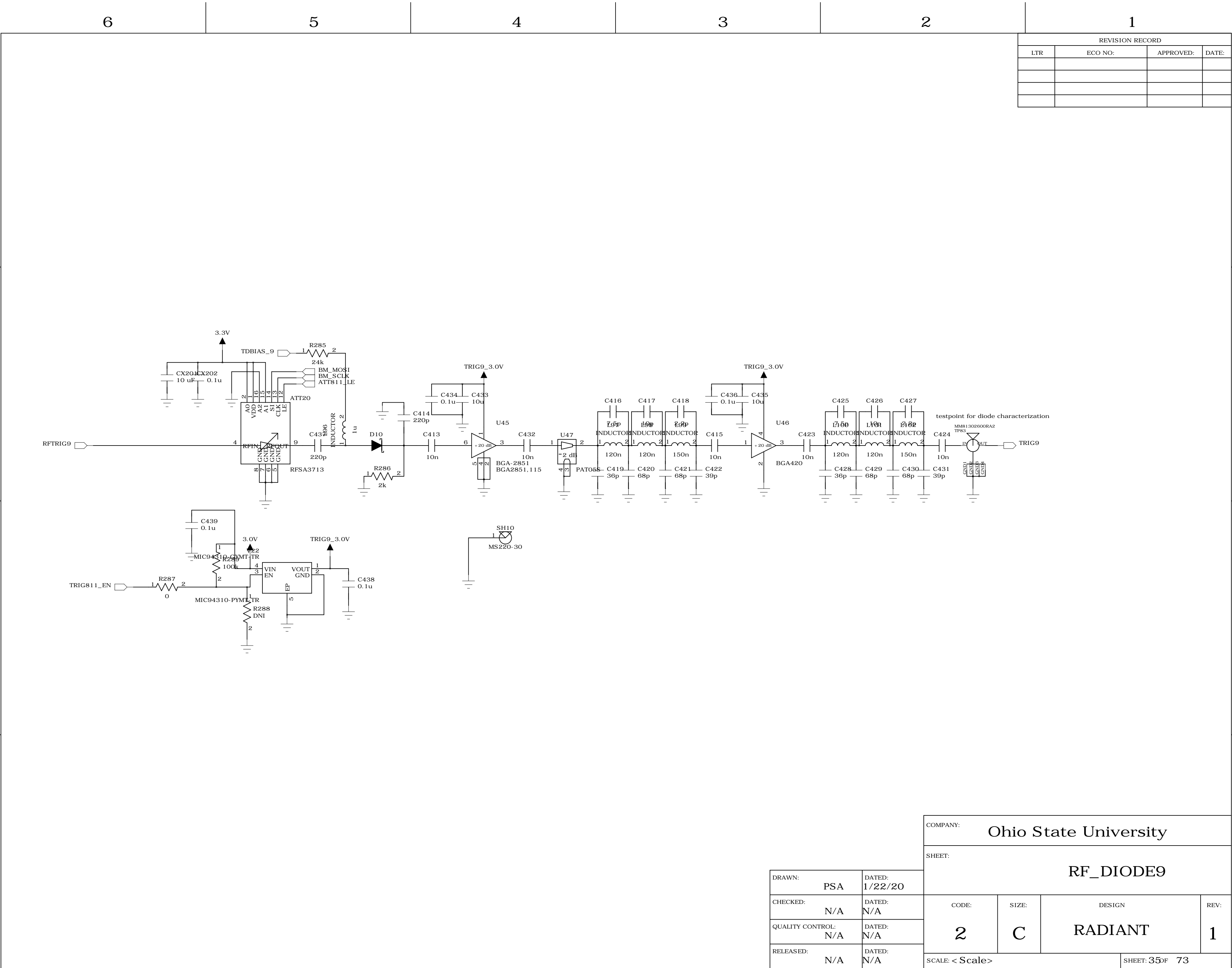
DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
QUALITY CONTROL:	N/A	DATED:	N/A
RELEASED:	N/A	DATED:	N/A

COMPANY: Ohio State University			
SHEET: RF_DIODE8			
CODE:	SIZE:	DESIGN:	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 33 OF 73

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
	Filtering/bypass capacitors		



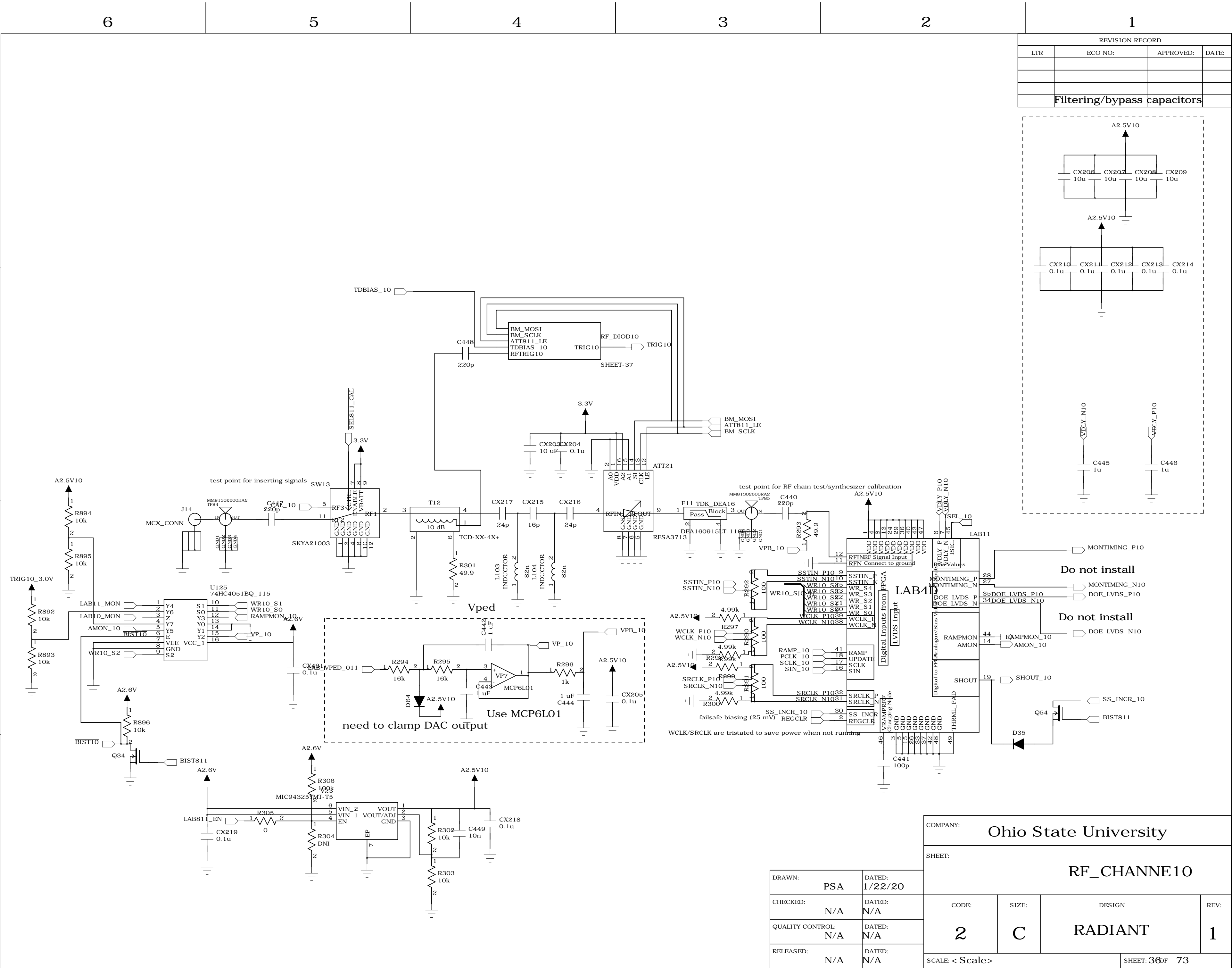
COMPANY:		Ohio State University	
SHEET:		RF_CHANNEL9	
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>		SHEET: 34 OF 73	



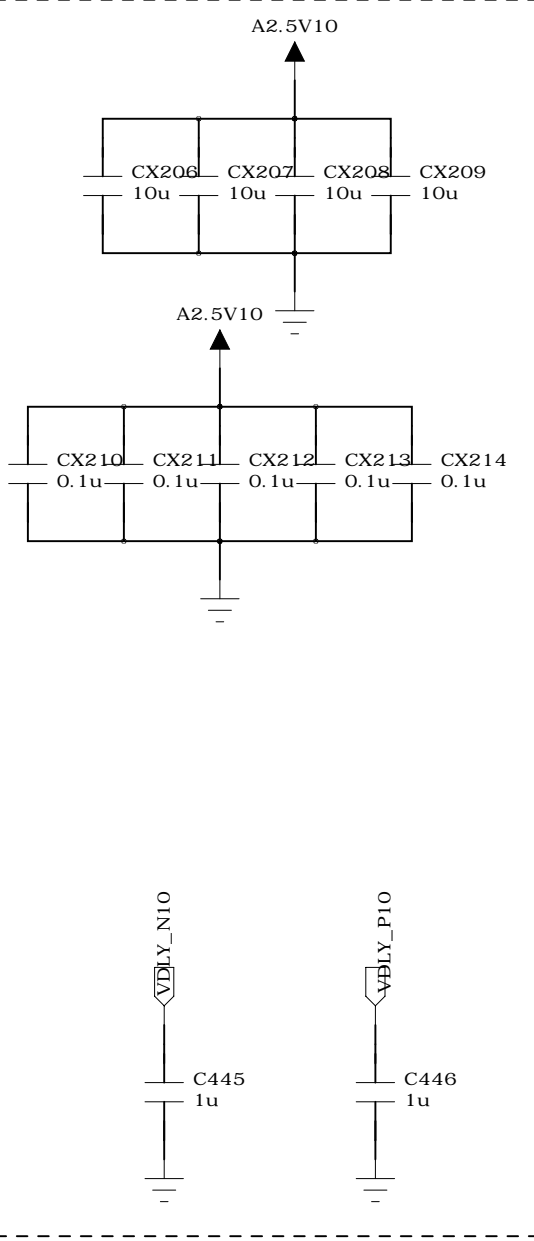
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Ohio State University			
SHEET: RF_DIODE9			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 35 of 73

DRAWN: PSA	DATED: 1/22/20
CHECKED: N/A	DATED: N/A
QUALITY CONTROL: N/A	DATED: N/A
RELEASED: N/A	DATED: N/A



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
Filtering/bypass capacitors			



COMPANY: Ohio State University			
SHEET: RF_CHANNE10			
CODE: 2	SIZE: C	DESIGN: RADIANT	REV: 1
SCALE: < Scale >			SHEET: 36 OF 73

DRAWN: PSA	DATED: 1/22/20
CHECKED: N/A	DATED: N/A
QUALITY CONTROL: N/A	DATED: N/A
RELEASED: N/A	DATED: N/A

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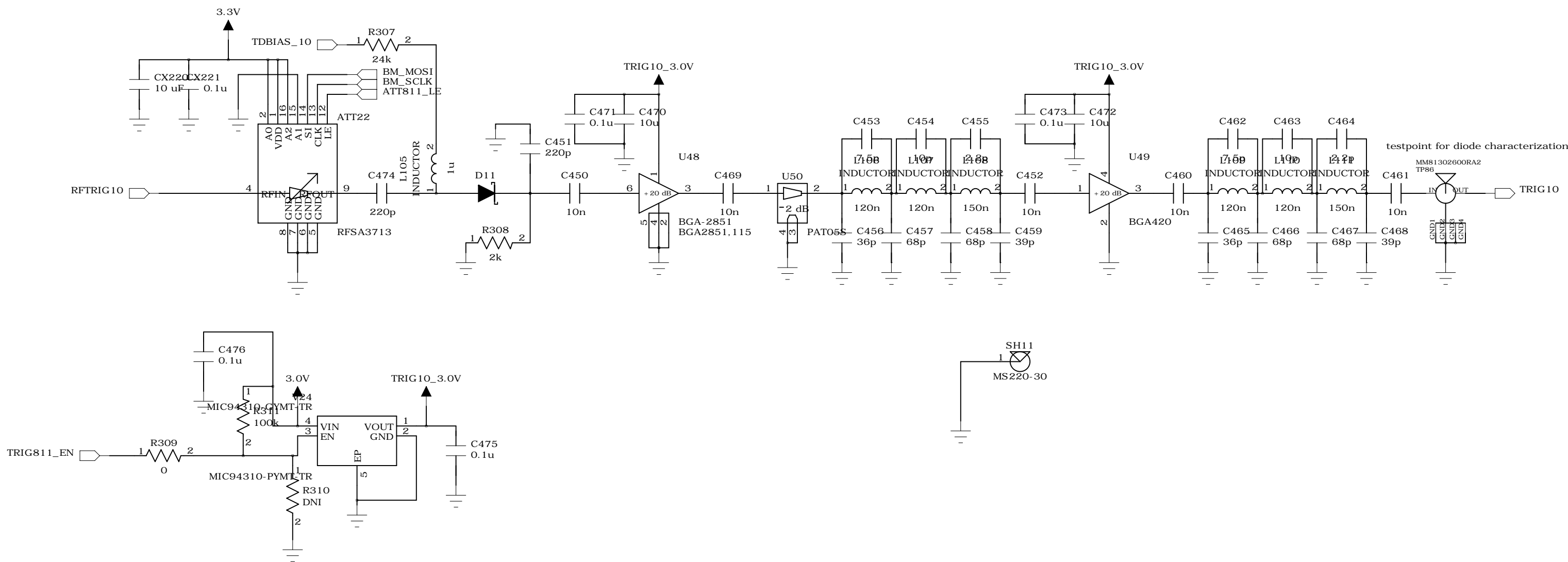
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REVISION RECORD

LTR	ECO NO:	APPROVED:	DATE:



COMPANY: Ohio State University			
SHEET: RF_DIOD10			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 37 of 73

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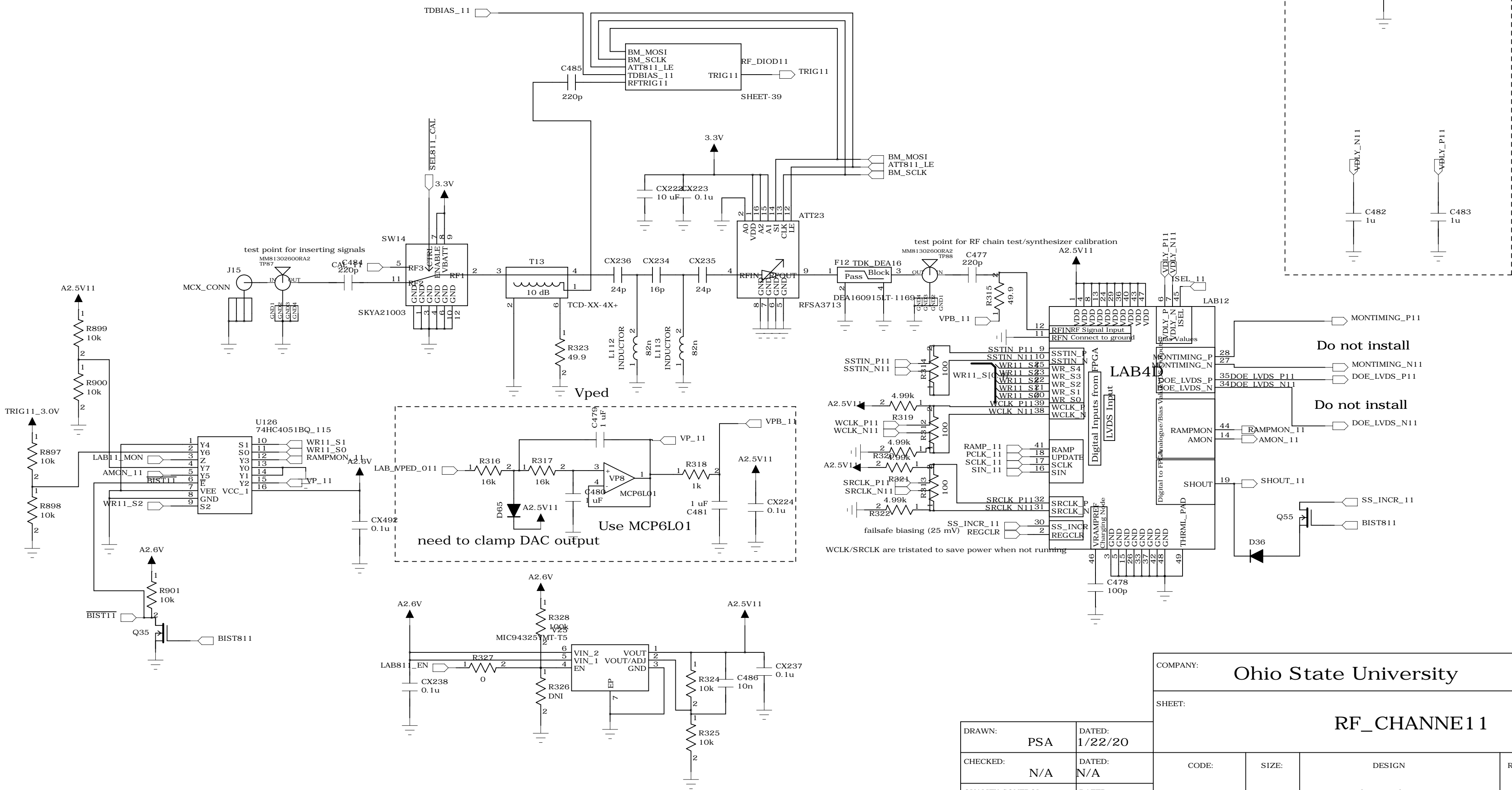
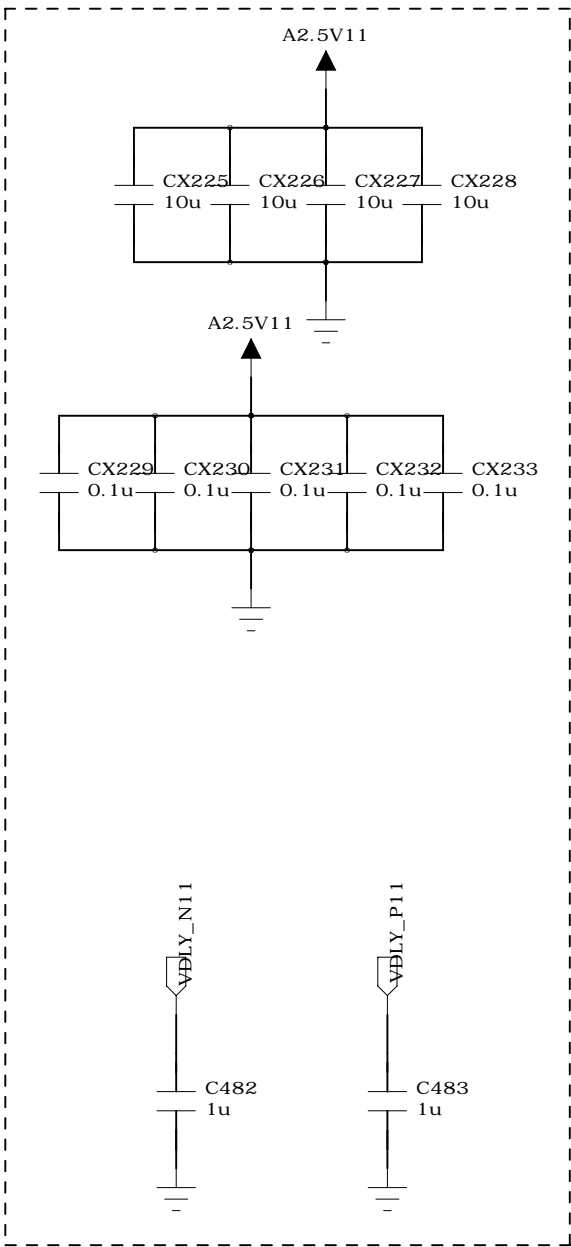
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
Filtering/bypass capacitors			



DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
QUALITY CONTROL:	N/A	DATED:	N/A
RELEASED:	N/A	DATED:	N/A

COMPANY: Ohio State University			
SHEET: RF_CHANNE11			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale >			SHEET: 38 OF 73

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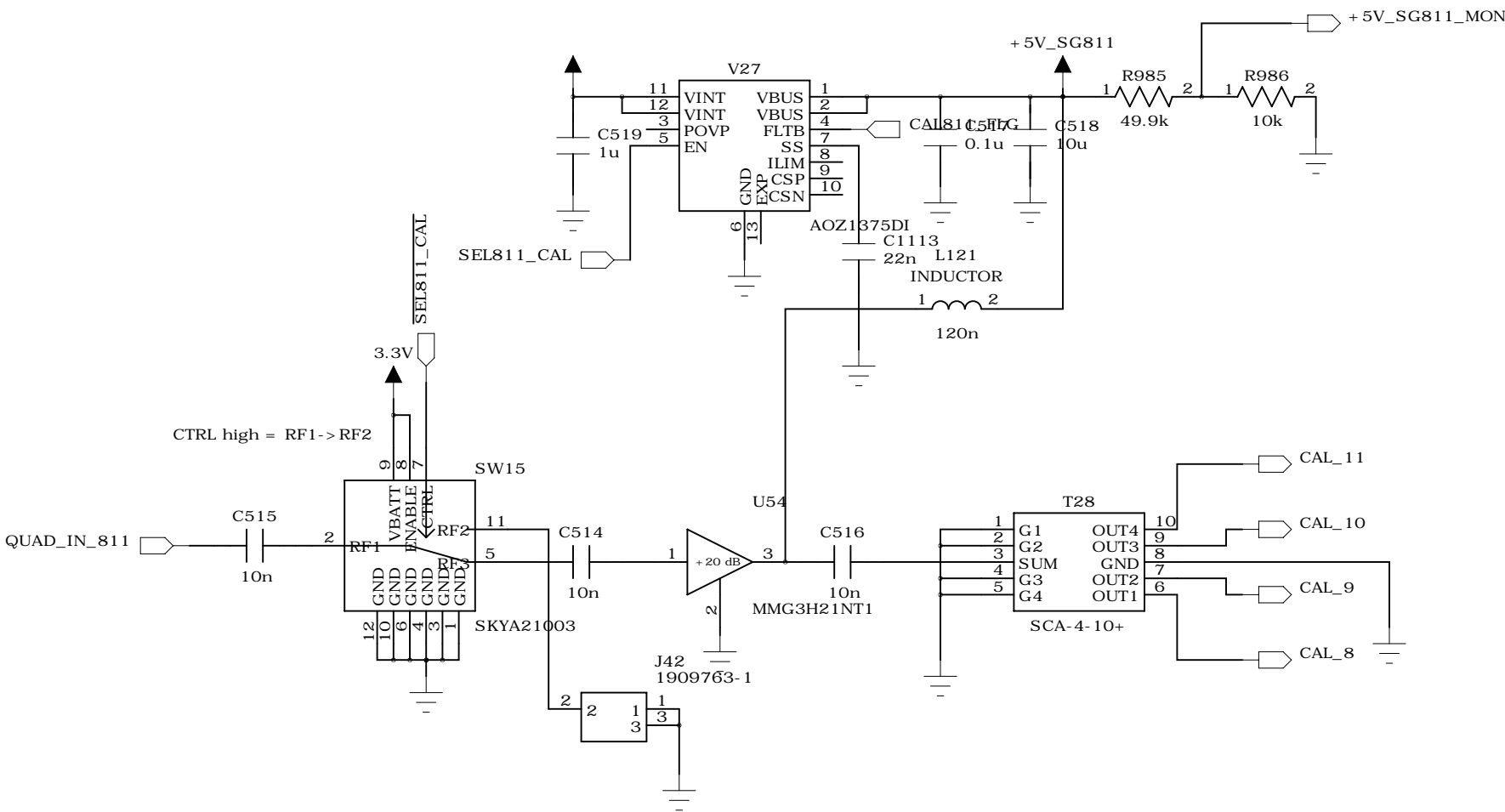
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
QUALITY CONTROL:	N/A	DATED:	N/A
RELEASED:	N/A	DATED:	N/A

COMPANY: Ohio State University			
SHEET: QUAD_SIG_GENERATOR_2			
CODE:	SIZE:	DESIGN:	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 40 of 73

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

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there's stuff here, it's just off-sheet cuz it's too big

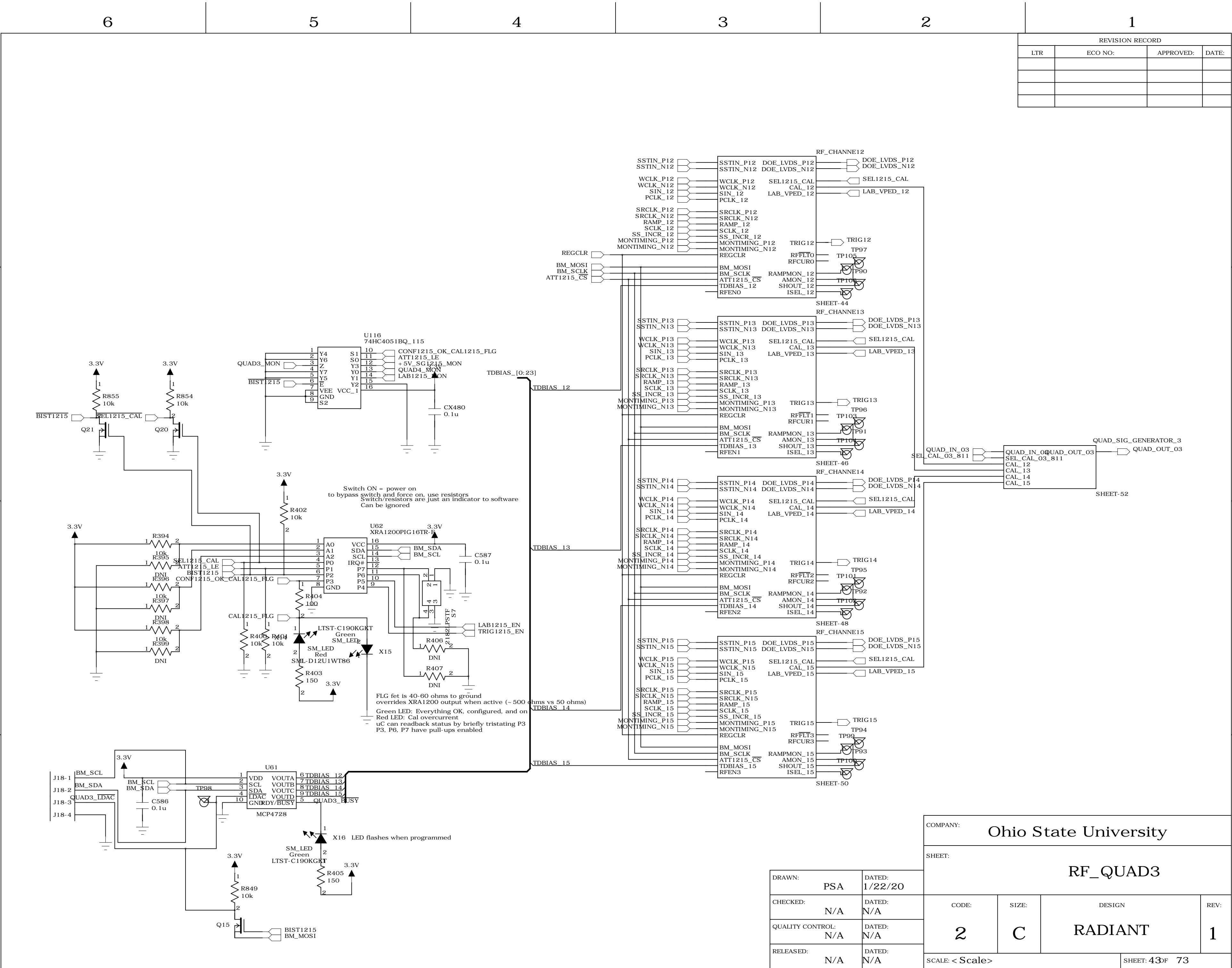
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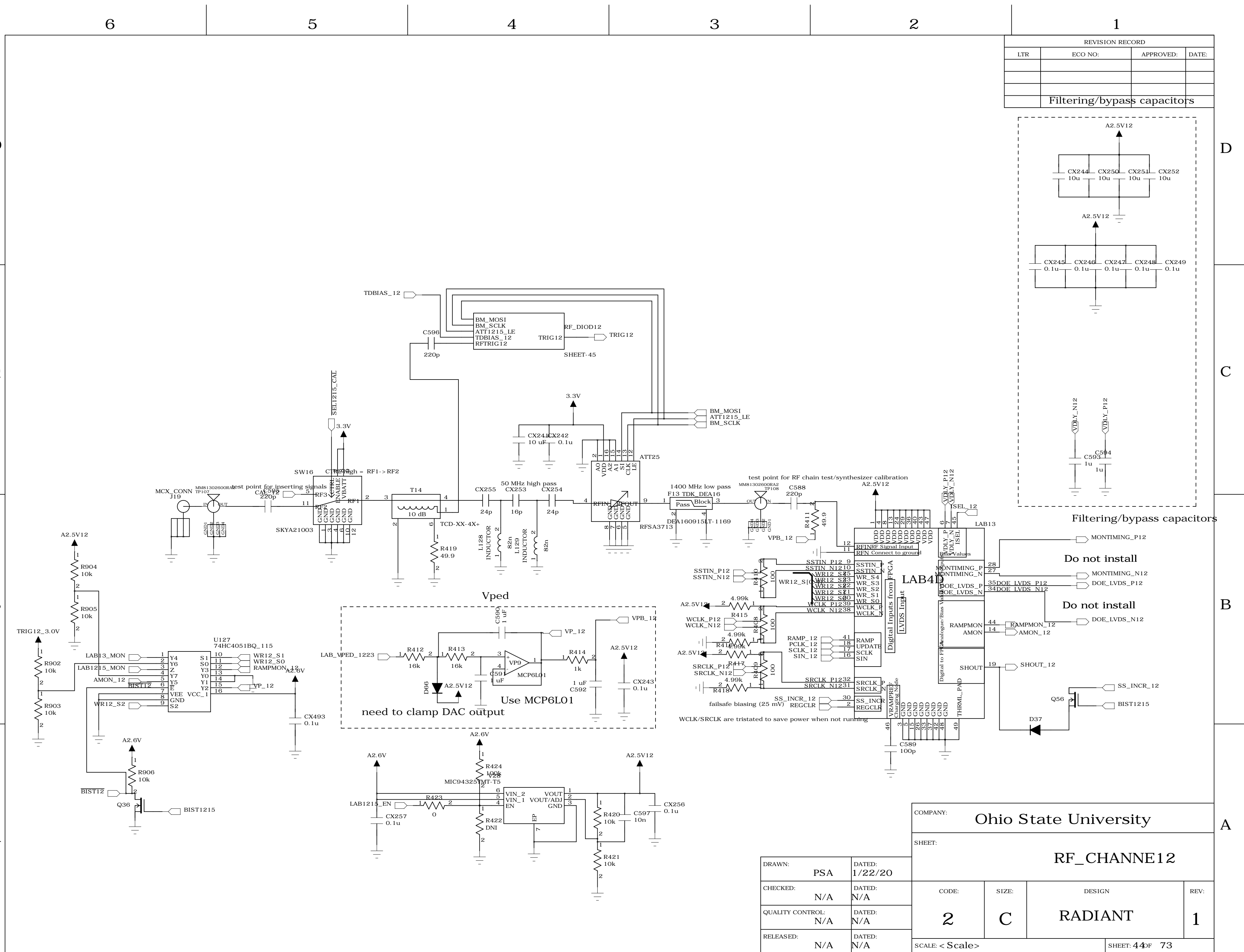
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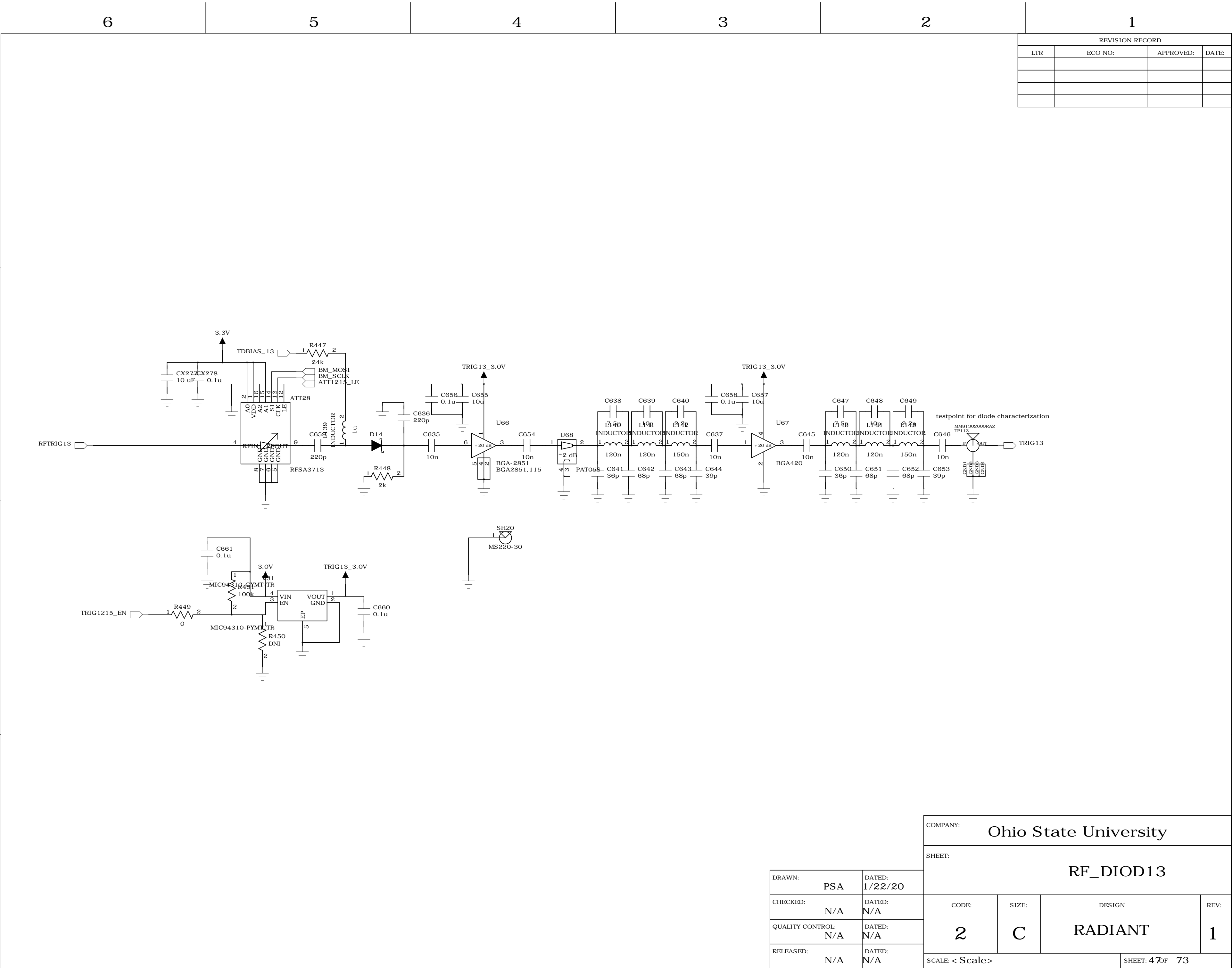
DRAWN: PSA		DATED: 1/22/20		AUXCPLDS				
CHECKED: N/A		DATED: N/A		CODE: 2	SIZE: C	DESIGN RADIANT		REV: 1
QUALITY CONTROL: N/A		DATED: N/A						
RELEASED: N/A		DATED: N/A						
SCALE: < Scale >							SHEET: 4 of 73	



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Ohio State University			
SHEET: RF_Quad3			
DRAWN: PSA	DATED: 1/22/20	CODE: 2	SIZE: C
CHECKED: N/A	DATED: N/A	DESIGN: RADIANT	REV: 1
QUALITY CONTROL: N/A	DATED: N/A	SHEET: 43 of 73	
RELEASED: N/A	DATED: N/A	SCALE: < Scale >	





REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Ohio State University			
SHEET: RF_DIOD13			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 47 of 73

DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
QUALITY CONTROL:	N/A	DATED:	N/A
RELEASED:	N/A	DATED:	N/A

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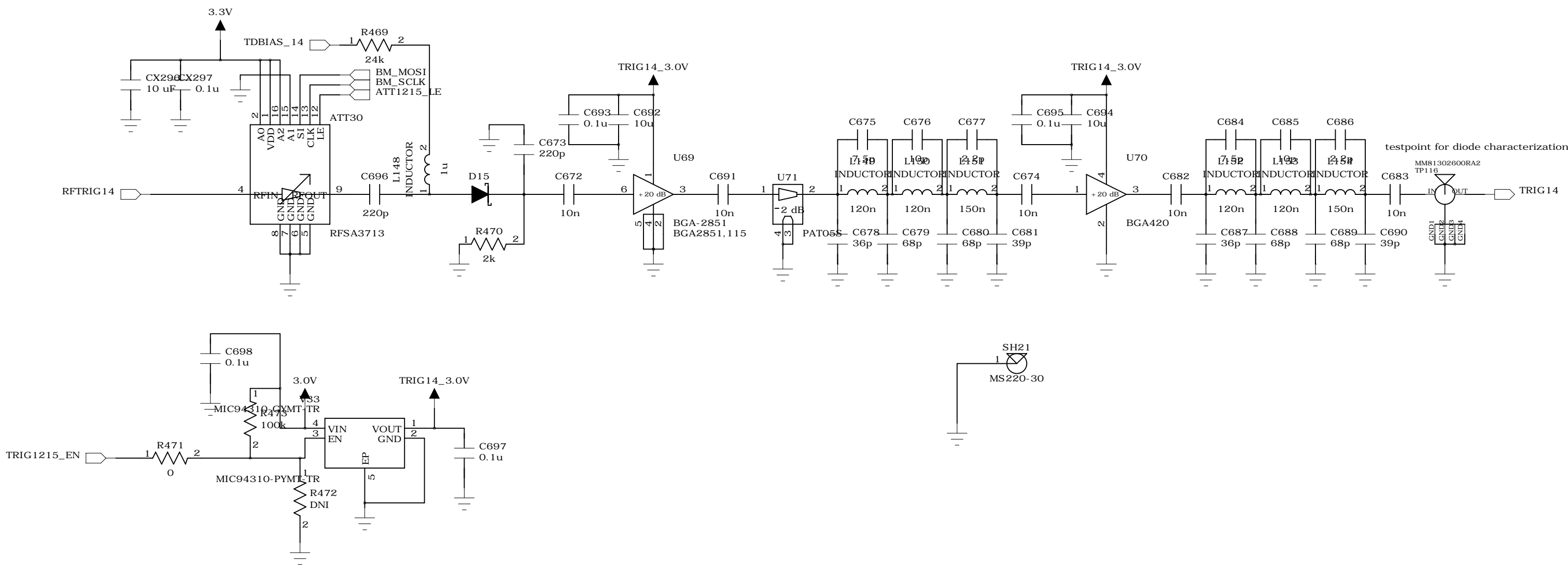
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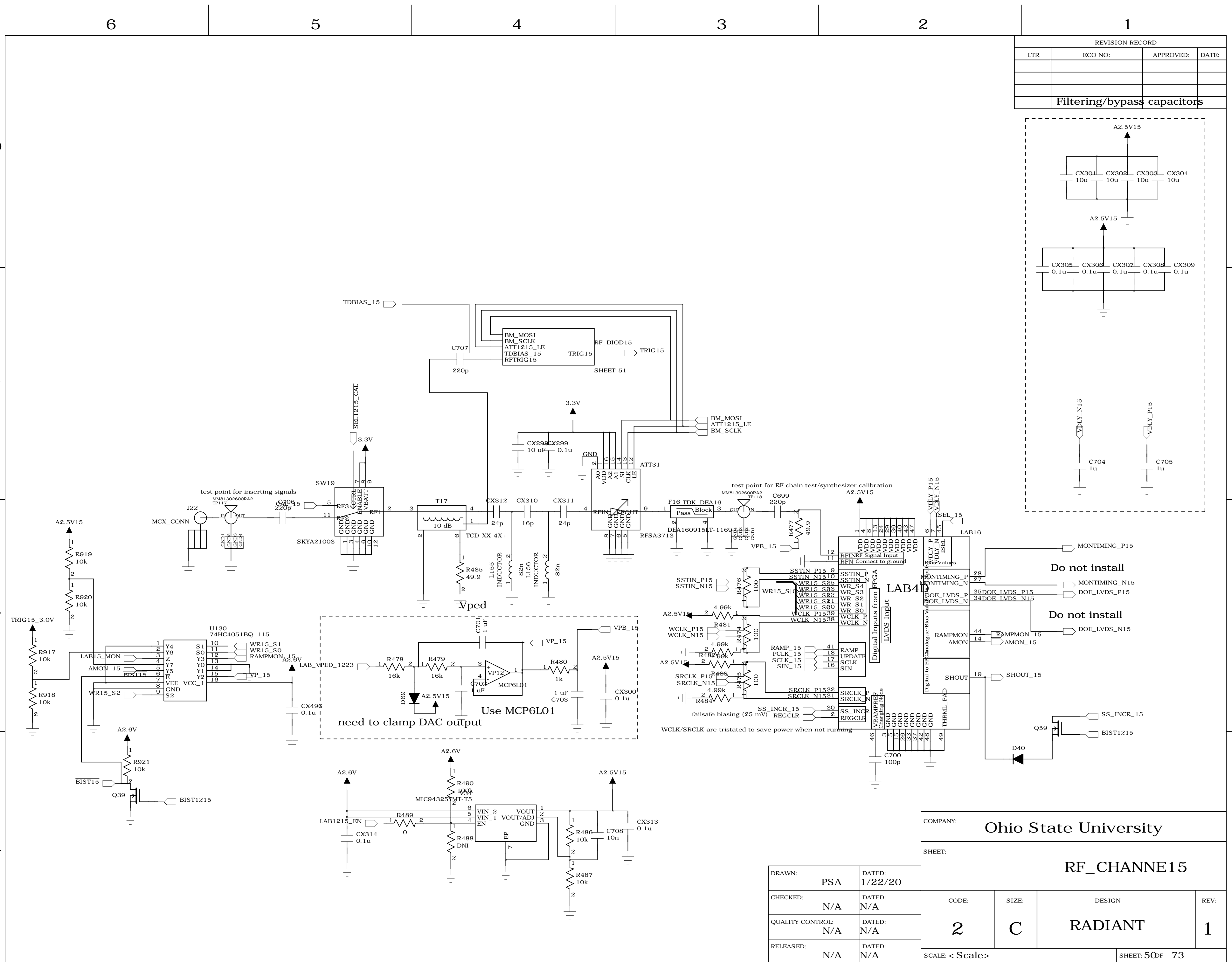
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: Ohio State University			
SHEET: RF_DIOD14			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 49 of 73



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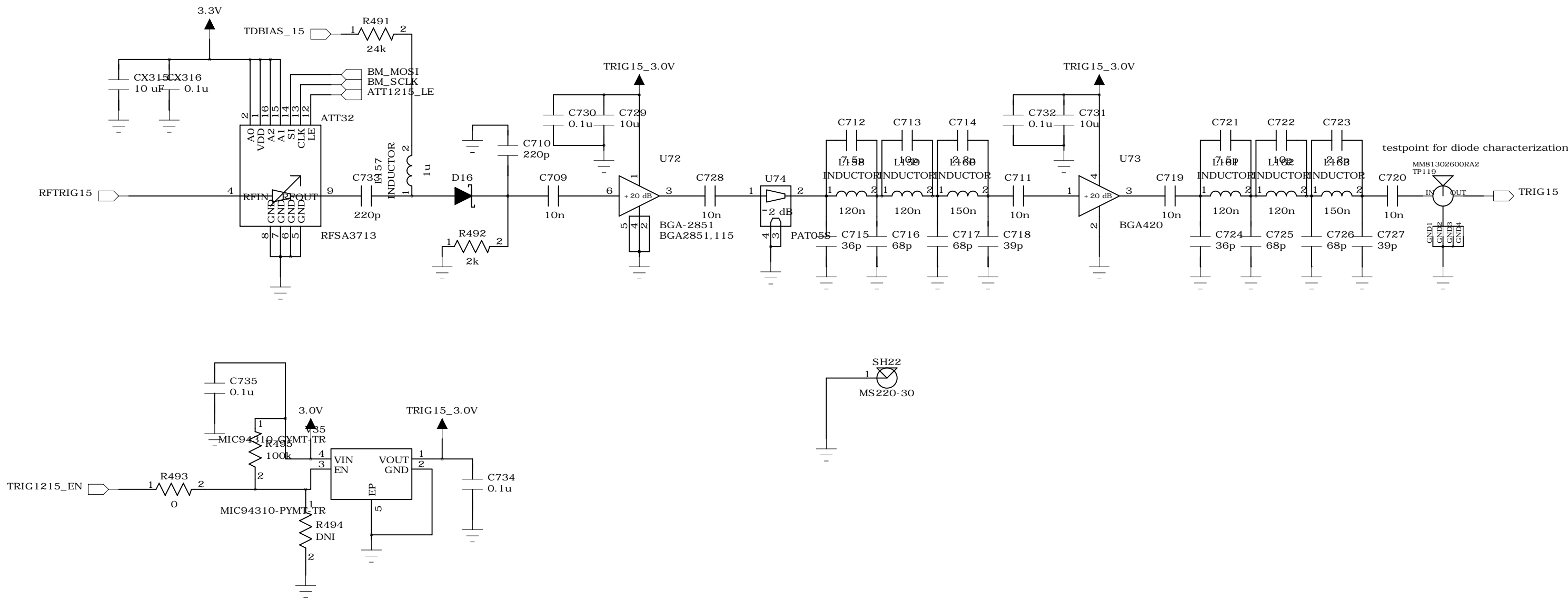
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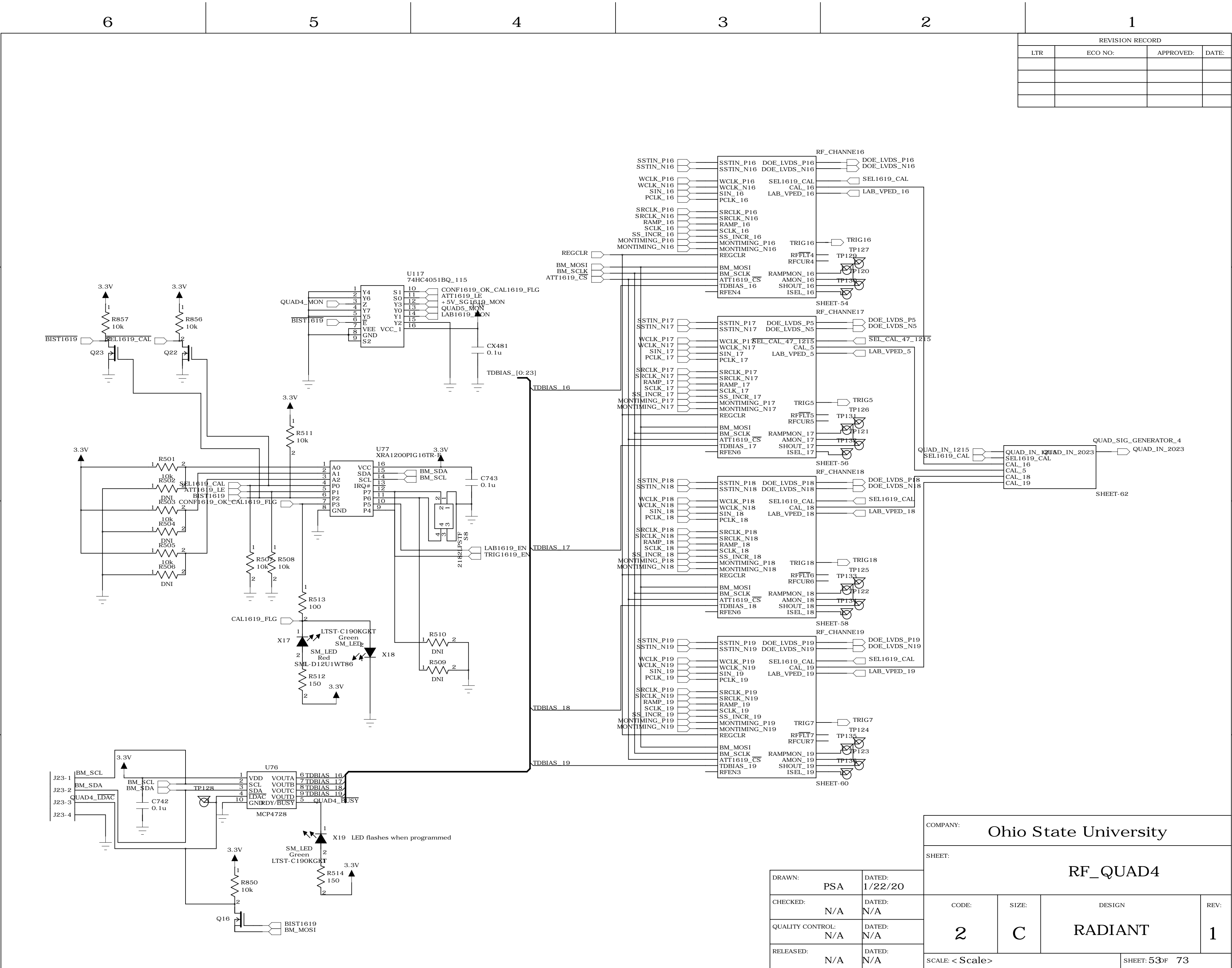
REVISION RECORD

LTR	ECO NO:	APPROVED:	DATE:



COMPANY: Ohio State University			
SHEET: RF_DIOD15			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 5 of 73

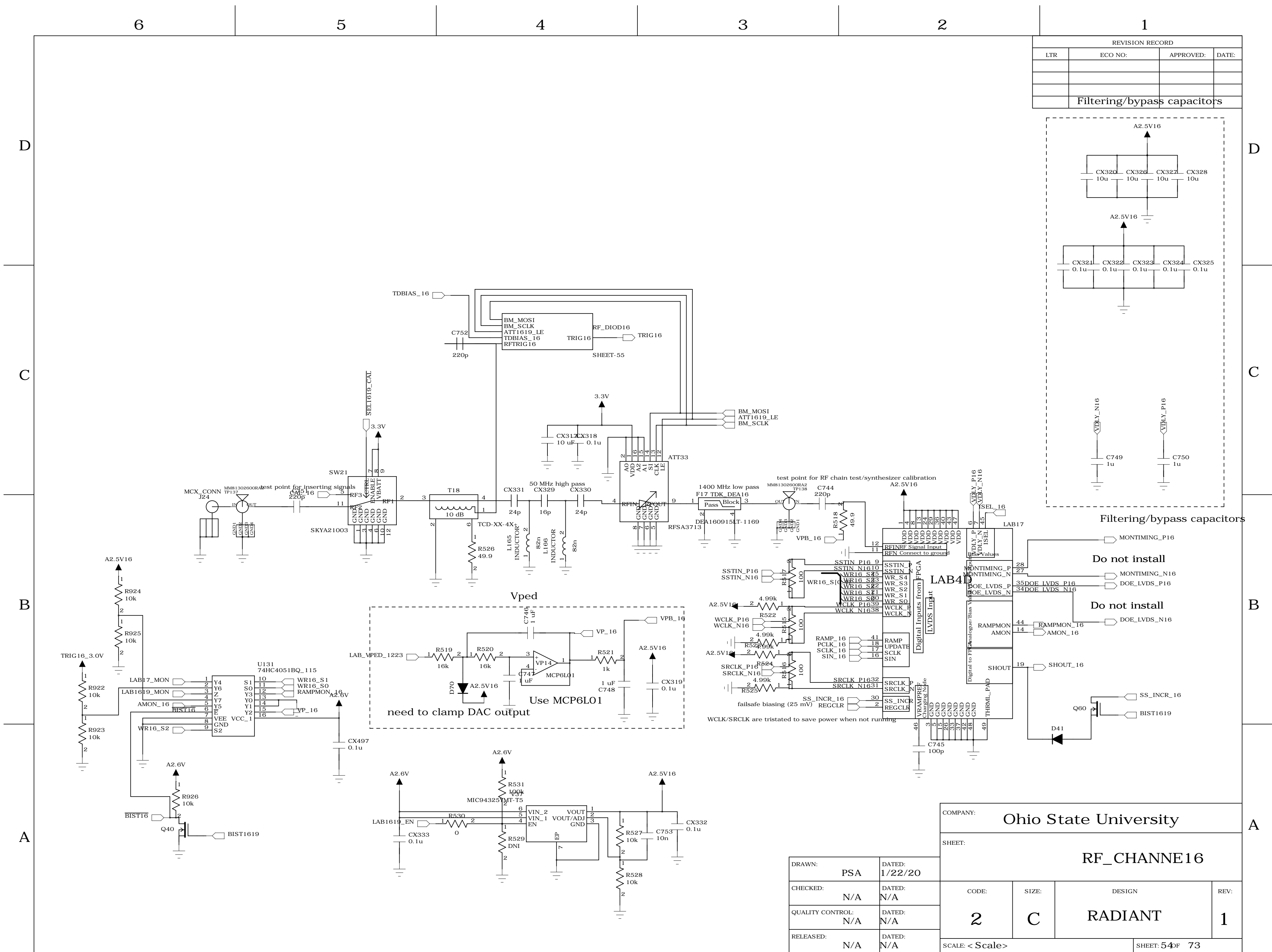
DRAWN: PSA	DATED: 1/22/20
CHECKED: N/A	DATED: N/A
QUALITY CONTROL: N/A	DATED: N/A
RELEASED: N/A	DATED: N/A

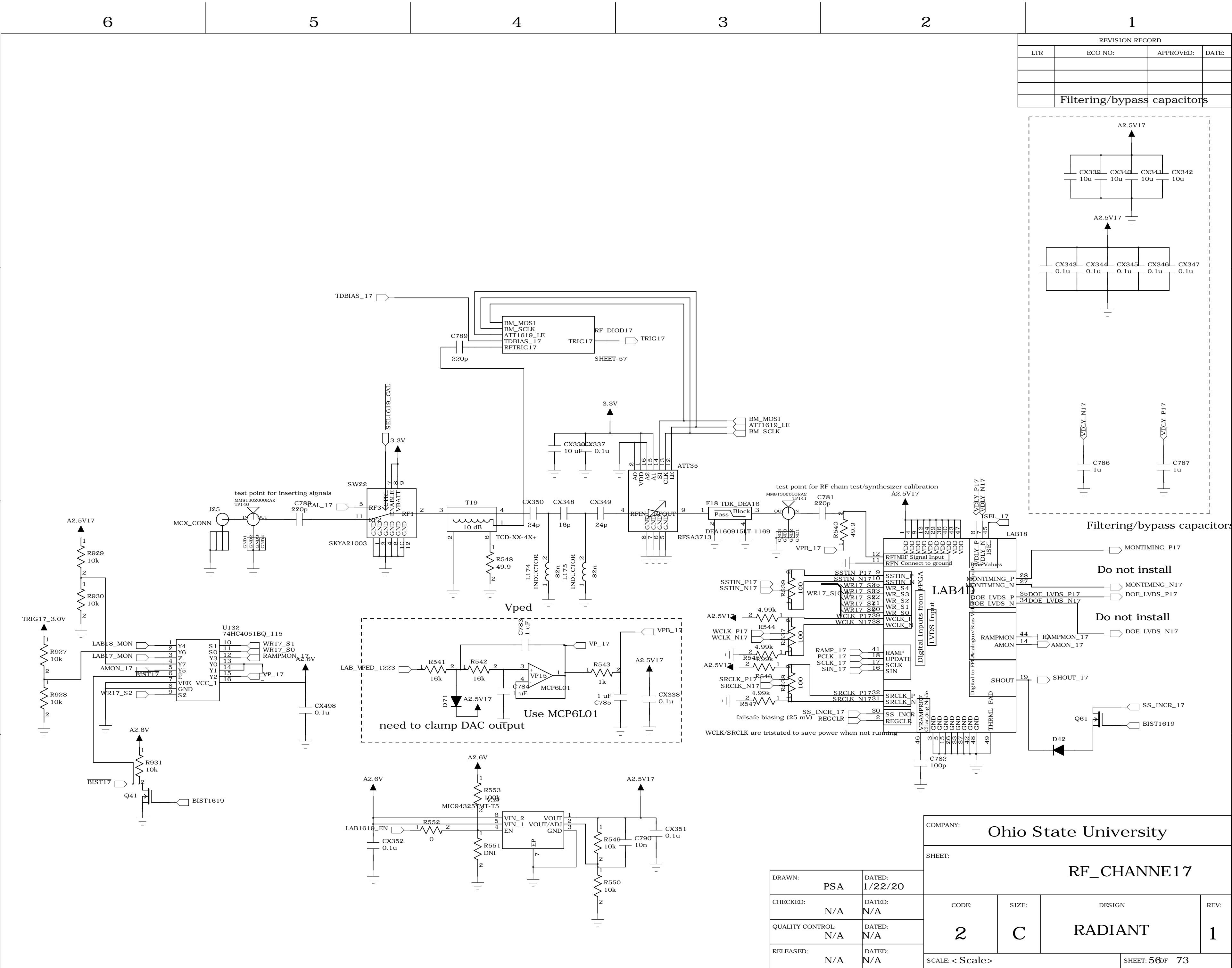


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

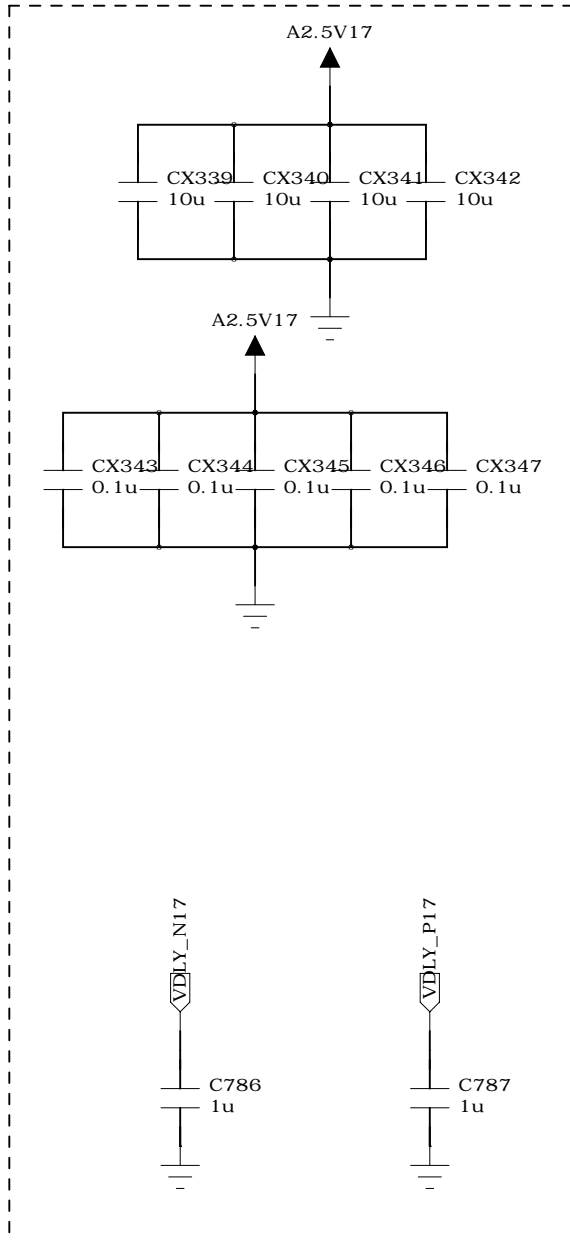
COMPANY: Ohio State University			
SHEET: RF_QUAD4			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 53 of 73

DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
QUALITY CONTROL:	N/A	DATED:	N/A
RELEASED:	N/A	DATED:	N/A





REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
Filtering/bypass capacitors			



Filtering/bypass capacitors

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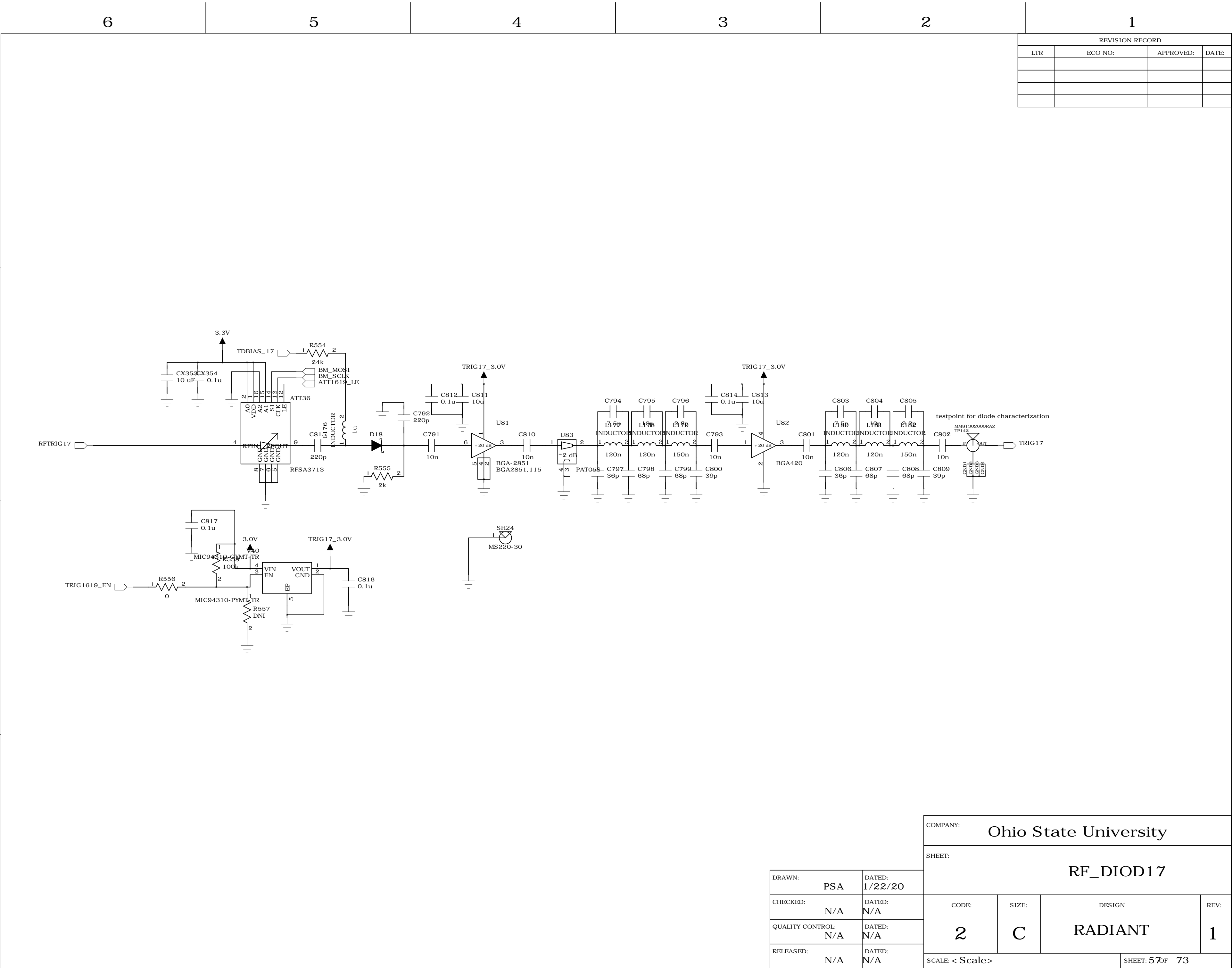
LAB735

LAB736

LAB737

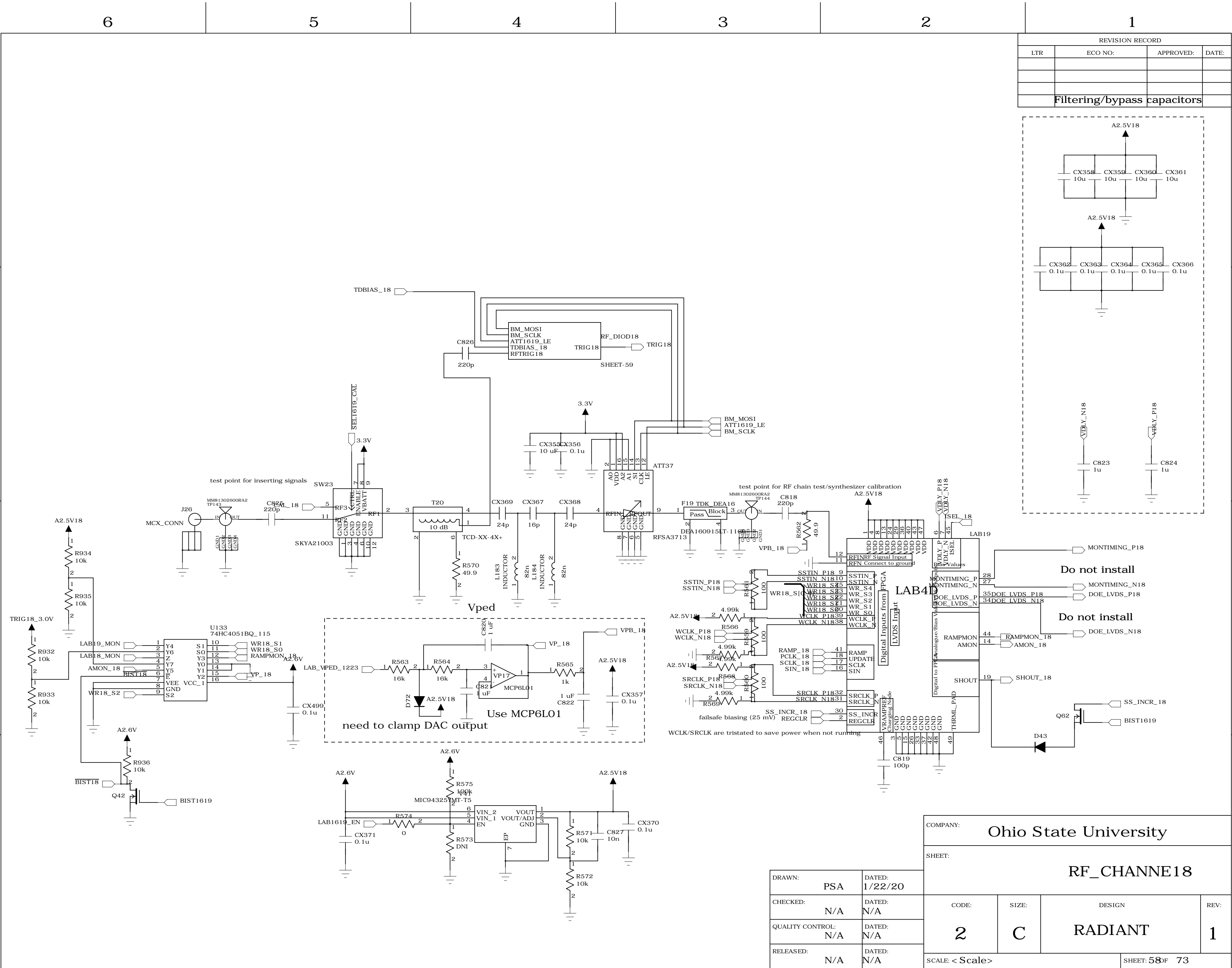
LAB738

LAB739

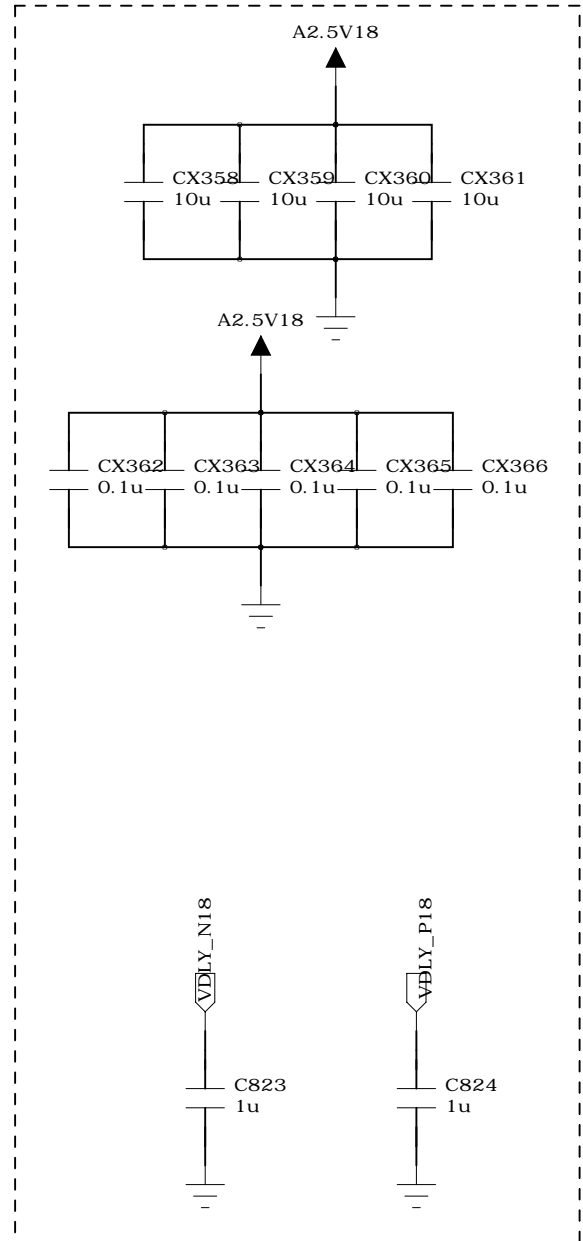


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

DRAWN: PSA		DATED: 1/22/20		COMPANY: Ohio State University			
CHECKED: N/A		DATED: N/A		SHEET: RF_DIOD17			
QUALITY CONTROL: N/A		DATED: N/A		CODE: 2	SIZE: C	DESIGN: RADIANT	REV: 1
RELEASED: N/A		DATED: N/A		SCALE: < Scale>			SHEET: 57 of 73



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
Filtering/bypass capacitors			



COMPANY: Ohio State University			
SHEET: RF_CHANNE18			
CODE: 2	SIZE: C	DESIGN: RADIANT	REV: 1
SCALE: < Scale >			SHEET: 58 OF 73

DRAWN: PSA	DATED: 1/22/20
CHECKED: N/A	DATED: N/A
QUALITY CONTROL: N/A	DATED: N/A
RELEASED: N/A	DATED: N/A

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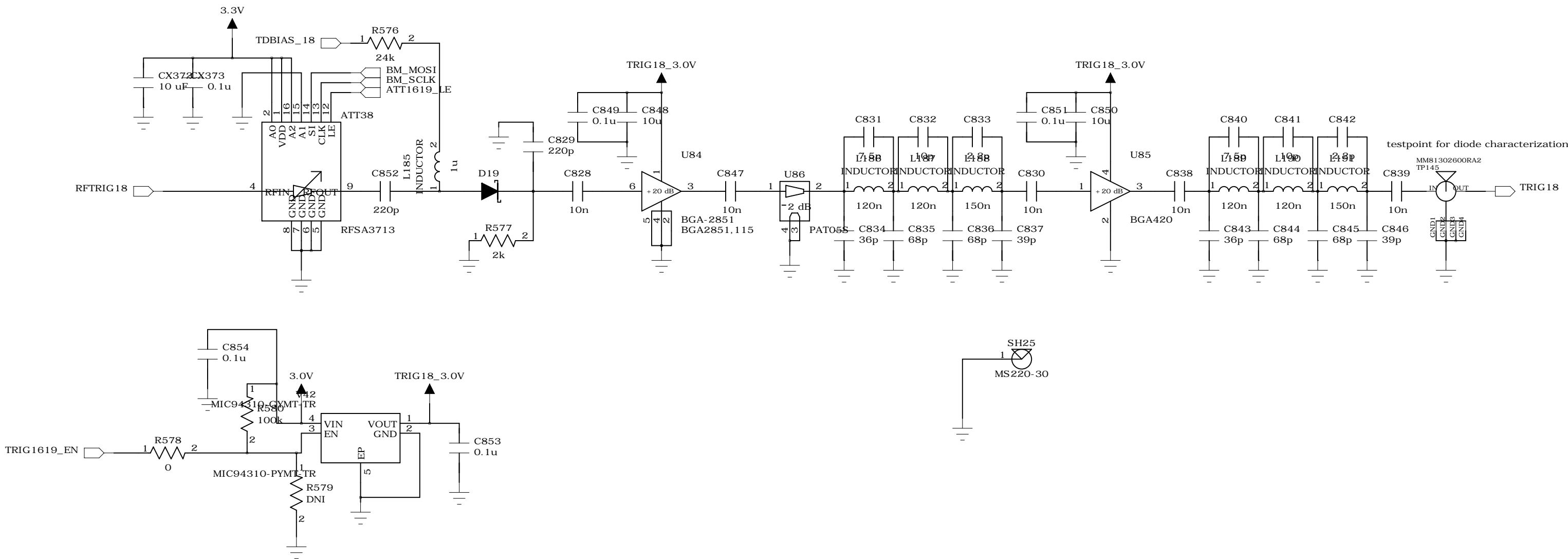
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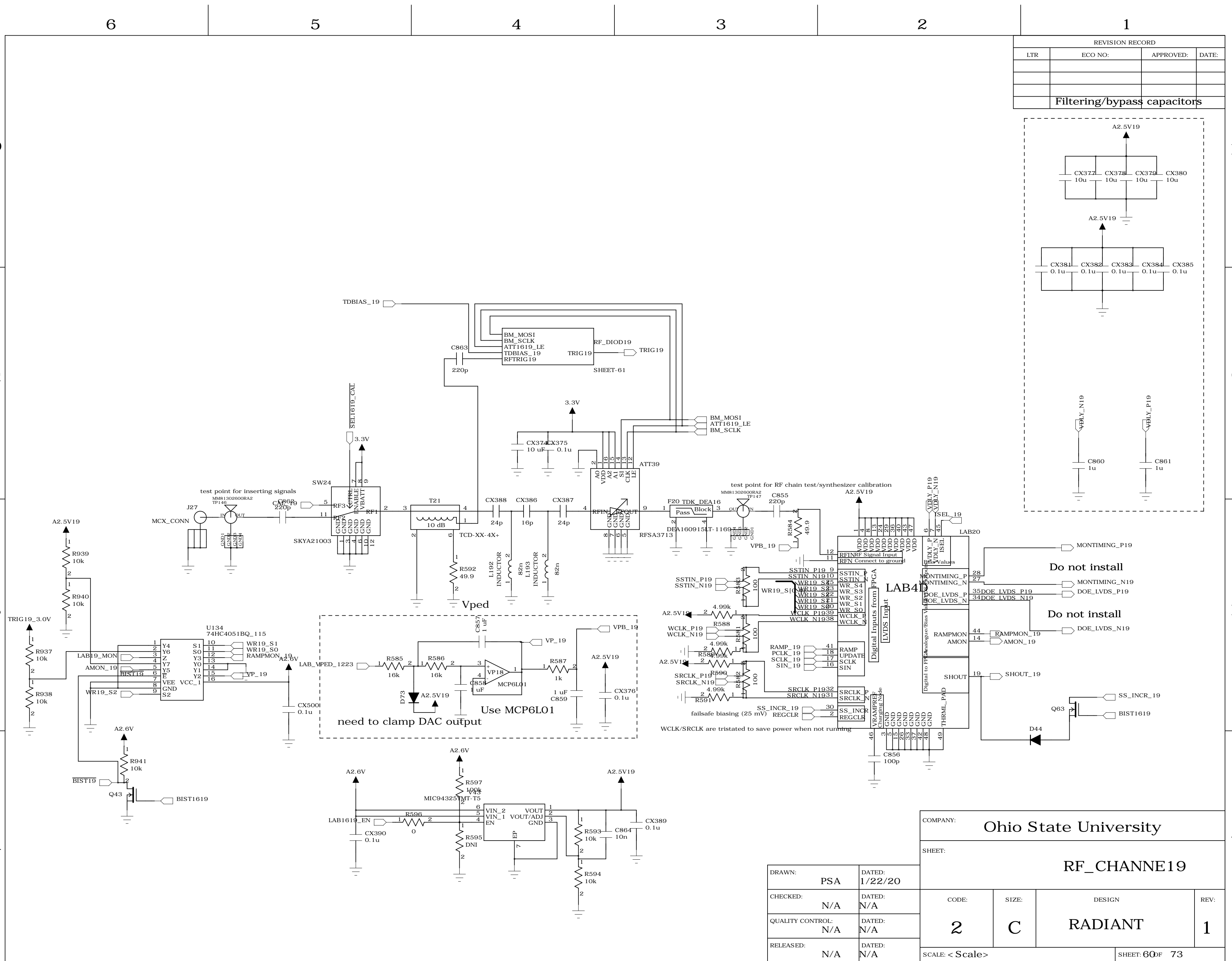
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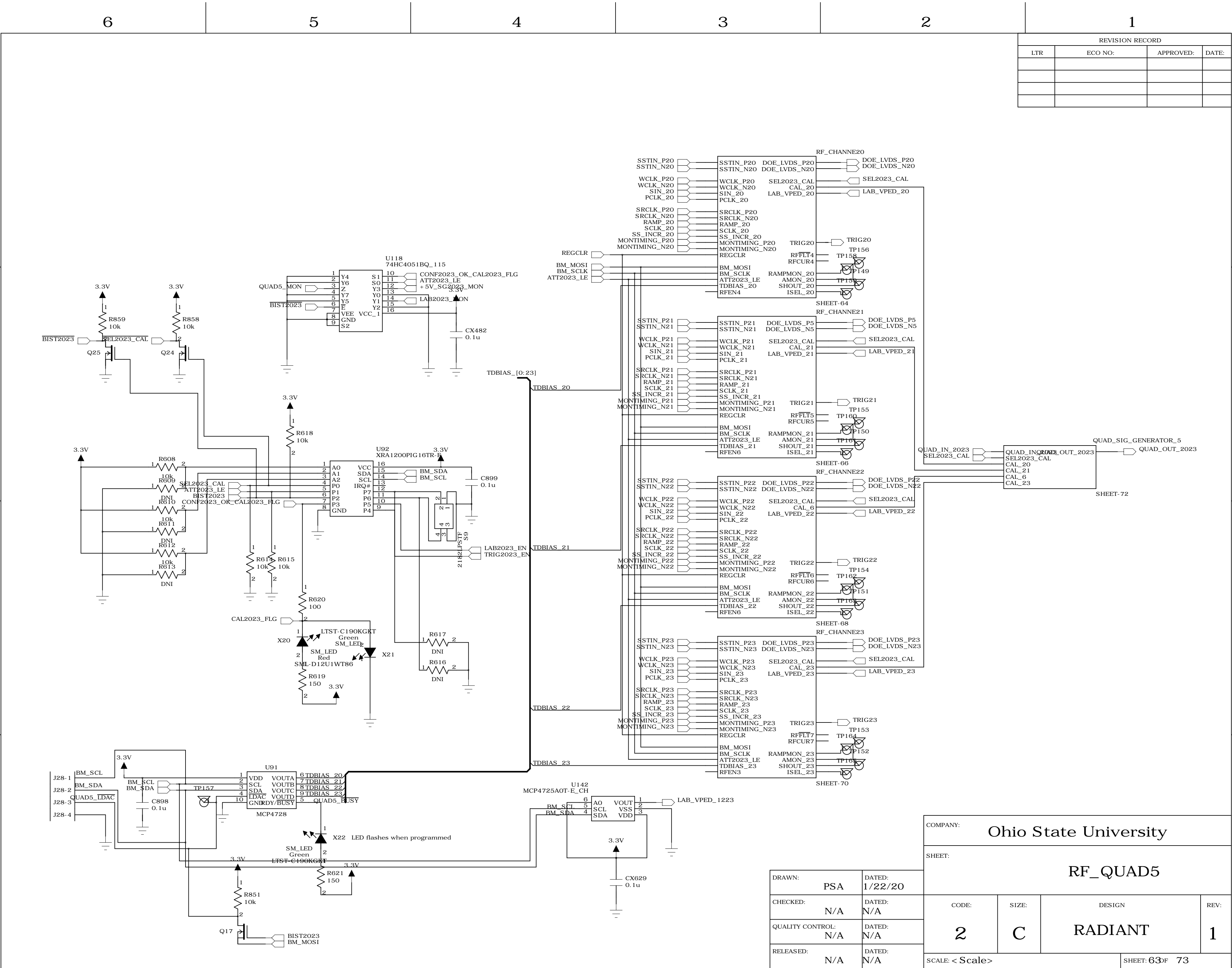
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: Ohio State University			
SHEET: RF_DIOD18			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 59 of 73

DRAWN: PSA	DATED: 1/22/20
CHECKED: N/A	DATED: N/A
QUALITY CONTROL: N/A	DATED: N/A
RELEASED: N/A	DATED: N/A

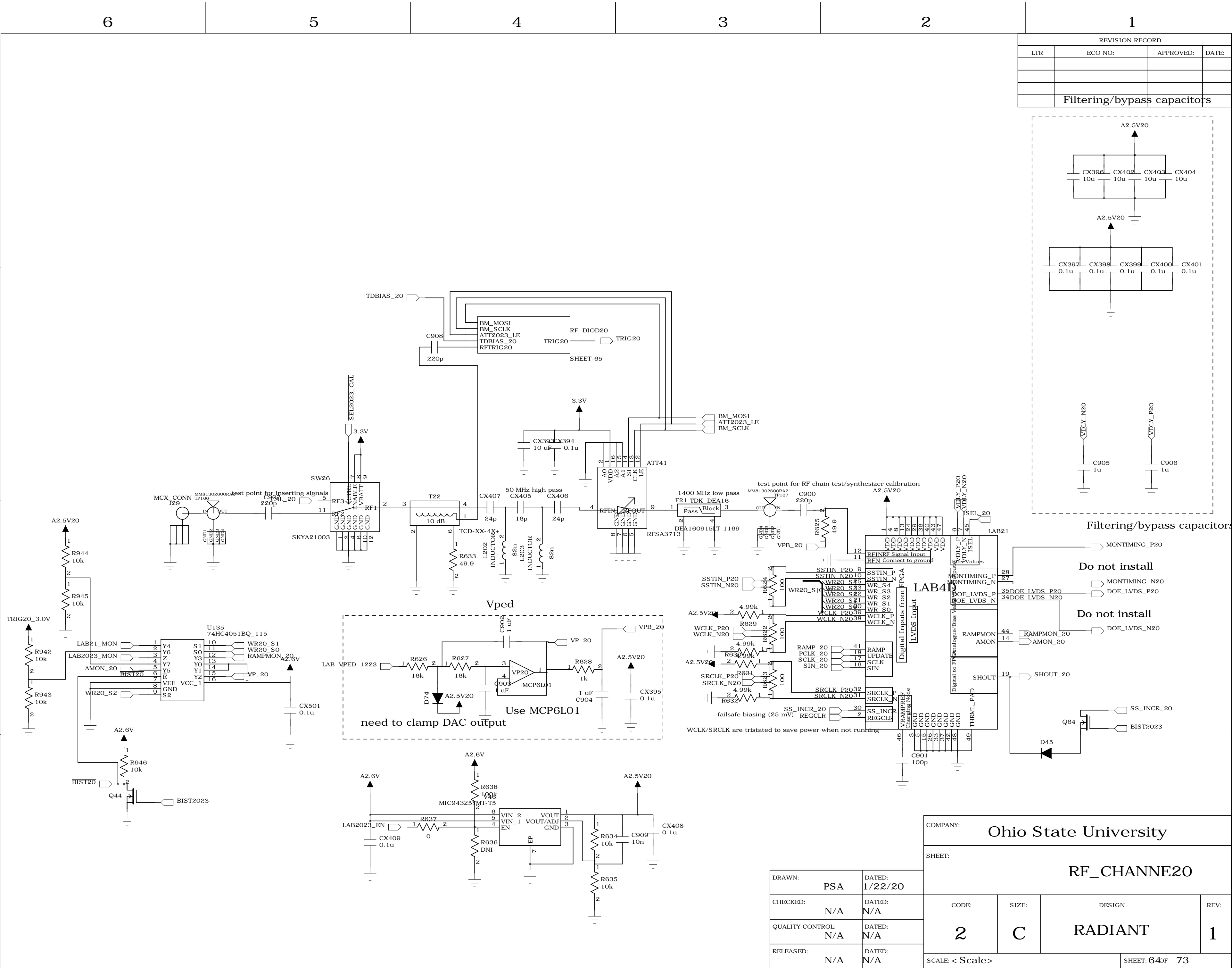




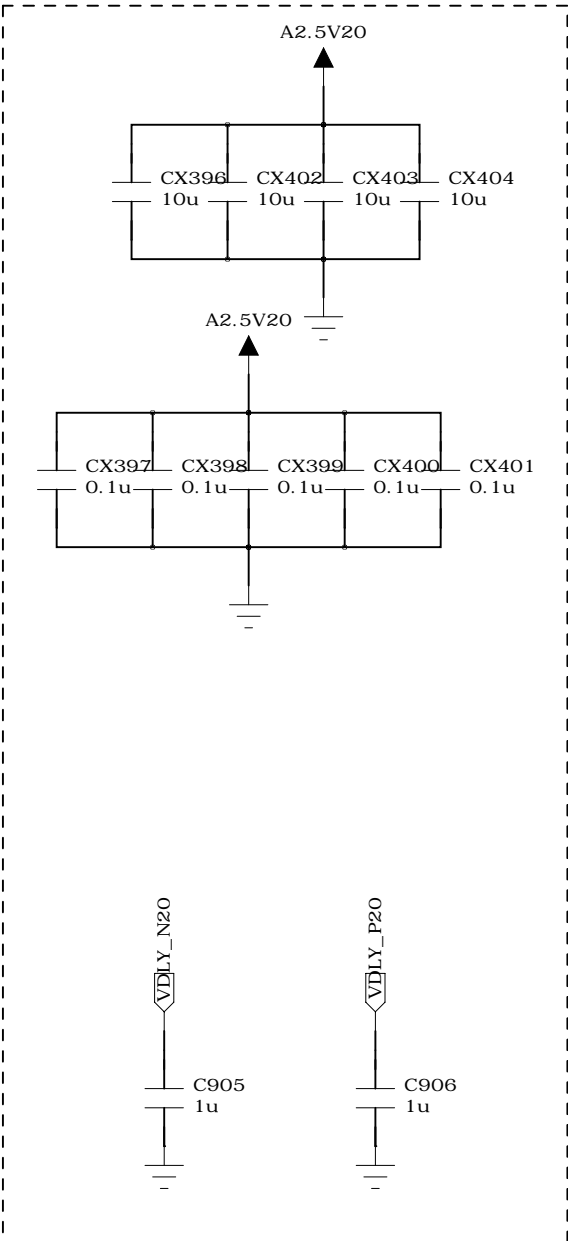
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

COMPANY: Ohio State University			
SHEET: RF_QUAD5			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 63 of 73

DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
QUALITY CONTROL:	N/A	DATED:	N/A
RELEASED:	N/A	DATED:	N/A



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
Filtering/bypass capacitors			



Filtering/bypass capacitors

COMPANY: Ohio State University			
SHEET: RF_CHANNE20			
CODE: 2	SIZE: C	DESIGN: RADIANT	REV: 1
SCALE: < Scale >			SHEET: 64 of 73

DRAWN: PSA	DATED: 1/22/20
CHECKED: N/A	DATED: N/A
QUALITY CONTROL: N/A	DATED: N/A
RELEASED: N/A	DATED: N/A

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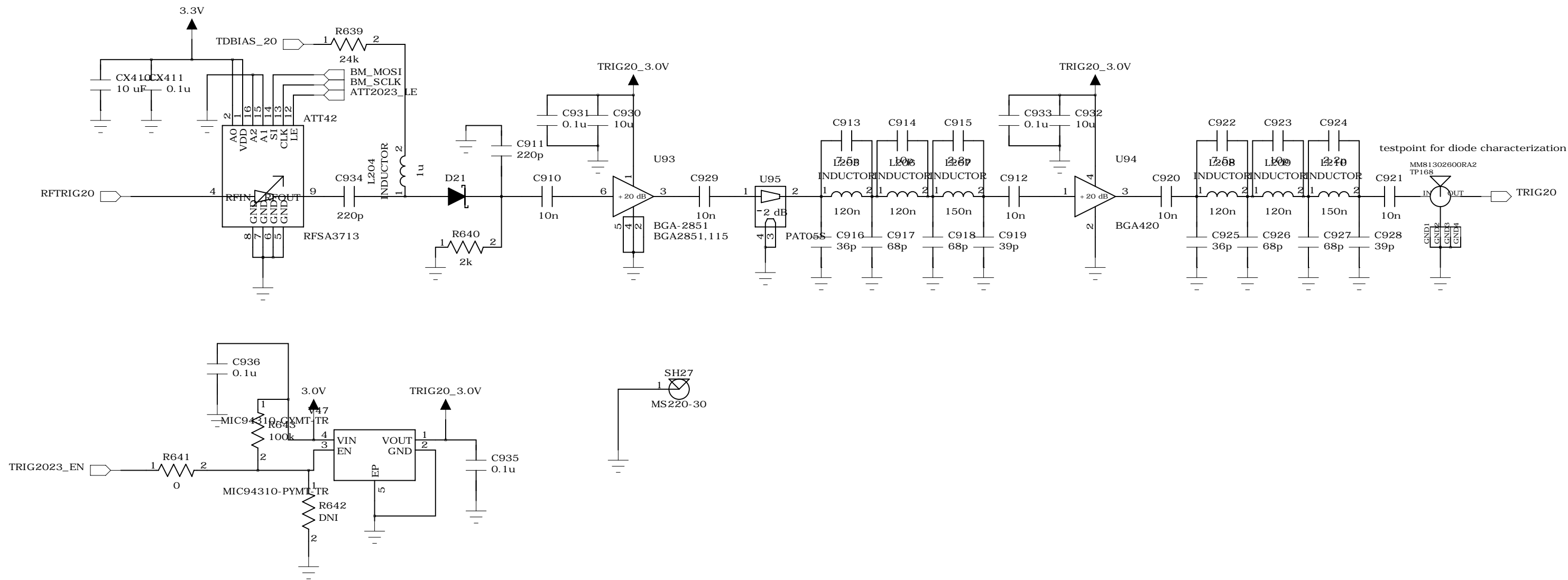
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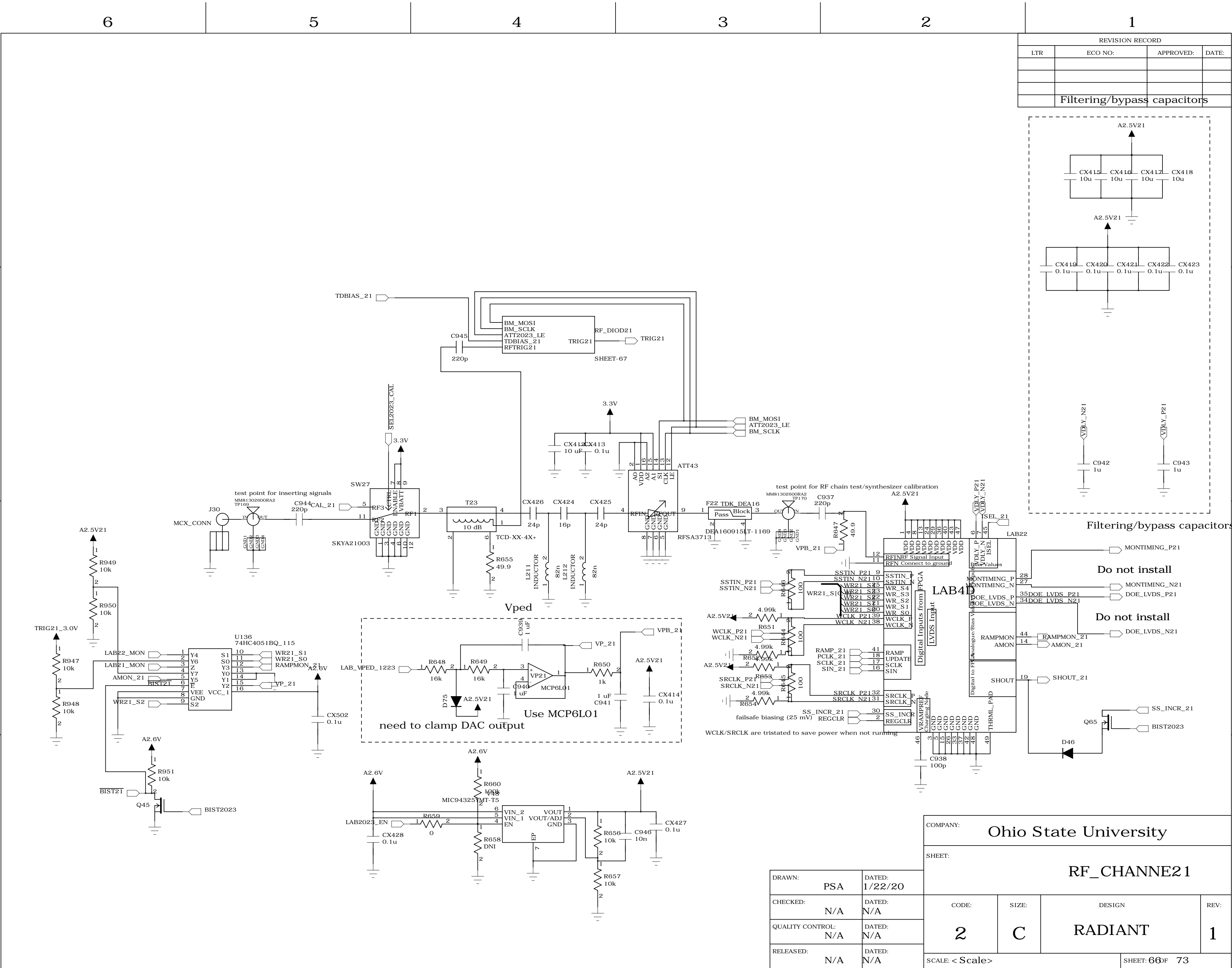
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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

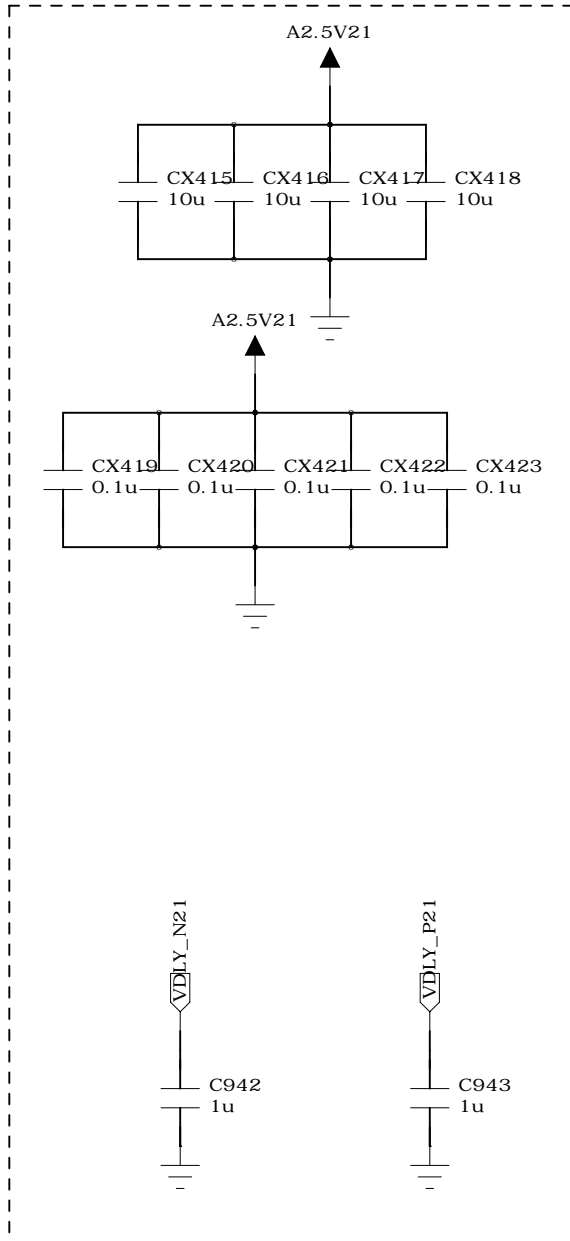


DRAWN:	PSA	DATED:	1/22/20
CHECKED:	N/A	DATED:	N/A
QUALITY CONTROL:	N/A	DATED:	N/A
RELEASED:	N/A	DATED:	N/A

COMPANY: Ohio State University			
SHEET: RF_DIOD20			
CODE:	SIZE:	DESIGN	REV:
2	C	RADIANT	1
SCALE: < Scale>			SHEET: 65 of 73



REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
Filtering/bypass capacitors			



Filtering/bypass capacitors

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LAB353

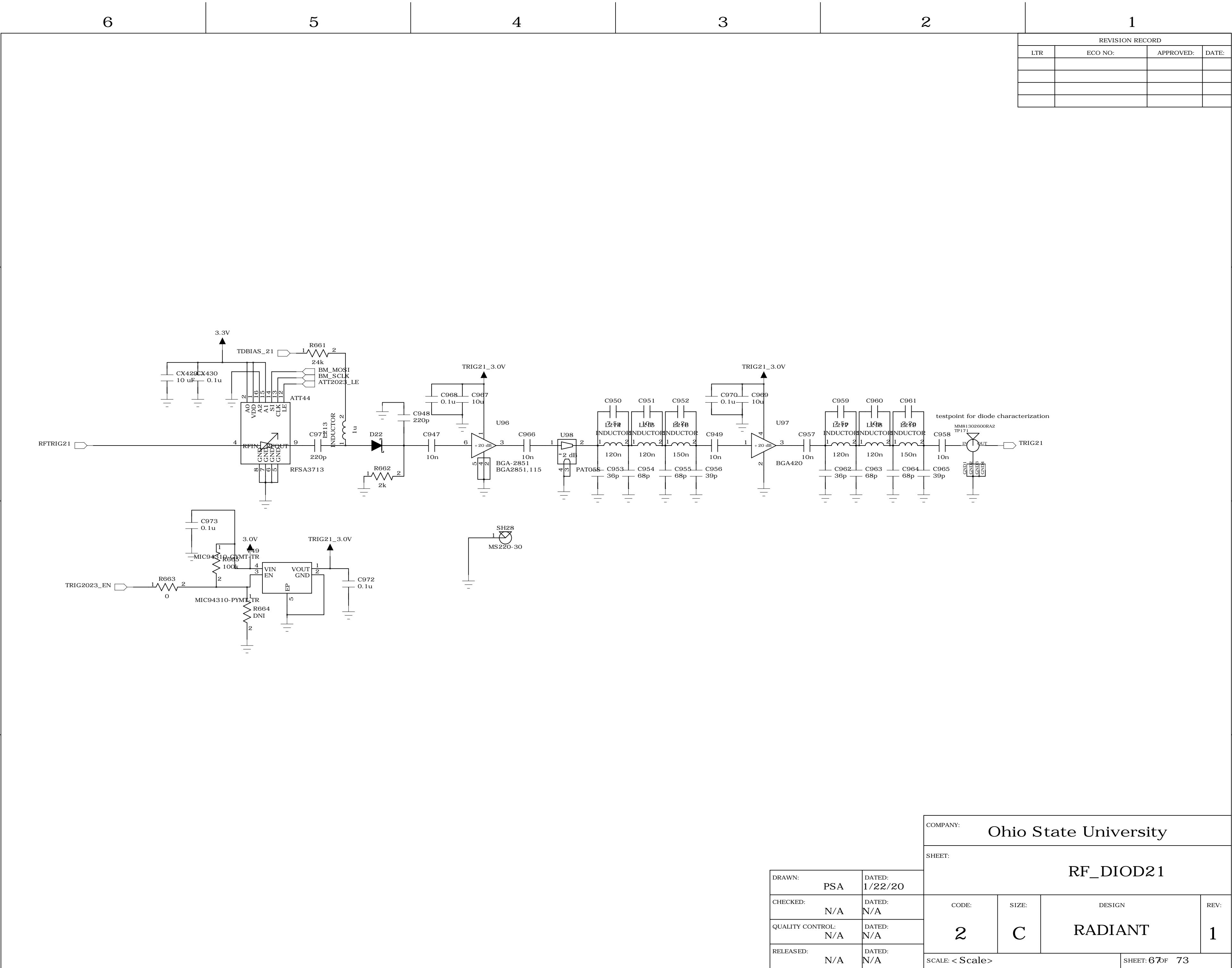
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LAB355

LAB356

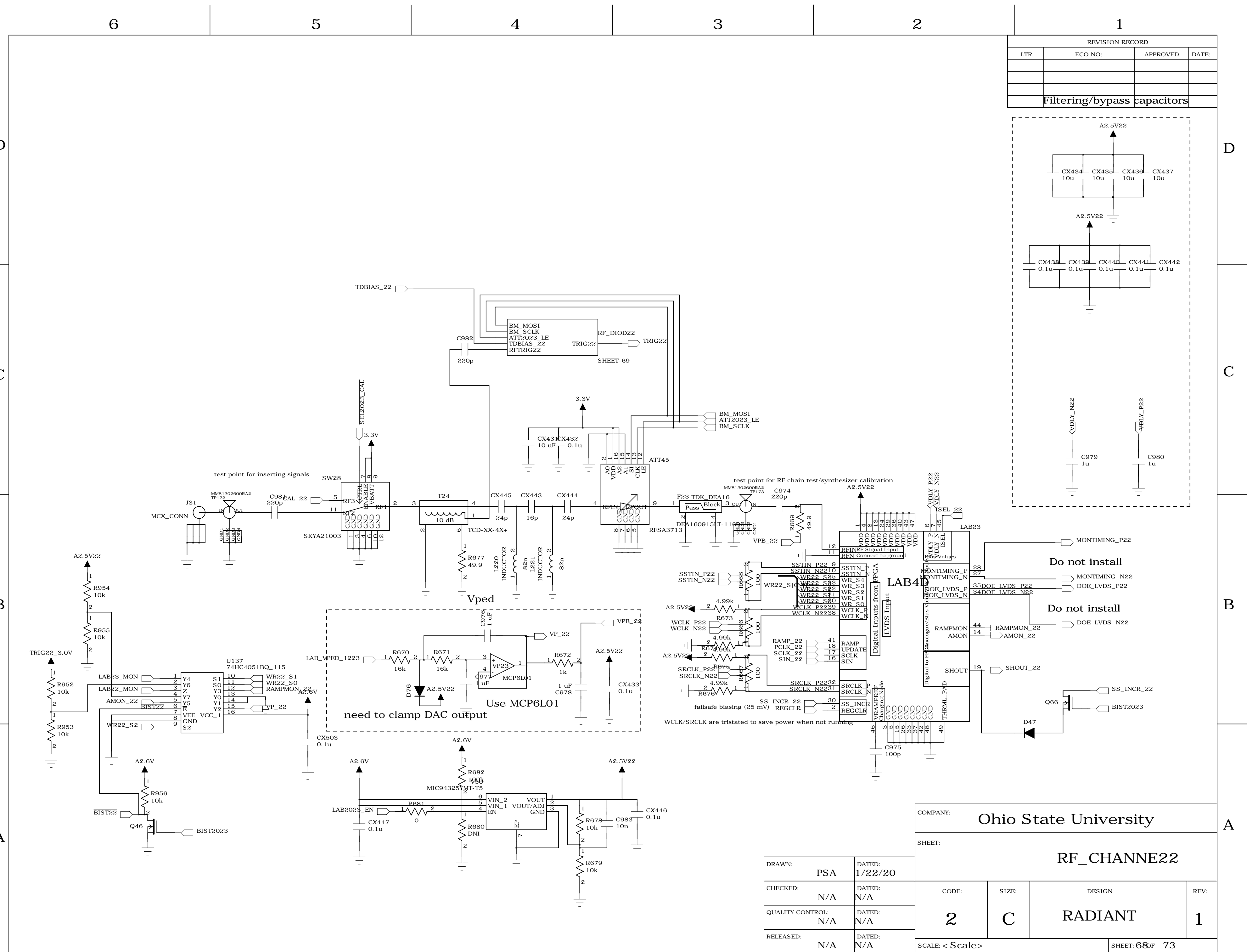
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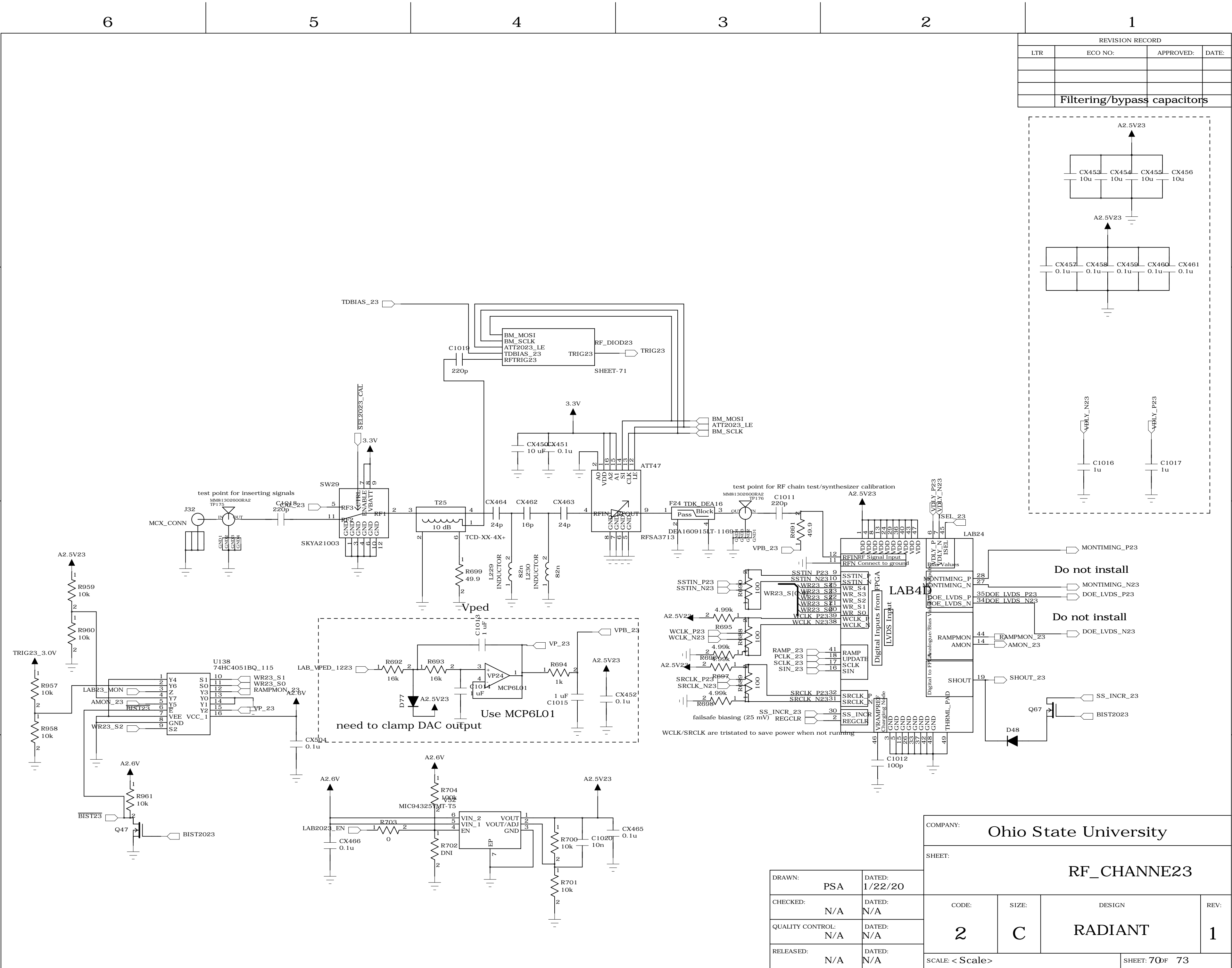
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QUALITY CONTROL: N/A		DATED: N/A		CODE: 2	SIZE: C	DESIGN: RADIANT	REV: 1
RELEASED: N/A		DATED: N/A		SCALE: < Scale>			SHEET: 67 of 73





REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
Filtering/bypass capacitors			

COMPANY: Ohio State University			
SHEET: RF_CHANNE23			
DRAWN: PSA	DATED: 1/22/20	CODE: 2	SIZE: C
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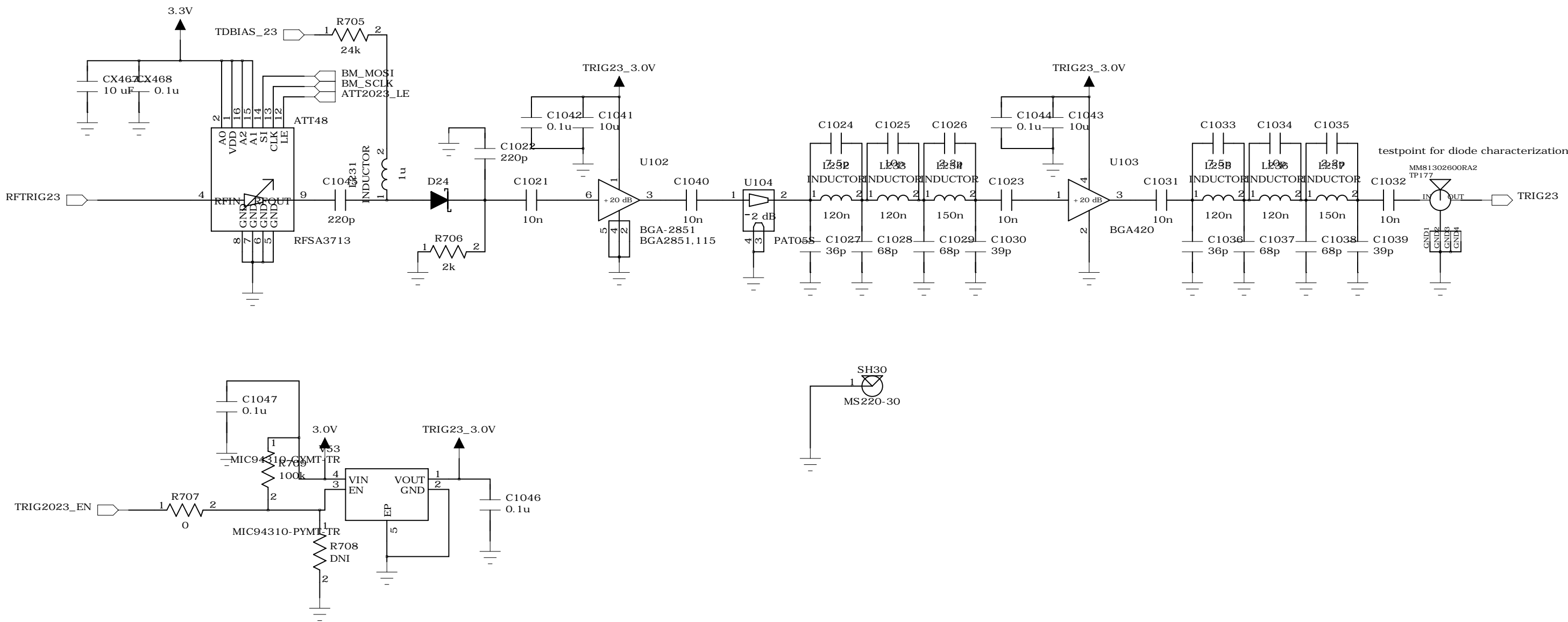
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REVISION RECORD			
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6

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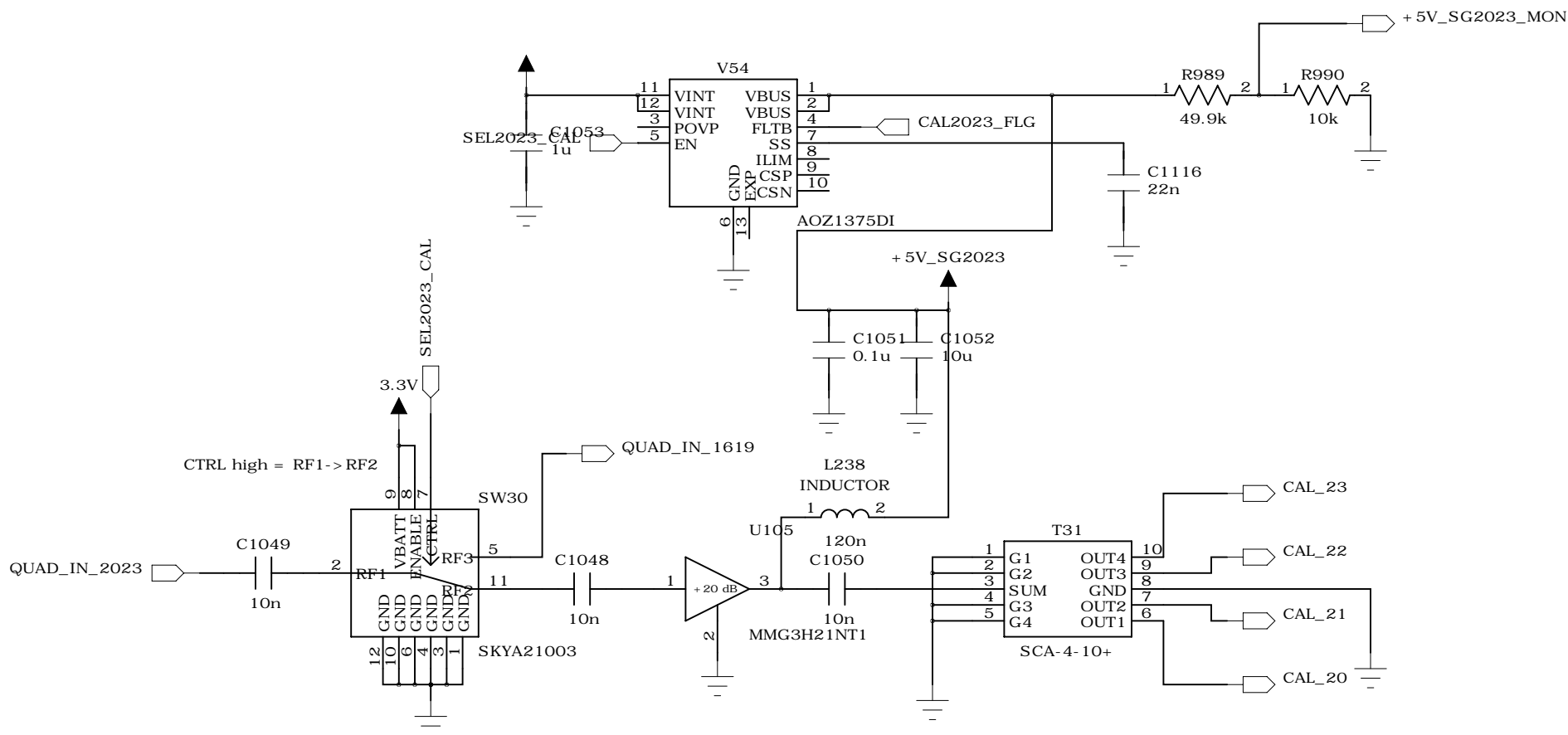
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REVISION RECORD			
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COMPANY: Ohio State University			
SHEET: QUAD_SIG_GENERATOR_5			
CODE:	SIZE:	DESIGN	REV:
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SCALE: < Scale>			SHEET: 73 of 73

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REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

