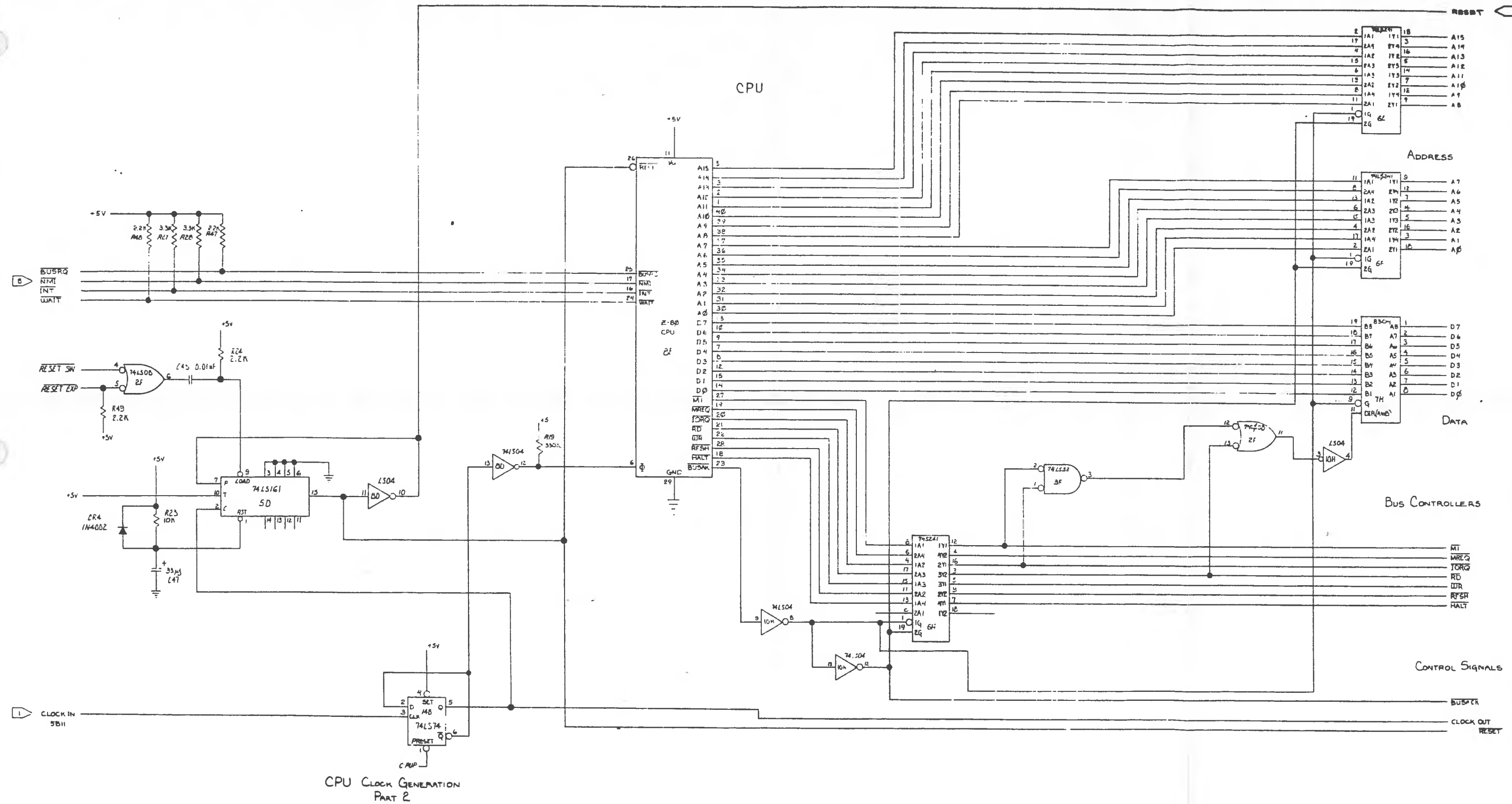
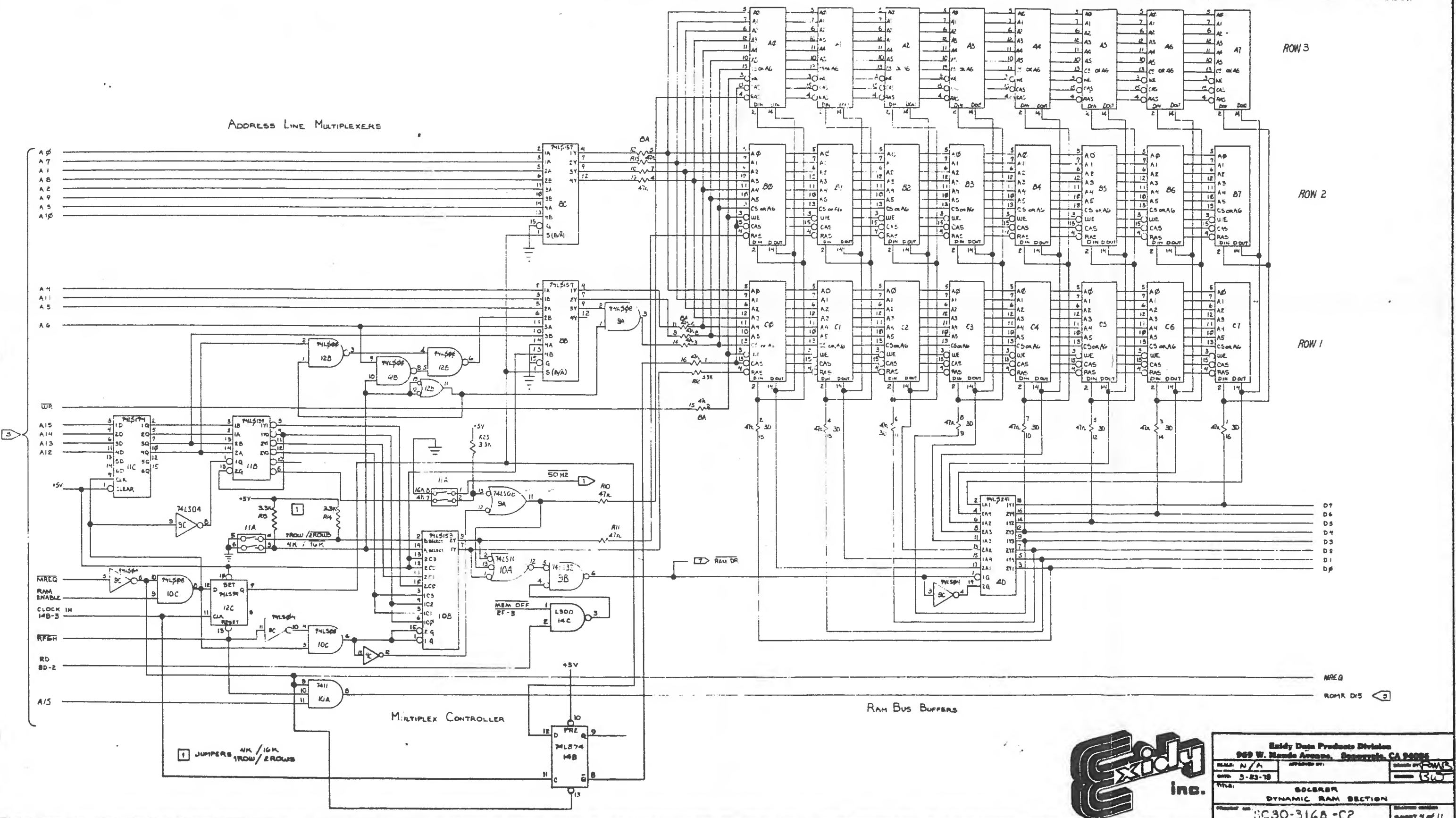


REVISIONS			
REV	DATE	DESCRIPTION	APPROVED
1	12-6-72	ENC 100072	B

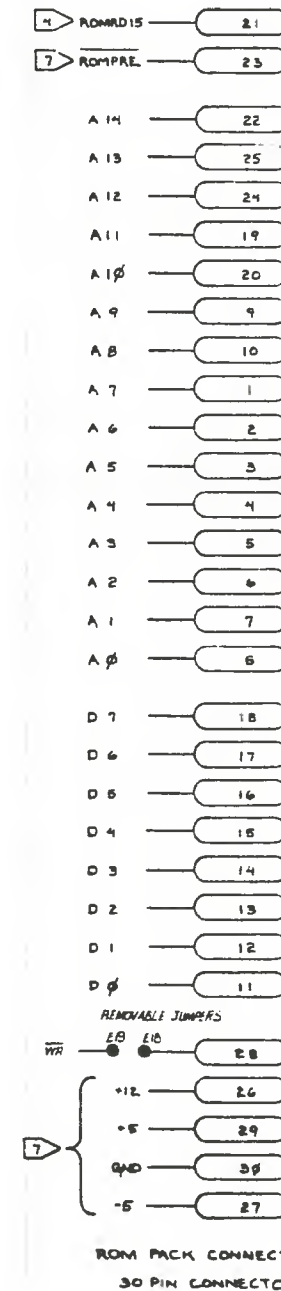
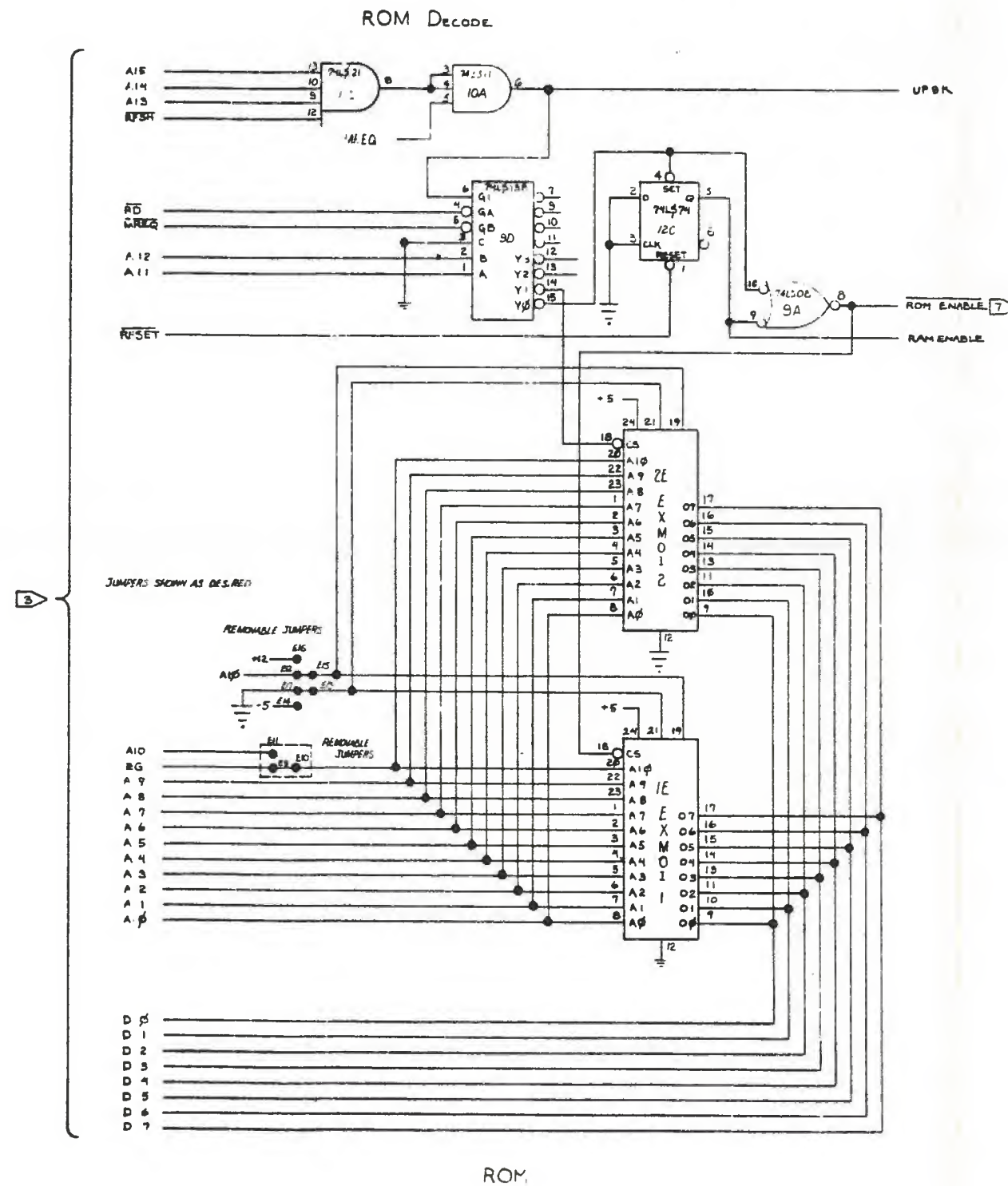


DATE: 12-6-72	APPROVED BY: B	DESIGNED BY: B
TITLE: WIZARD CENTRAL PROCESSOR UNIT		
SUBTITLE: 800 CPU		
PROJECT NO: SC30-5168-C2		SHEET 3 of 11

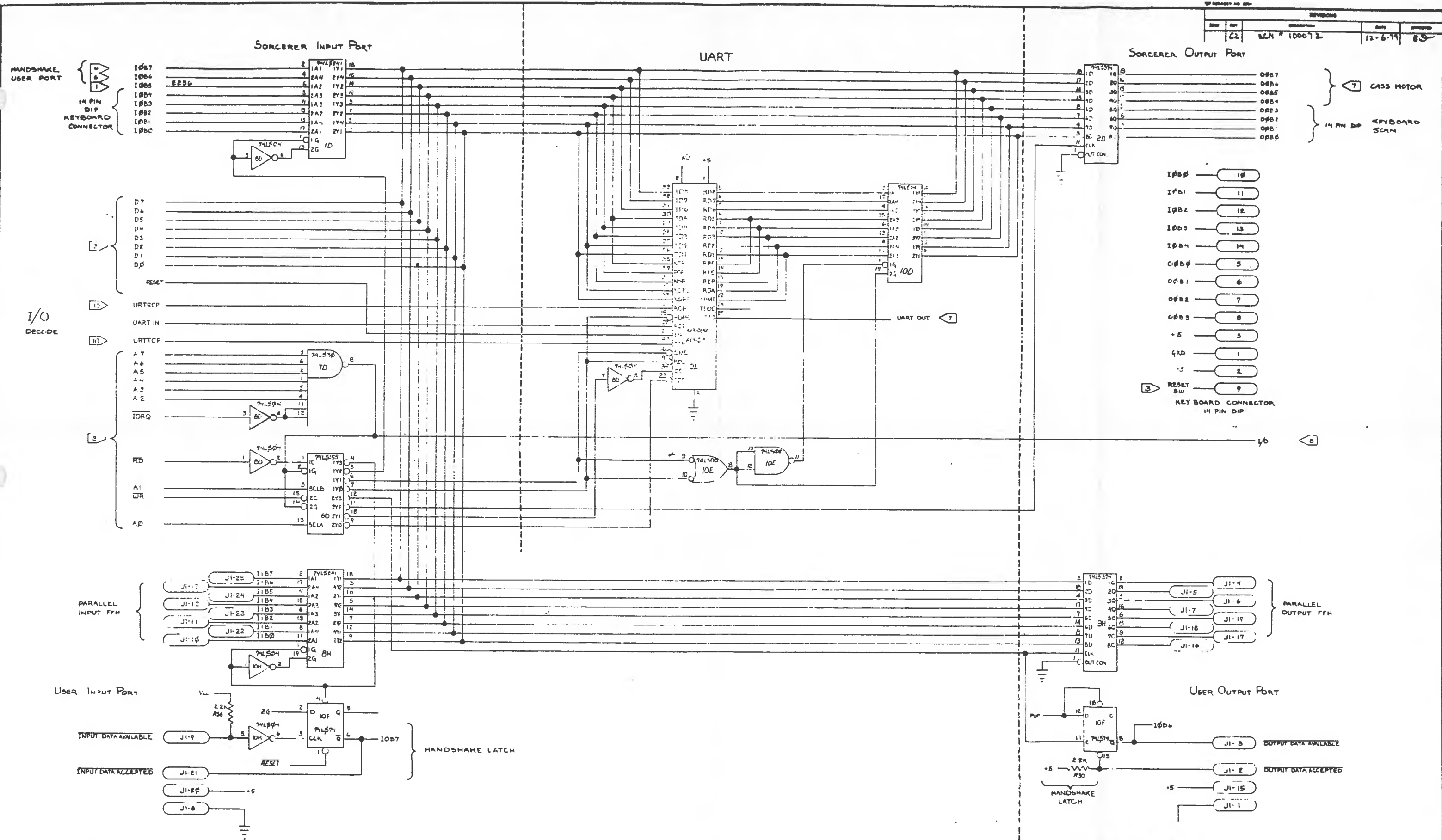
NOTES:
 (1) 2104 OR EQUIVALENT IN 4K VERSION, 2116 OR EQUIVALENT IN 16K VERSION.
 (2) EMPTY IN 4K AND 16K VERSION, 2104 OR EQUIVALENT IN 8K VERSION, 2116 OR EQUIVALENT IN 32K VERSION.



Exidy Data Products Division			
969 W. Honda Avenue, Sunnyvale, CA 94085			
DATE: 5-23-78	APPROVED BY: [Signature]	DESIGNED BY: [Signature]	CHECKED BY: [Signature]
PROJECT: 8030-3168-C2			
SHEET 4 of 11			



Exidy Data Products Division 969 W. Maude Avenue, Sunnyvale, CA 94085			
DATE: 3-23-78	APPROVED BY: [Signature]	DESIGNED BY: [Signature]	TESTED BY: [Signature]
PART: ROM SECTION			
PART NO. SC30-316B-C2			
SHEET 5 of 11			

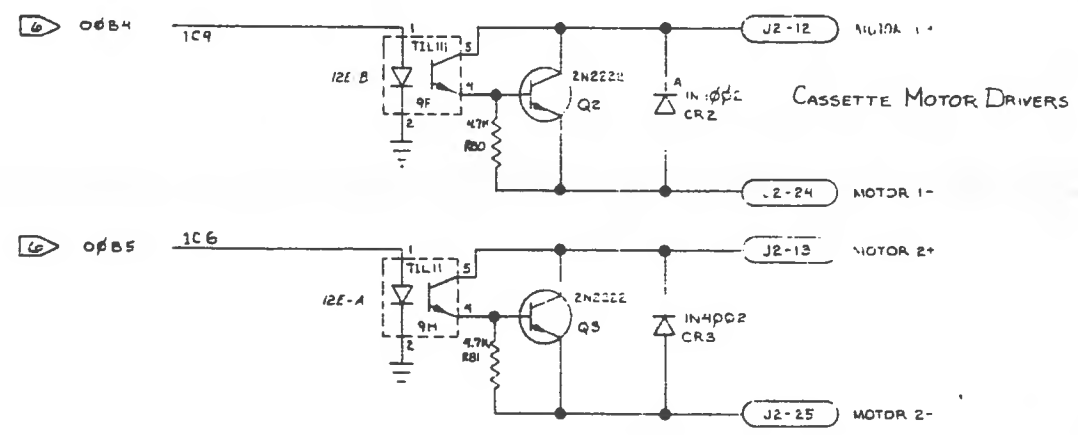
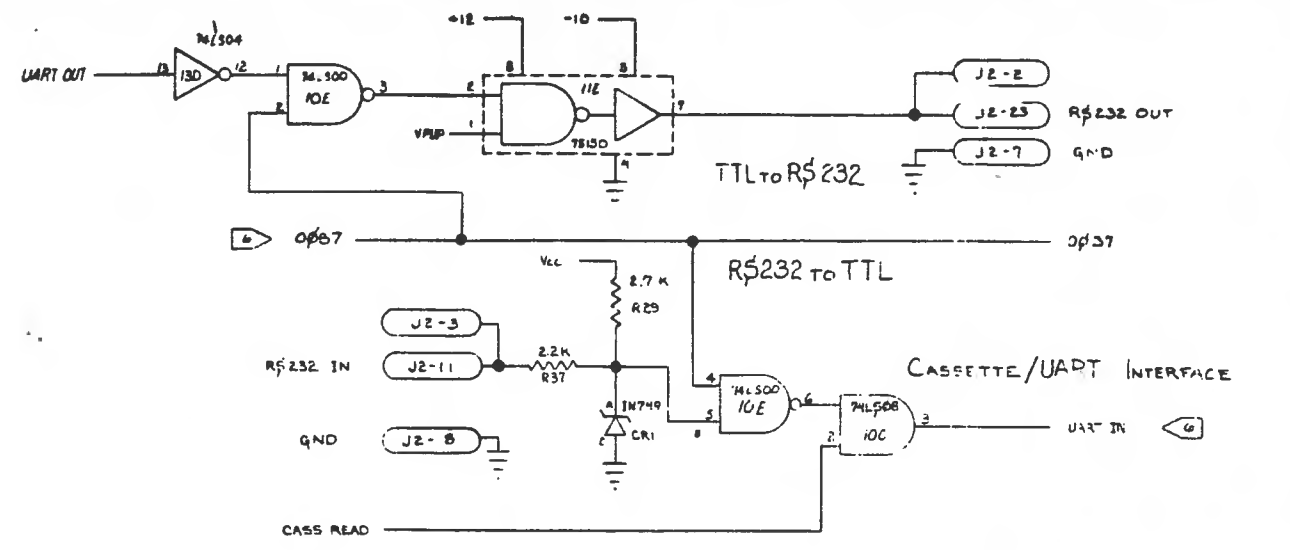


REVISIONS			
REV	DATE	DESCRIPTION	BY
1	12-6-79	100072	BS



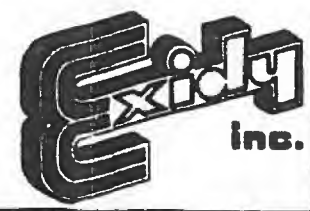
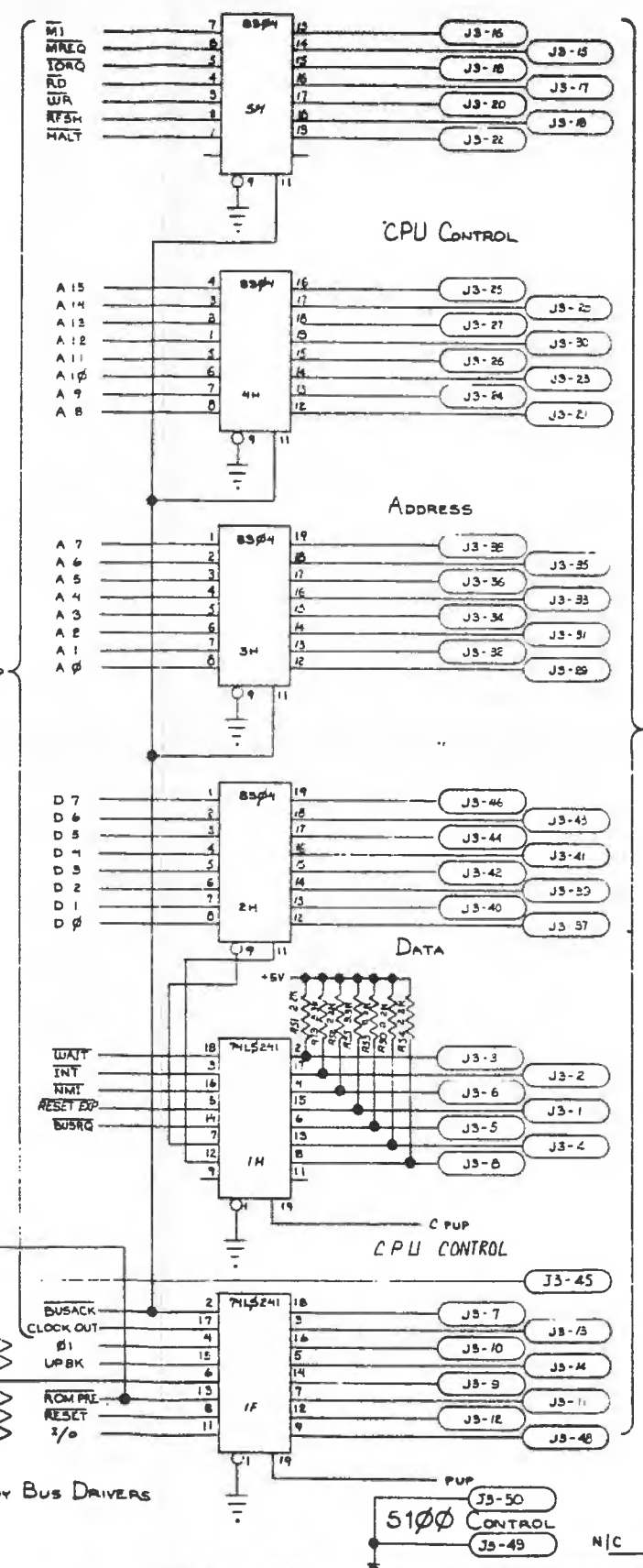
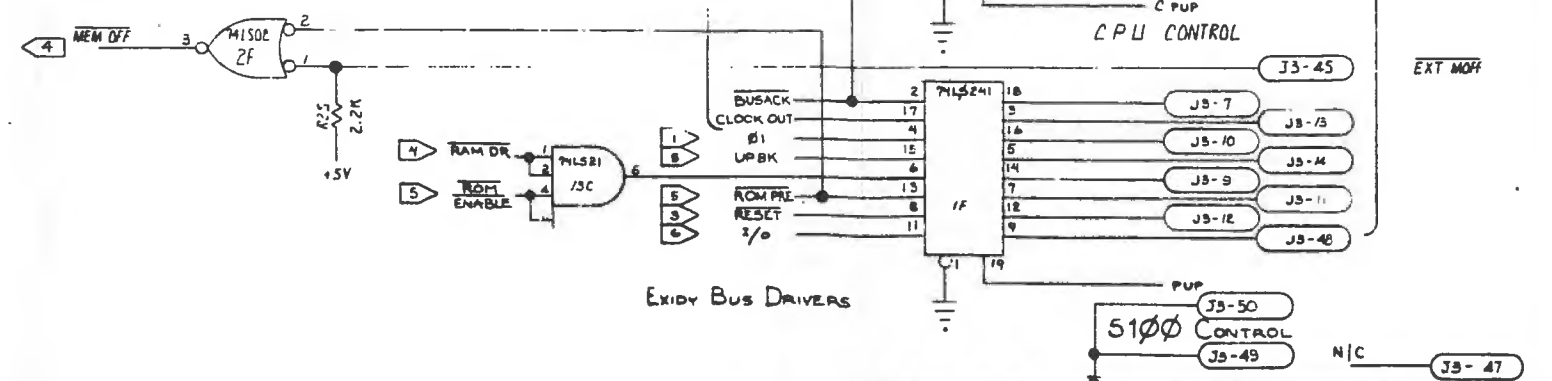
Exidy Data Products Division, 960 W. Niles Avenue, Sunnyvale, CA 94086			
DATE: 12/1/79	DESIGNED BY: J. W. W.	REVIEWED BY: BS	DATE: 12-23-79
I/O 1 UART			
PART NO: SC30-3168-C2		SHEET 6 of 11	

REV	DATE	DESCRIPTION	BY	CHKD
1	12-1-79	SC90-3168-02	RL	



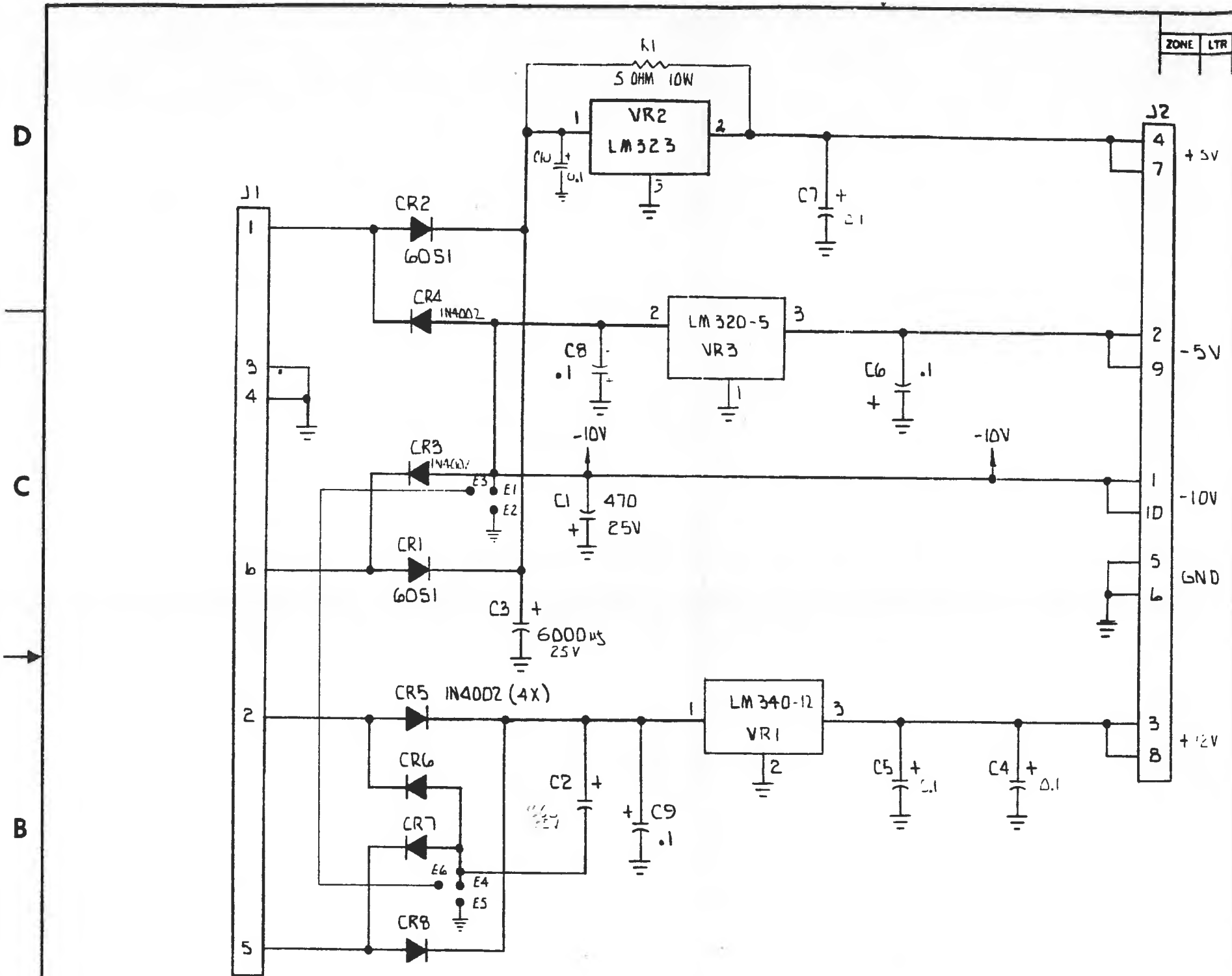
- PS-1 -10V
- PS-2 -5V
- PS-3 +12V
- PS-4 +5V
- PS-5 GND
- PS-6 GND
- PS-7 +5V
- PS-8 +12V
- PS-9 -5V
- PS-10 -10V

POWER SUPPLY CONNECTOR



Exidy Data Products Division, 969 W. Monte Avenue, Sunnyvale, CA 94086			
DATE: N/A	REVISED BY:	DESIGNED BY: RWA	
DATE: 8-21-79		REVISED: RL	
TITLE: INTERFACES			
PROJECT NO. SC90-3168-02		DRAWN BY: 7/11	

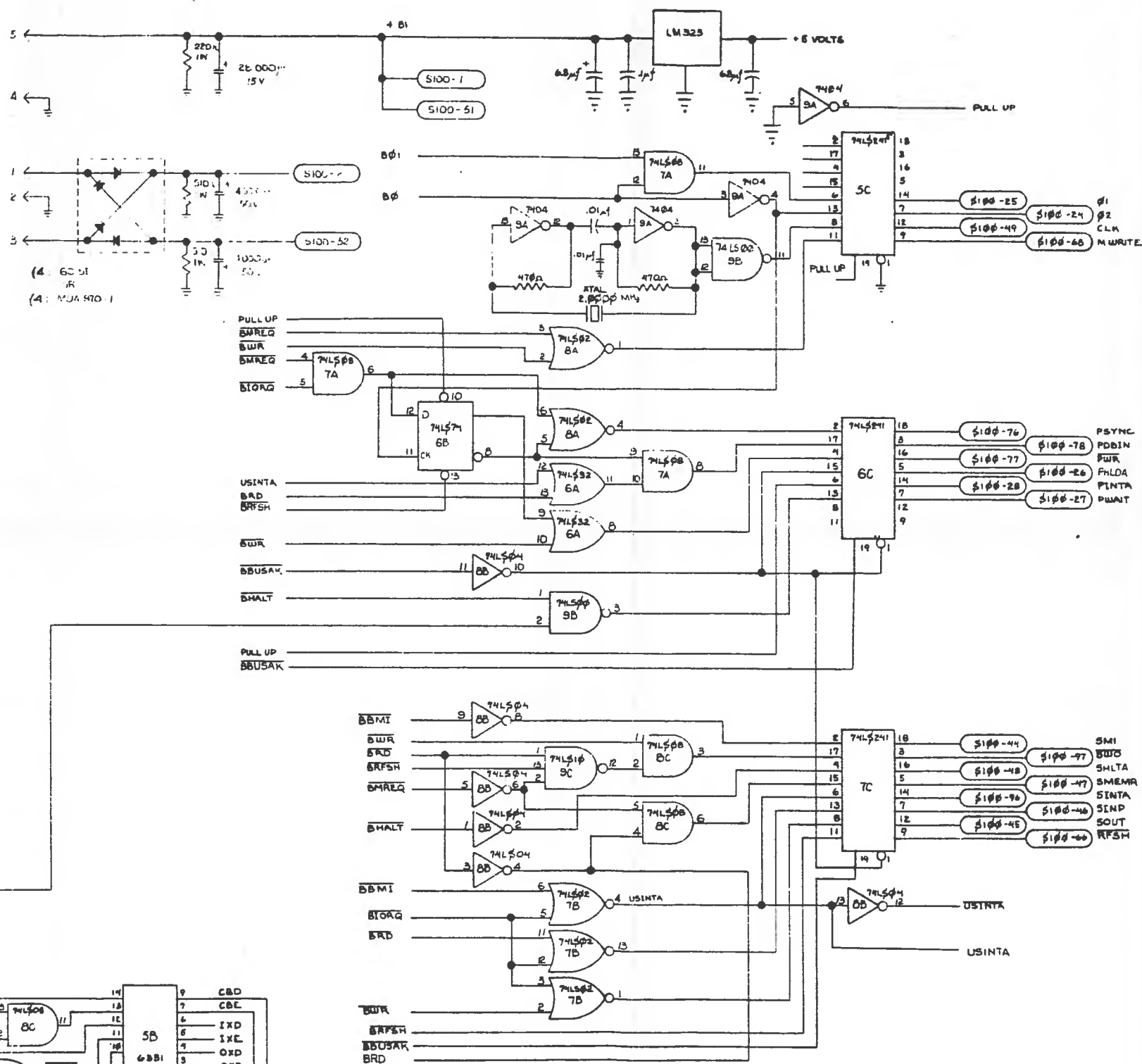
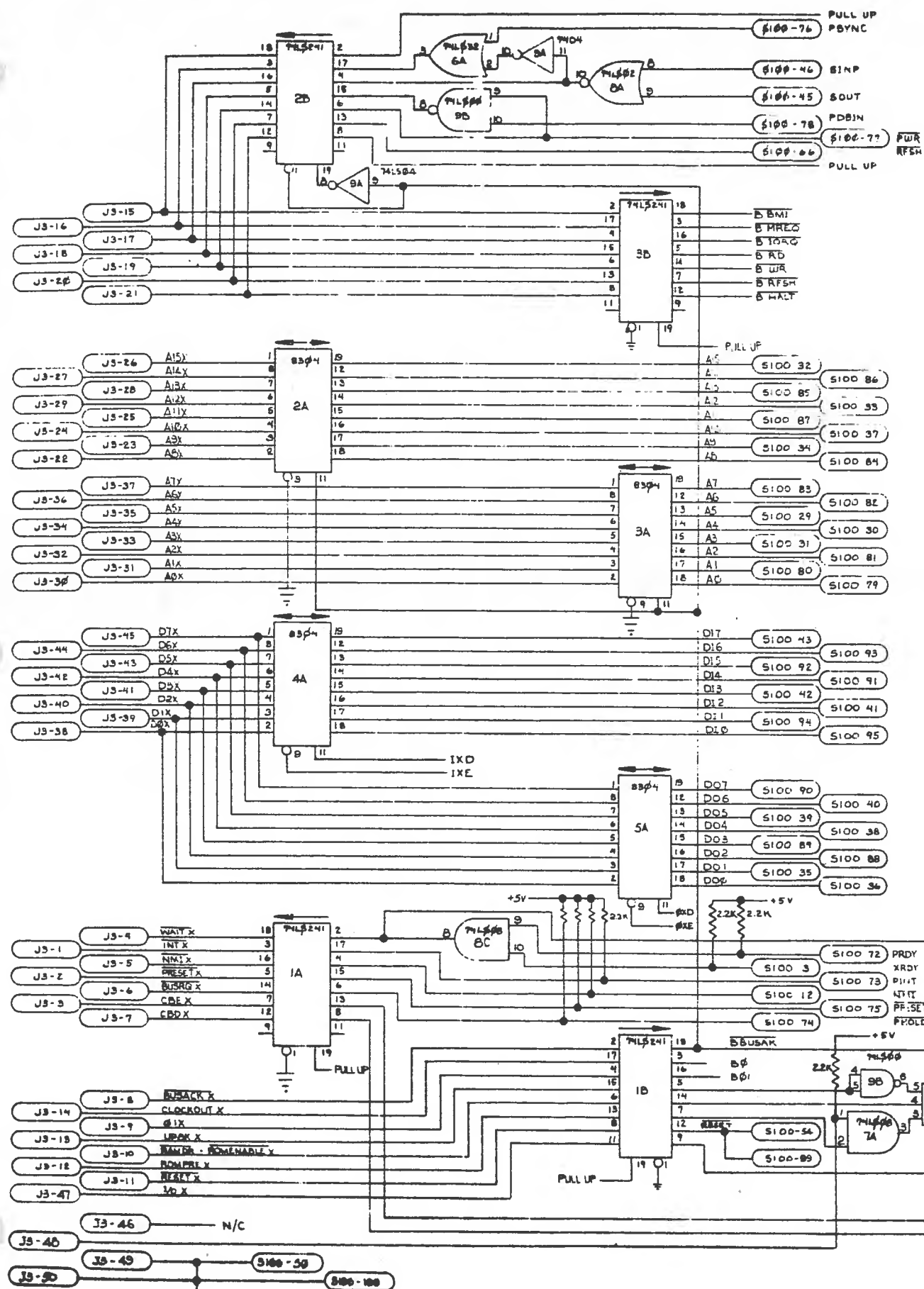
REVISIONS			
ZONE	LTR	DESCRIPTION	DATE

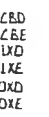
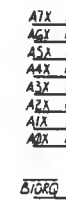


REFERENCE DESIGNATIONS		
FIRST	LAST	DELETED
C1	C10	
CR1	CR8	
R1	R1	
VR1	VR3	
J1	J2	

2. ALL CAPACITANCE ARE MICROFARADS.
1. ALL RESISTORS ARE IN OHMS.
NOTES: UNLESS OTHERWISE NOTED-

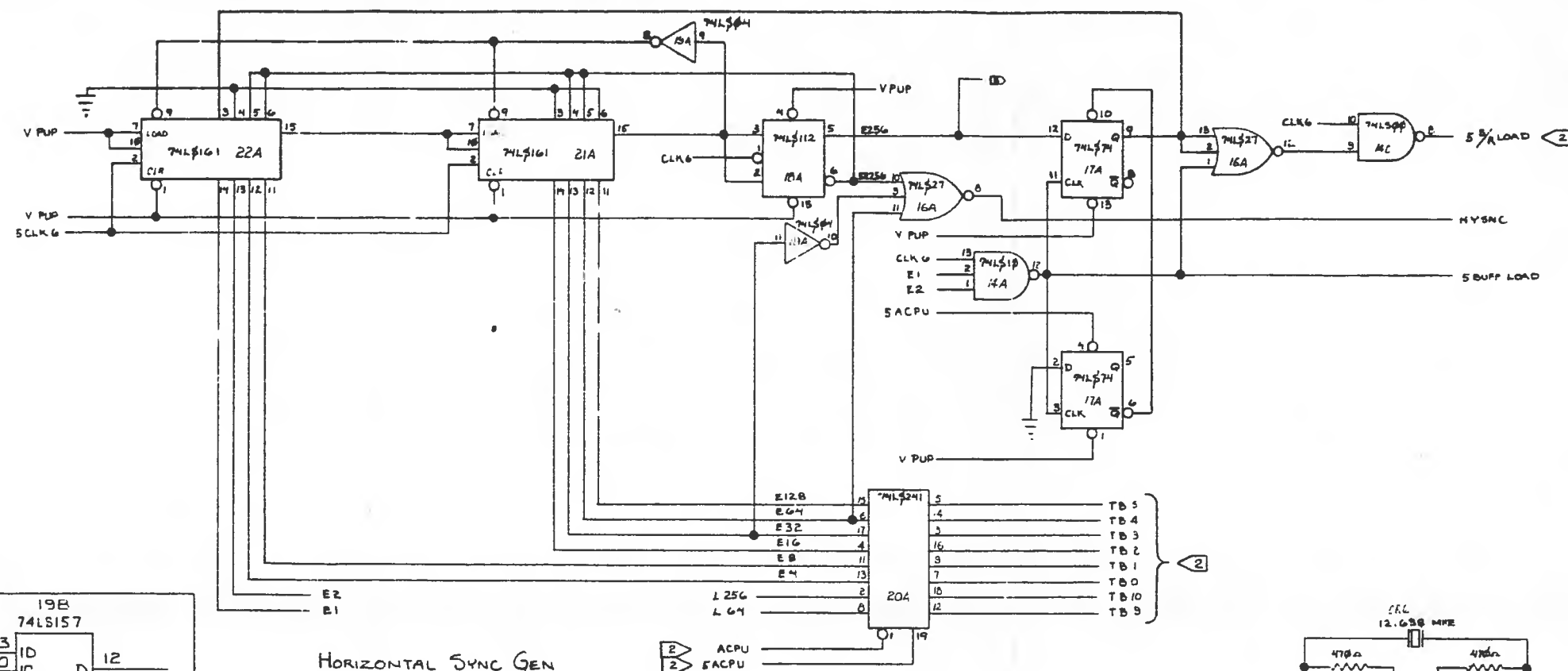
QTY REQD	CODE IDENT	PART OR IDENTIFYING NO.	NOMENCLATURE OR DESCRIPTION
PARTS LIST			
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE: FRACTIONS DECIMALS ANGLES ± .XX ± ± ± .XXX ± ±		CONTRACT NO.	EXIDY DATA PRODUCTS DIVISION 390 JAVA DRIVE, SUNNYVALE CA 94086
MATERIAL		APPROVALS DRAWN <i>B.D.</i> CHECKED	DATE 7-19-79
FINISH		SPLITTER POWER SUPPLY SCHEMATIC	
NEXT ASSY		DO NOT SCALE DRAWING	SCALE
USED ON		SIZE C	CODE IDENT NO. 2C
APPLICATION		DRAWING NO. 30-0003-B	SHEET 1 OF 1





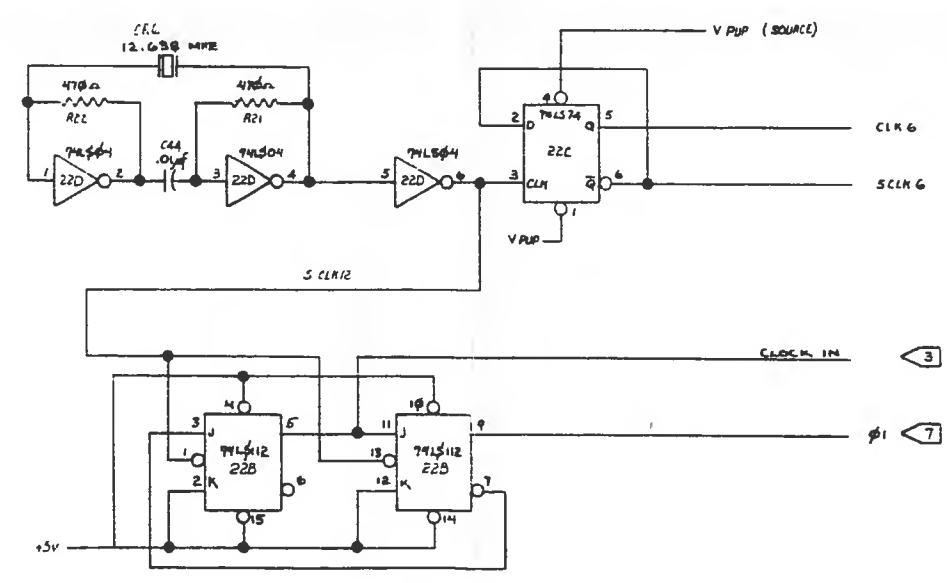
MI
ND
ULTA
MEMA
SINTA
SINP
SOUT
RESH

REVISIONS				
REV	DATE	DESCRIPTION	BY	APPROVED
K2	100072		17-6-77	✓

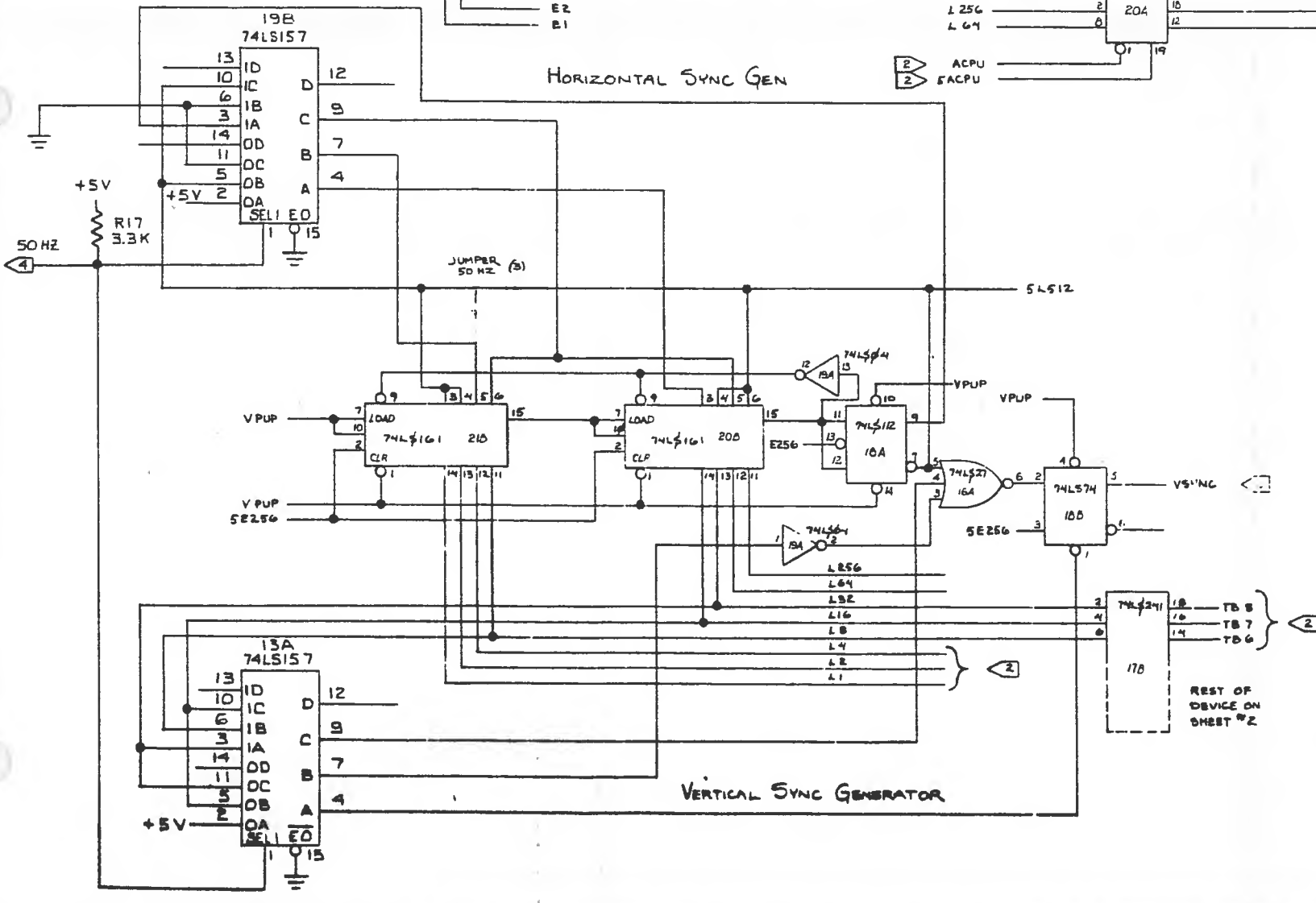
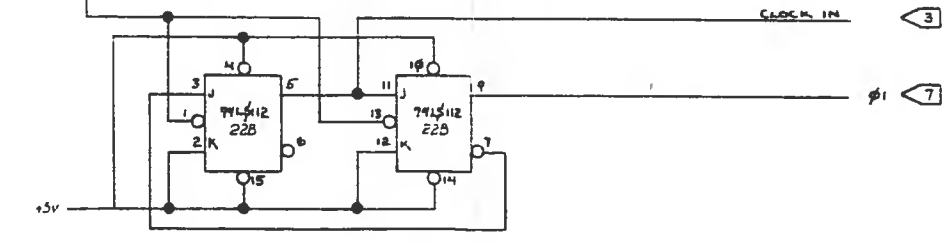


HORIZONTAL SYNC GEN

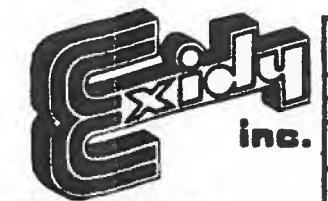
VIDEO CLOCK GENERATOR



CPU CLOCK GENERATOR PART 1



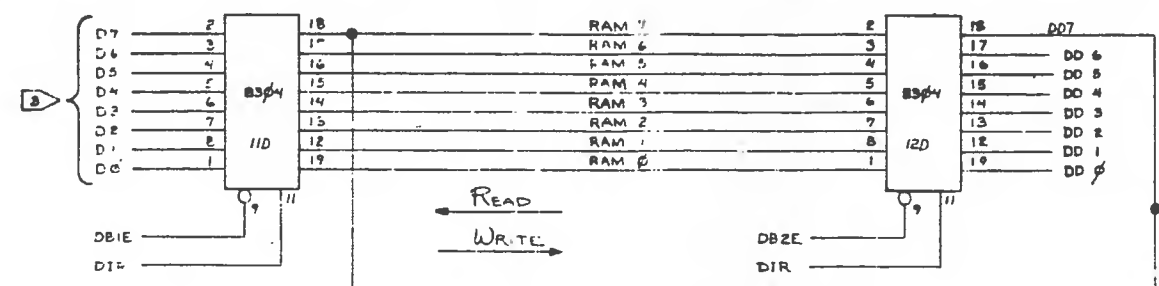
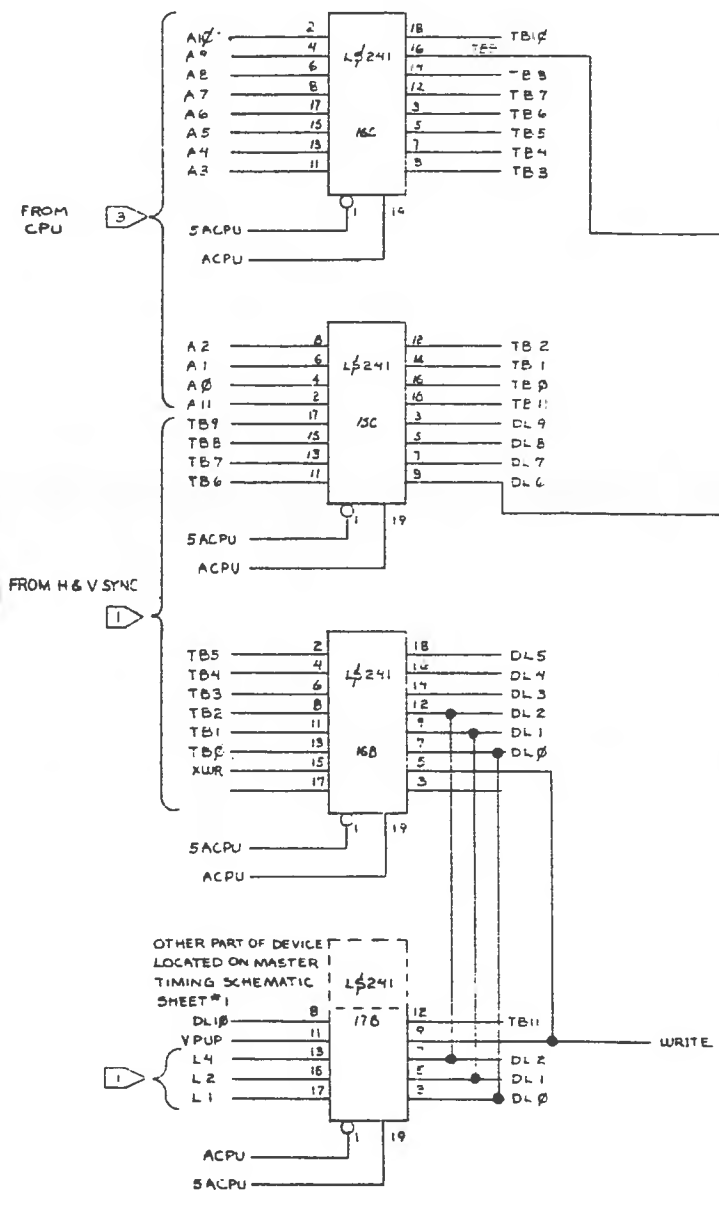
VERTICAL SYNC GENERATOR



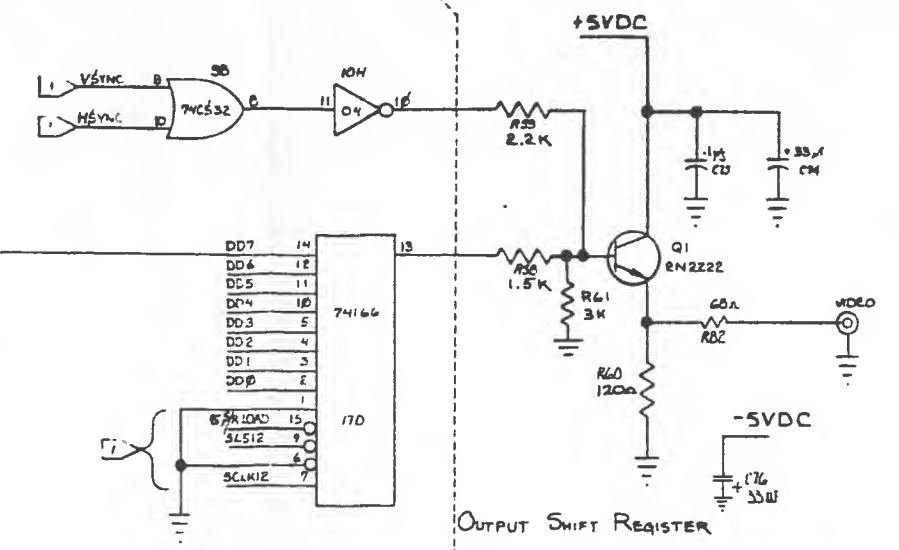
DESIGN: N/A	APPROVED BY:	DESIGNED BY: RDM
DATE: 8-3-78		REVISION: R2
TITLE: MASTER TIMING FOR EXIDY STANDARD VIDEO		
PROJECT NO: SC30-3168-C2		
SHEET 1 of 11		

REVISIONS				
REV	DATE	DESCRIPTION	BY	APPROVED
1	12-6-77	REVISED TO 100072	CL	BS

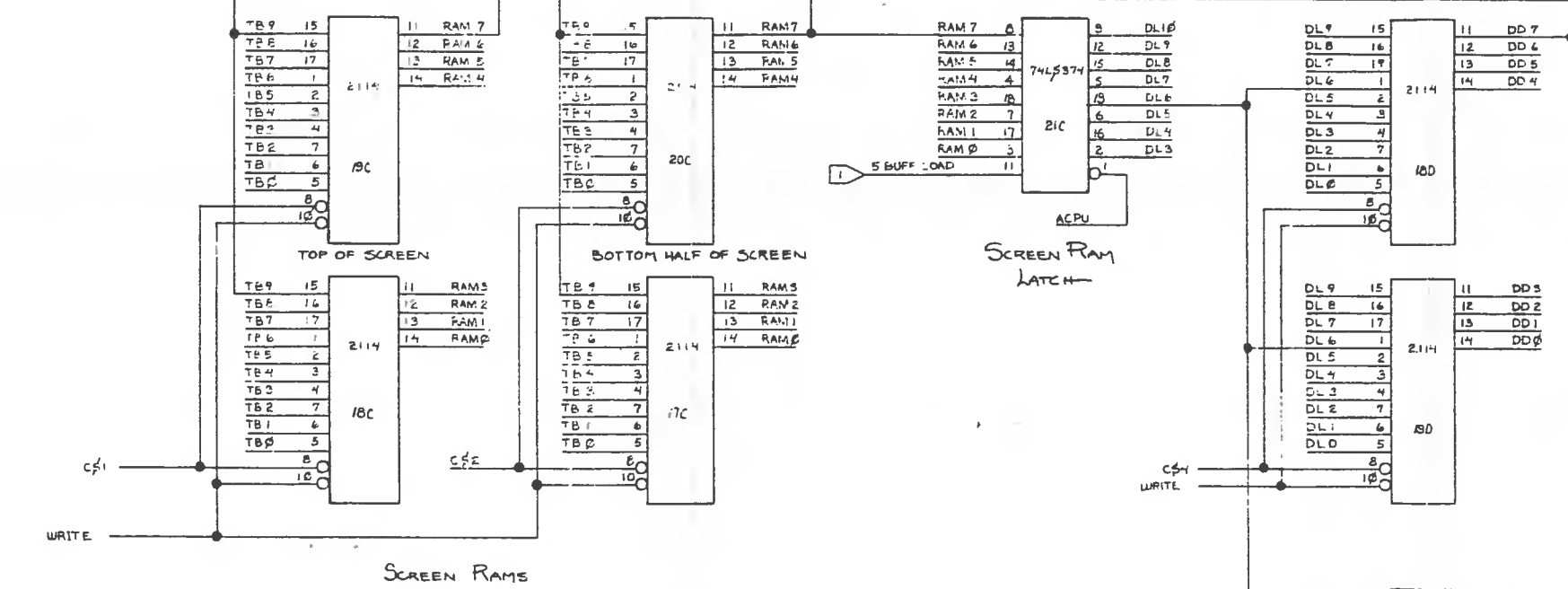
SCREEN ADDRESS MULTIPLEXING



BIDIRECTIONAL DATA BUS BUFFERS



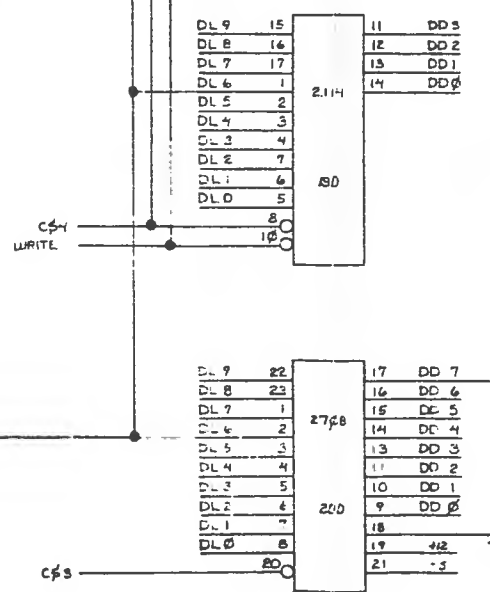
OUTPUT SHIFT REGISTER



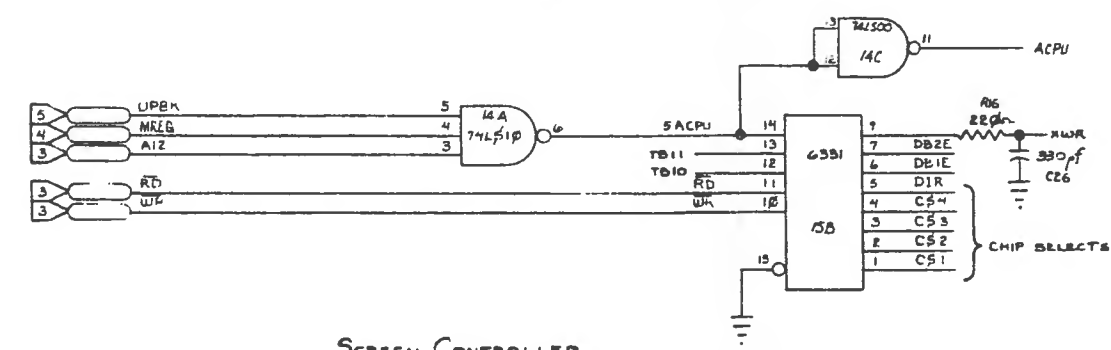
SCREEN RAMS

SCREEN RAM LATCH

USER DEFINABLE CHARACTER GENERATION



CHARACTER GENERATION



SCREEN CONTROLLER



DATE: 12-6-77	REVISED BY: CL	DESIGNED BY: BS
DATE: 12-6-77	REVISED BY: CL	DESIGNED BY: BS
30 LINES 64 CHARACTERS ADDRESS 0000 > 0FFF		
SC90-3168-C2		
SHEET 8 of 11		

