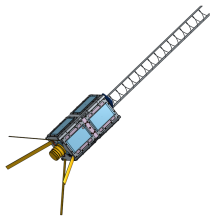


# OreSat Block Diagram

Oregon's First satellite  
www.oresat.org  
V5.0 2020/04/21



**M0** = ST STM32F046K6 48 MHz 32 bit ARM Cortex-M0 running ChibiOS  
**L4** = ST STM32L452RE 80 MHz 32 bit ARM Cortex-M4F running ChibiOS  
**M4** = ST STM32F446 168 MHz 32 bit ARM Cortex-M4F running ChibiOS  
**A8** = Octavo OSD335x-SM 1 GHz 32 bit ARM Cortex-A8 running Linux  
**OPD** = OreSat Power Domain (power bus + I2C card power control)  
— = Subsystem — = PCB — = Power — = Comm. — = RF