

Graphic Type

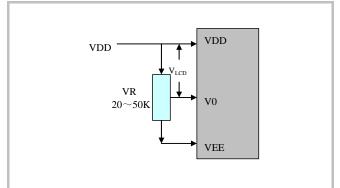
Feature:

- 1 \ 128x64 dot-matrix
- 2. STN/Transmissive/Negative/Blue
- 3. Backlight: White /side light
- 4. Operating Temp.: -10 $^{\circ}\text{C} \sim$ +60 $^{\circ}\text{C}$
- 5、1/64 duty cycle, 1/9 Bias
- 6. Built-in Controller (SBN0064G or equivalent)
- 7. Viewing angle: 6 o'clock

Absolute Maximum Rating:

Item	Symbol	St	Unit		
		M_{IN}	T _{YP}	M_{AX}	Omt
Power supply for logic	V_{DD} - V_{SS}	-0.3		7.0	V
Input voltage	$V_{\rm I}$	-0.3		V _{DD} +0.3	V

Adjusting Display Contrast:



Note: Adjust V0 to VSS as an initial setting. When the module is operational, readjust V0 for optimal display appearance.

Electrical Characteristic: (VSS=0V, Ta = 25°C)

Parameter	Symbol	Condition	M _{IN}	T _{YP}	M _{AX}	Unit
Supply voltage for logic	V_{DD}		4.8	5.0	5.2	v
Supply current for logic	I _{DD}			2.5		mA
	$ m V_{LCD}$	-10℃				V
Operating voltage for LCD		+25℃		9.0		v
		+60℃				V
Supply voltage for Backlight	V_{BL}			5.0		v
Supply current for Backlight	I_{BL}			90		mA

Interface Pin Connections:

Pin No.	Symbol	Level	Description	
1	VSS	0V	Ground.	
2	VDD	+5.0V	Supply voltage for logic operating.	
3	V0		Adjusting voltage for LCD driving (variable).	
4	RS	H/L	In parallel bus mode register select 1: Data Register, 0: Instruction Register, Busy flag-Address Counter.	
5	R/W	H/L	In parallel bus mode Read write control 0: write 1: read	
6	E	H/L	In parallel bus mode Enable trigger	
7~14	DB0~DB7	H/L	8-bit bi-directional data bus.	
15	CS1	H/L	Chip Selection When CS1=1, Left area is Selection	
16	CS2	H/L	Chip Selection When CS2=1, Right area is Selection	
17	RET	H/L	System reset low active	
18	VEE		DC/DC voltage converter output.	
19	LED+	5.0V	Power supply for Backlight	
20	LED-	0V	The backlight ground.	