

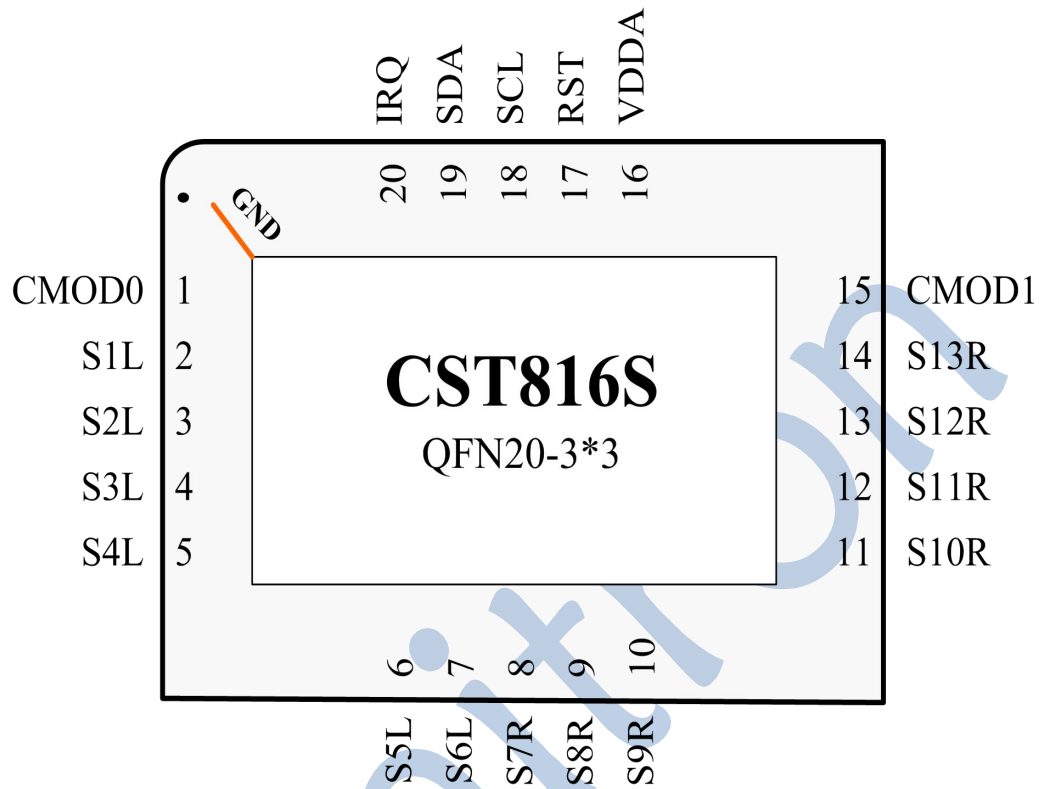


## 1 Overview

## 2. Chip Features

Hynitron

### 3. Pinout/Description

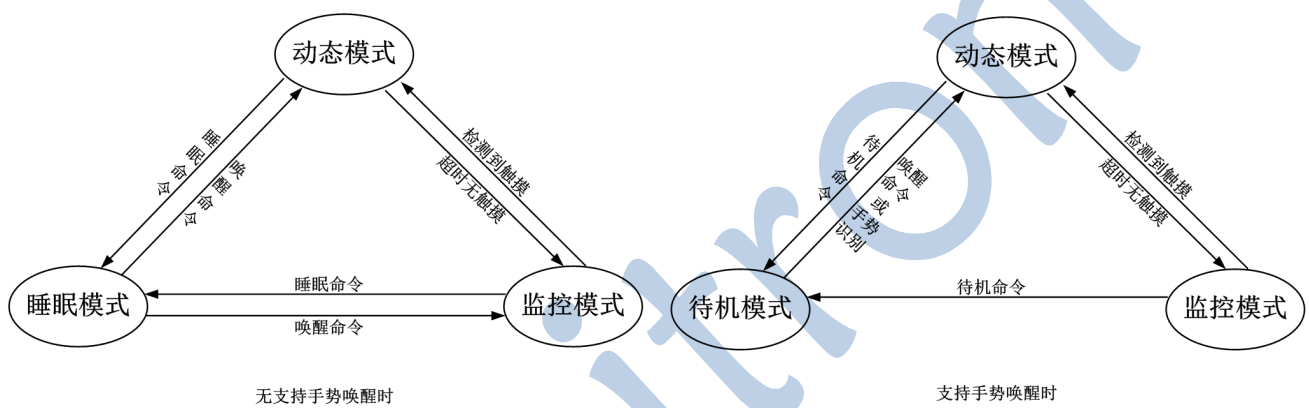


name	Description	Remarks

Remarks:

#### 4. Function description

##### 4.1 Working mode



##### 4.2 Channel/Node Configuration

#### 4.3 Power-on/Reset

#### 4.5 Low power mode

#### 4.6 I2C communication

#### 4.7 Interrupt mode

#### 4.8 Parameter setting

## **5. Application design specifications**

### **5.1 Power decoupling capacitor**

### **5.2 COMD filter capacitor**

### **5.3 Waterproof precautions**

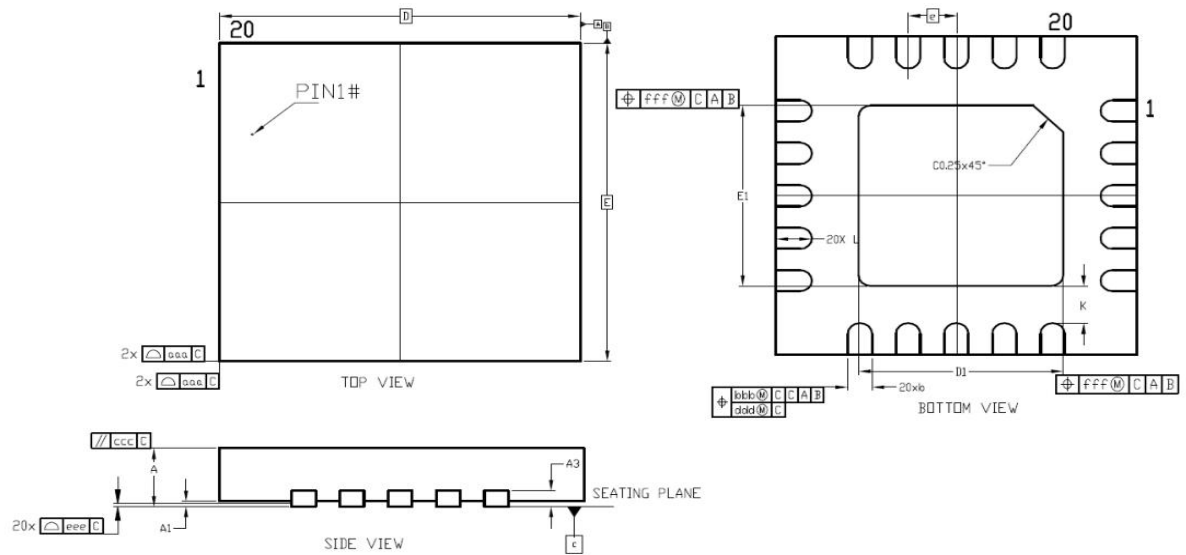
### **5.4 ESD precautions**

### **5.5 Notes on electromagnetic interference**

### **5.6 Ground wire**

[illegible]

## 7. Product packaging



QFN20 外形图

DIM SYMBOL	MIN.	NOM.	MAX.
A	0.50	0.55	0.60
A1	0	0.02	0.05
A3	—	0.152 REF	—
b	0.15	0.20	0.25
D	3.00BSC		
E	3.00BSC		
D2	1.60	1.70	1.80
E2	1.60	1.70	1.80
e	0.40BSC		
L	0.25	0.30	0.35
K	0.20	—	—
aaa	0.10		
bbb	0.07		
ccc	0.10		
ddd	0.05		
eee	0.08		
fff	0.10		

QFN20 外形尺寸



## 8. Reference circuit

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