



UNIVERSITY OF  
CAMBRIDGE

Department of Computer  
Science and Technology

Research project report title page

Candidate 1234N

*“Using effect handlers for efficient parallel scheduling”*

Submitted in partial fulfillment of the requirements for the  
Master of Philosophy in Advanced Computer Science

Total page count: 68

Main chapters (excluding front-matter, references and appendix): 47 pages (pp 7–53)

Main chapters word count: 467

Methodology used to generate that word count:

[For example:

```
$ make wordcount
gs -q -dSAFER -sDEVICE=txtwrite -o - \
    -dFirstPage=6 -dLastPage=11 report-submission.pdf | \
egrep '[A-Za-z]{3}' | wc -w
467
```

]

# Abstract

Write a summary of the whole thing. Make sure it fits on one page.

# Contents

<b>1</b>	<b>Introduction</b>	<b>7</b>
1.1	Thesis . . . . .	7
1.1.1	Parallelism . . . . .	8
1.1.2	Concurrency . . . . .	9
1.2	Tools . . . . .	10
1.2.1	System . . . . .	10
1.2.2	OCaml Multicore . . . . .	10
1.3	Findings . . . . .	11
<b>2</b>	<b>Background</b>	<b>12</b>
2.1	Scheduling . . . . .	12
2.1.1	System Threads . . . . .	12
2.1.2	Lightweight Threads . . . . .	13
2.1.3	Preemption . . . . .	13
2.1.4	Work stealing . . . . .	14
2.2	Multiprocessing . . . . .	15
2.2.1	Amdahl’s law . . . . .	15
2.2.2	Lock-free algorithms . . . . .	16
2.2.3	Atomic operations . . . . .	16
2.2.4	Linearizability . . . . .	17
2.2.5	DSCheck . . . . .	17
<b>3</b>	<b>Related work</b>	<b>19</b>
3.1	Go . . . . .	19
3.2	Tokio (Rust) . . . . .	20
3.3	OCaml . . . . .	21
3.3.1	Threading . . . . .	21
3.3.2	Effects . . . . .	21
3.3.3	Garbage collection . . . . .	22
3.3.4	Domainslib . . . . .	22
<b>4</b>	<b>Design</b>	<b>23</b>

4.1	Objectives . . . . .	23
4.2	Benchmarks . . . . .	24
4.2.1	Packet processing . . . . .	24
4.2.2	Audio mixer . . . . .	24
4.2.3	Binomial option pricing . . . . .	25
4.3	Ordering . . . . .	25
4.3.1	FIFO and LIFO . . . . .	26
4.3.2	Hybrid . . . . .	28
4.4	Contention and locality . . . . .	28
4.4.1	Core structure distribution . . . . .	28
4.4.2	Staged . . . . .	29
4.5	Other design choices . . . . .	30
4.5.1	Work distribution . . . . .	30
4.5.2	Overflow queue . . . . .	31
4.5.3	Resizing . . . . .	31
4.5.4	Yield . . . . .	31
4.5.5	Promise . . . . .	32
<b>5</b>	<b>Implementations and evaluation</b>	<b>34</b>
5.1	Schedulers . . . . .	34
5.1.1	Design principles . . . . .	35
5.2	Methods . . . . .	36
5.2.1	System . . . . .	36
5.2.2	Procedures . . . . .	36
5.2.3	Statistical methods . . . . .	36
5.3	Server benchmark . . . . .	38
5.3.1	General case . . . . .	38
5.3.2	Server benchmark with cache effects . . . . .	44
5.3.3	Server benchmark with multi-modal processing latency . . . . .	45
5.4	Mixer benchmark . . . . .	46
5.5	Binomial option pricer benchmark . . . . .	48
5.6	Comparison with Domainslib . . . . .	50
<b>6</b>	<b>Summary and conclusions</b>	<b>52</b>
<b>A</b>	<b>Rejected design choices</b>	<b>61</b>
A.1	Backpressure . . . . .	61
A.2	Cancellations . . . . .	62
A.3	Blocking operations . . . . .	62
A.4	Dynamic pool size . . . . .	62
A.5	Batching . . . . .	62

<b>B</b>	<b>Core data structures</b>	<b>63</b>
B.1	MPMC queue on infinite array . . . . .	63
B.2	MPMC queue blocking . . . . .	64
B.3	MPMC queue non-blocking dequeue . . . . .	64
B.4	MPMC queue non-blocking enqueue . . . . .	65
B.5	Work stealing queue . . . . .	65
B.6	Work stealing stack . . . . .	66
B.7	Resizing . . . . .	67
B.8	Sharded queue . . . . .	67
B.9	False sharing . . . . .	68
B.10	Validation . . . . .	68
B.11	Limitations . . . . .	68

# Chapter 1

## Introduction

This section introduces and motivates the central problem of this dissertation in 1.1, and provides an overview of the findings in 1.3.

### 1.1 Thesis

20 years ago computer processors looked very different than today. The first stock, non-embedded multicore processor has just been released [5] but the customers would have to wait until 2004 to see the first multicore x86 CPU - dual core AMD Opteron [2]. Up to four of them could have been stacked inside HP ProLiant DL585 server. At the time, a revolutionary technology, which allowed continuation of the exponential increase of computational power in chips [28] in the light of the slowdown of Dennard scaling [18]. This technological direction is still relevant today and remains a large driver of performance improvements. Today, a standard dual-socket machine equipped with two stock processors (AMD EPYC 7702 [1]) offers 256 logical cores. In the meantime, the maximum clock speed between these two processors improved by only 20% (respectively 2.8 GHz and 3.35 GHz for Opteron and Threadripper). Thus, software engineers have strong reasons to try to parallelize workloads.

Practice shows that parallel programming is far from trivial. Mistakes are common in the daily life of programmers and in research, in algorithms oftentimes claimed to be proven [51]. This inability to take advantage of parallelism is suggested to have a real impact on the productivity of IT-using sectors of economy [40]. There is a number of research spaces trying to make parallel programming easier. One particularly important for modern operating systems and runtimes is scheduling.

A common definition of scheduling explains it in terms of allocating resources to tasks. Justifiably. It captures the essence of the term in a concise and interdisciplinary manner. However, in the context of systems, perhaps, a more illuminating way to think about scheduling is about bridging hardware parallelism and software concurrency. Not long

ago, computer could get by with far less advanced schedulers as hardware offered no parallelism. Similarly, workloads used to be less concurrent. With less networking, worse interfaces, fewer programs running on a single computer, more acute cost of context switching, interactivity and fairness were a far smaller concern than they are today.

This is no longer the case with processors scaling mostly horizontally and evermore complex applications. Thus, this dissertation argues the following thesis.

Modern hardware is so parallel and workloads are so concurrent, that there is no single, perfect scheduling strategy across a complex application software stack. Therefore, there are significant performance advantages to be gained from customizing and composing schedulers.

The first part of the statement is akin to construction of No Free Lunch theorem [33] for schedulers. While there is a growing body of NFL theorems for optimization problems (e.g. [71], [35]), such fundamental results typically formalize knowledge, rather than produce direct guidance for real-world engineering. Therefore, this dissertation aims for a different exposition of the problem, one that analyses practical differences between scheduling policies. It considers widely-used performance metrics and explores the trade-offs between pragmatic scheduling schemes, guided by benchmarks executed on stock hardware.

The following sections strengthen the thesis from the point of view of hardware parallelism (1.1.1), software concurrency (1.1.2) and the opportunity presented by OCaml Multicore (1.2.2).

### 1.1.1 Parallelism

Moore’s law is an observation stating that number of transistors in integrated circuits tends to double every two years [50]. Around the turn of the millennium, increasing single-core performance at exponential rate was no longer possible due to physical limits. Thus, manufacturers turned to stacking multiple processing units within a single chip [30]. As shown by figure 1.1, the exponential growth of transistors continued, together with exponential increase in cores. While some researches conjecture this trend to slow down [27], [28], we still see rapid, if not exponential, growth in this area.

Hence, parallelism is here to stay. Naturally, in contrast with clock frequency increases, schedulers have to be carefully crafted in order to take full advantage of horizontal scaling of the underlying architecture. That’s because:

- Designs need to evolve as synchronization primitives like locks or atomics do not scale endlessly and e.g. a naive work stealing scheduler may have been good enough on 16-thread Intel Xeon in 2012 but fail to utilize all 128 threads of a contemporary AMD ThreadRipper [20].





Figure 1.1: 50 years of evolution of processors. The single-core performance sees diminishing returns but during the last 15 years CPU manufacturers turned to multicore architectures. Figure generated using [55].

- Modern high-core architectures feature non-uniform . Thus, memory latency patterns vary with the topology and scheduling decisions may benefit from taking memory hierarchy into account (e.g. [26], [29]). Moreover, the non-uniformity appears also in consumer products such as Apple M1 or Intel Core i7-1280P. These feature two sets of cores: one optimized for performance and another one for efficiency.

### 1.1.2 Concurrency

Workloads become more concurrent and there is a number of reasons behind that. High concurrency is a necessary condition to take advantage of the available parallelism. Compute-heavy application have strong incentives to try to distribute work across multiple cores in the world of stalled single-clock speeds.

Secondly, the complexity of underlying workload increases as computers become more ubiquitous. There is more useful programs available and the act of composing their executions (to run together) requires concurrency. The programs alone become more complicated. Data-intensive applications [42] require more concurrency as networked portions of the code benefit from scheduling guarantees.

Finally, concurrency is a convenient tool for expressing certain workloads. Legacy Windows GUI applications are organized around an event loop, which processes inputs from the user. Programmer has to carefully judge the handler logic to never starve the loop and cause the UI to freeze. It turns out very difficult in practise; a typically small IO may

be pointed at a network location and take orders of magnitude longer. A simple compute might stall for far longer on an overloaded system. A modern Go application can simply spawn a new goroutine for every handler and rest assured that runtime prevents starvation of the event loop.

## 1.2 Tools

This sections describes the system and programming environment used to verify the thesis.

### 1.2.1 System

The report aims to investigate runtime scheduling space on a modern, high-core machine. Used system features two AMD EPYC 7702 processors, which gives a total of 128 physical cores and 256 threads. It runs Ubuntu 22.04. For more details, see 5.2.1.

### 1.2.2 OCaml Multicore

#### Scheduler as a library, scheduler composable

OCaml is a unique functional programming language, which aims to deliver performance and safety. It found many research and industrial users despite a significant shortcoming for a performance-oriented language - lack of support for multithreading. This confined real-world applications to monadic concurrency libraries such as Async and Lwt, which are troublesome to use:

- **No runtime support.** Lack of runtime support hurts the development in a number of ways, e.g.: there is no way to efficiently switch between tasks, tracing becomes harder as there is no real notion of separate threads and stacks.
- **Blocking.** Combine single-core with lack of preemption and any single CPU-intensive or blocking call is prone to cause chaos by starving other threads: cause network timeouts, delay timers, etc. On the surface, there is no qualitative difference with multicore as any  $n$  non-preemptive threads can be blocked by  $n$  misbehaving task. But in practise multicore is far safer. Firstly, developer may isolate low-latency tasks on a different pool, allowing for more relaxed programming elsewhere. Secondly, a multicore runtime may feature a guardian thread, which spawns new OS threads whenever needed [7].

Nonetheless, after years-long effort, multicore support has been recently merged into the main tree. Thus it is a favourable time to take a closer look at lightweight threads schedulers. Significant changes to existing schedulers are notoriously slow and troublesome, as even subtle differences to scheduling policy may expose bugs in existing programs. During my industrial experience, the only way to deal with such a change was having developers manually verify the code and re-do the testing. Incidentally, both methods are known

to be bad at finding bugs in multiprocessing. This research project, however, is likely to contribute to the actual design and implementation of OCaml scheduler.

Finally, Multicore OCaml is a particularly interesting object of study as it includes a number of novel solutions. Common programming languages either include threading support, which is tightly coupled with the language itself, or offer no support and, thus, library-schedulers cannot offer much beyond simply running scheduled functions in some order. OCaml, on the other hand, features fibers and effects. Together, they allow writing a direct style, stack-switching scheduler as a library. Further, OCaml allows composing schedulers. A much-needed mechanism for executing diverse workloads with portions having different optimization criteria [68].

## 1.3 Findings

fill in

# Chapter 2

## Background

This section begins by describing the general problem of scheduling, key concepts and two modern real-world runtime schedulers (2.1). Afterwards, it describes the tools of choice: important properties of OCaml programming environment (3.3) and details of fine-grained multicore programming (2.2).

### 2.1 Scheduling

Scheduling attempts to assign resources to tasks in a way that optimizes some metrics [52]. The problem itself is not limited to computer science and arises in a number of areas, e.g. organization management. In the case of computer science, it stems from a very fundamental need to compose executions of multiple programs. However, supporting such a model of execution is not trivial. Instructions of multiple programs are inherently impractical to compose, since they all write and read the same registers. In effect, composition is achieved by interleaving executions of multiple programs, and a special mechanism to hide existence of these pauses by saving and restoring state. This leaves us with a clear-cut scheduling problem as there is a need for a holistic decision-making process to determine, which program runs when and for how long. This decision-making perspective is illustrated by figure 2.1 and such a model of the problem will be helpful to keep in mind throughout the rest of the report.

#### 2.1.1 System Threads

System threads are a fundamental OS feature, which allows to detach the number of synchronous executions from the number of processing units. All kinds of program grew to rely on this abstraction heavily. For example, Google Chrome spawns a single process per tab [3], and depending on the logic, each such process spawns additional threads. This aggressive acquisition of resources leads to smooth operation of the web browser but puts significant pressure on the operating system, which has to ensure that in the sea of



Figure 2.1: A simplified graph of fine-grained tasks, which scheduler attempts to traverse in order to terminate. Green box represents executed task, blue boxes are currently enqueued tasks. The orange boxes are tasks that are going to be scheduled once their parent execute. They are not visible to the scheduler at the moment. This graph shows how little is actually known about the tasks, and scheduler-traps in the shape of infinite-depth subgraphs, which are prone to cause starvation.

threads everything gets a fair share of the CPU time and their individual objectives are achieved, e.g. a heavy web browser and light but latency-sensitive audio driver.

### 2.1.2 Lightweight Threads

System threading provides the fundamental mechanisms to execute concurrent workloads across parallel hardware. But programmers need to be wary of designed computations as the cost of interacting with kernel is non-trivial. It may vary from mode switching to full syscall overhead. System call logic requires a number of things to happen (e.g. context switch, capability check, audit), which thwart the cost of a small task. Thus, the performance of highly concurrent workloads suffers significantly.

This is inconvenient for the developers. Spawning little concurrent tasks is the natural way to express many common server-side workloads, e.g. processing HTTP requests. Therefore, language designers started to construct lightweight threads, what stands for threading implemented entirely in the user space. They aim to be very cheap to use and they do deliver on that promise. Context switch in Go requires saving and restoring just 3 registers [8]. In OCaml only 2 [58]. Conversely, a system scheduler needs to save and restore *all* registers, and perform mode switching [8]. Therefore, lightweight threads are naturally better suited to support fine-grained concurrency. The next section touches on a disadvantage of not having access to privileged mode 2.1.3.

### 2.1.3 Preemption

Preemption is the ability to suspend other tasks without any cooperation on their side. It is a cornerstone of modern OS-level scheduling, in which each program is assigned its quantum of CPU time. Once the quantum is used up, scheduler forcibly ensures other

programs get to run. Preemption is a necessary condition to keep the system stable in the world with imperfect and malicious programs. In practise, preemption has more profound consequences. Individual programs no longer have to worry about their composition with others. They may cooperate but ultimately, all decision-making is done by the privileged scheduler, which is able to make more holistic decisions.

Nevertheless, true preemption is not possible as a pure software solution. Operating systems rely on the hardware support for privileged mode and interrupts. After all, if preemption was not restricted, a single erroneous or malicious application could completely destabilize whole system. Alternatively, running runtime of business logic within the kernel is not acceptable as such application threatens entire infrastructure if compromised. This makes true preemption inaccessible to lightweight threads for security reasons. Moreover, even if there was a safe mechanism to allow true preemption without elevating preemptor into privileged mode, such mechanism might defeat its purpose. Lightweight threads aim to be very cheap and that conflicts with using full OS-level context-switching mechanisms.

Different programming languages and libraries deal differently with this limitation. Particular approaches are discussed in further sections (3.1, 3.2).

### 2.1.4 Work stealing

Work-stealing is an extremely widespread concept applicable to all kinds of multicore scheduling. It has been introduced at least 40 years ago [16] and remains used in some of the most cutting edge schedulers, e.g. Completely Fair Scheduler (the default scheduler in Linux) [9], Cilk [17], Go [7].

Work-stealing splits the global work queue (in the FIFO case) into a number of local, per-CPU runqueues. Each processor strongly prioritizes its own runqueue; namely, it will enqueue and dequeue elements from its own structure as long as it is possible. This alone is similar to a number of unrelated processors, actual distribution of work happens when the impossibility criterion is met. If enqueue is impossible (due to lack of space), owner can either resize the runqueue or transfer some of the work into a global overflow queue. If dequeue is impossible (due to lack of items), processor chooses a different processor at random and steals work from the victim, thus balancing the load. There is a number of benefits flowing from such organization.

- **Less synchronization and faster hot path.** Since local runqueues are single-producer, the enqueue operation needs little synchronization. Likewise, local dequeue operation only need to synchronize with stealers making it fast as well.
- **Low contention.** Whenever load on the system is high, processors converge to using their own queues, thus eliminating any need to compete with others for work.

- **Locality.** Children tasks are likely to execute on the same processor as their parents, what maximizes the locality and improves overall performance.

Chase-Lev deque (double-ended queue) [21] is one of the notable, classic work stealing implementations. Its design is similar to the example describe above but while steal remains FIFO, local insertions and removal follow LIFO order. It aimed to minimize required synchronisation, since local thread operates at one end of the structure and steal at the other but also, although not recognized by the authors, it improves locality of the workload 4.3.

## 2.2 Multiprocessing

Multiprocessing is a crucial technique to take advantage of the modern processors. There is a number of multicore programming paradigms, each providing different trade-off between safety and performance. While it is rarely the case in software engineering practise, schedulers are critical enough to fully prioritise the latter criterion, and choose lock-free programming.

This section introduces key multiprocessing concepts with a focus on the lock-free perspective.

### 2.2.1 Amdahl's law

As already discussed, taking advantage of parallel hardware requires extra engineering effort. The actual amount of effort varies wildly between tasks. In some cases parallelization is trivial and almost boils down to using relevant threading API, e.g. computation of five independent hashes. In fact, system might automatically parallelize a computation specified by *List.map* [4].

However, on the other hand of the spectrum lie programs, for which parallelization yields marginal or no benefits. It is enough to slightly tweak the previous example to show that. Assume computation of a chain of 5 hashes, wherein each hash depends on the output of the previous one. In such a case, the hashes themselves need to happen in a sequential manner. There might still be opportunity for parallelization but it depends strictly on implementation of the hash operation. Moreover, even if hash operation is very parallelizable, the synchronisation will need to happen before and after every hash operation in the chain (10x), rather than just at the beginning and end of the workload.

The speedup coming from parallelization is described by Amdahl's law [32].

$$Speedup = \frac{1}{1-p+\frac{p}{n}}$$

Where  $n$  is the number of processors and  $p$  portion of the program that is parallelizable. The law puts a strict bound on the gains coming from using multiple processors and

hints why adding more cores yields diminishing returns, as observed in the latter sections. Namely, doubling the number of processing units halves the time spent in parallelizable portion of the workload. Thus, gains on parallelizable portion rapidly become smaller than any non-trivial sequential part of the program.

### 2.2.2 Lock-free algorithms

Lock-freedom characterizes algorithms, which guarantee that at least one of many threads is going to terminate in a finite number of steps [32]. This is not the case with locks; if thread holding a lock is not scheduled then no one is allowed to make progress. Lock-free algorithms are built with the same low-level primitives, that are used to build high-level blocking synchronization, but here they are applied to the problem at hand. Thus, absence of explicit lock objects does not guarantee lock-freedom, as in theory, blocking behavior can be reimplemented from scratch.

The main advantage of using lower-level primitives, atomic operations, comes in the form of better control over synchronisation, which may translate into better performance. Naturally, performance improvement is not guaranteed; contentious lock-free algorithm may scale much worse than an alternative implementation based on high-quality locking.

### 2.2.3 Atomic operations

Atomic operations are hardware-supported instructions, which guarantee synchronized, indivisible execution. They are mostly non-blocking and tend to operate on at most one processor word. Actual usage and implementations vary between architectures but languages typically provide a unified interface. So is the case with OCaml. This project relies primarily on two atomic operations:

- **Compare and swap.** CAS takes 3 arguments, one variable (memory address and two values: old, new. If the variable equals the old value, then it gets updated to the new value in one step. Success or failure are reported to the caller.
- **Fetch and add.** FAD takes two arguments, one variable (memory address) and an integer value. The integer is added to the variable and prior value is reported. FAD always succeeds.

Both operations have similar latency when applied to the same use case [56]. CAS has the benefit of being much more general but at the cost of potentially having to retry. It is worth pointing out, that these operations essentially push the burden of synchronisation to the microarchitectural level but it remains non-trivial. Atomic operations may still take one or two orders of magnitude more cycles than their non-atomic counterparts and most common instructions.

Implementations of atomic operations may allow or forbid certain kinds of memory



reordering [10]. OCaml atomics only come with sequential consistency, which is the strongest restriction. Sequential consistency forbids any reordering across an atomic operation. Therefore, memory order relaxations are not further considered in this report.

### 2.2.4 Linearizability

Linearizability is a property of execution trace, which states that all actions can be re-composed into a correct, sequential history. It is crucial whenever operations execute in parallel, be it in a distributed system or on architectural level. High level concurrency primitives such as locks help to achieve this property trivially; if relevant state is protected by a single lock then any functions linearize since they are forced into sequential order by the very lock. However, such coarse-grained primitives may leave performance on the table.

When performance is the priority, a common approach is to try to distribute the state to allow many agents to act in parallel. Here, linearizability is easy to involuntarily give up, and becomes a critical property to watch. For example, design of a queue does not generally prohibit enqueueer and dequeuer to operate from operating independently on different ends. Such behavior can be achieved using atomic operations to modify *head* and *tail* indices, what effectively doubles the throughput offered by lock. However, suddenly a simple size function  $tail - head$  may produce gibberish output as it no longer linearizes. Independent reads of *head* and *tail* can observe respective values from two different states and compute a particular  $tail - head$  value without the queue ever being that size.

### 2.2.5 DSCheck

Writing concurrent programs is an error-prone process. This is true for all kinds of applications but in particular if they use the lowest level synchronisation primitives, atomic operations. Humans are not good at the kind of precise reasoning required to deal with super-exponentially increasing number of interleavings as more functionalities are added and state becomes more fragmented. Generally, there is no solution to this difficulty. Commonly, academics attempt to write formal proofs for the code, which is also a non-trivial process and have been found to let errors through (e.g. [51] disproved [43]).

CDSChecker [51] is a novel approach to verification of lock-free algorithms. It lets the user automatically validate all possible interleavings of a lock-free algorithm, which is extremely helpful in overall validation of the code and tracking down rare race conditions (e.g. occurring a few times in a million executions of tight loop designed to expose such race [14]). Moreover, CDSChecker does not require building any model of the code, which could be incorrect, but uses the core implementation of algorithm with shadowed atomics library.

Core lock-free data structures of this dissertation have been validated using CDSChecker's

adaptation to OCaml - DSCheck [36].

# Chapter 3

## Related work

Scheduling is omnipresent in computer science. It may happen at multiple levels simultaneously: data-center orchestration [31], OS-level threading (§2.1.1) and runtime threads (§2.1.2). The field is very rich in literature and applications. Therefore, instead of summarising all the related work (which would be beyond the scope of this report), this section takes a close look at the design of two modern runtime schedulers. Project work and design decisions tie back to this review.

The first analyzed runtime is part of the Go programming language (§3.1). The excellent support for concurrency is one of its main features. Just like OCaml, it is a high-level language with features like garbage collection. However, OCaml also strives strongly to maintain high-performance, does not feature preemption or a built-in runtime scheduling. Therefore, the second reviewed scheduler is Tokio (§3.2), which is a Rust library. It covers all of the mentioned discrepancies.

Finally, OCaml’s unique features for supporting concurrency and parallelism are discussed.

### 3.1 Go

Go is an imperative programming language, which aims to be the language for building distributed systems. The first sketches of Go were created by senior Google engineer, heavily drawing on company’s extensive experience with building complex backend applications. Go features a deeply integrated support for lightweight threads, called goroutines, whose design was central to the language since its very beginning.

Go standard library adds a significant amount of machinery on top of the POSIX thread interface. Goroutine is quite flexible in that it can be a short-lived task or a long-running process, since the compiler automatically inserts statements returning control to the scheduler in strategic places, like function entry or loops. In contrast with typical OS threads, Go works hard to let developer schedule new routines as freely as possible, allowing hundreds of thousands of them to be executed concurrently thanks to e.g. marginal memory

footprint - 2KiB per routine at initialization, while OS threads costs 1MiB (although the memory is virtual).

On the surface, Go features a classic work stealing scheduler with local queues and steal-half operation to distribute work. There are, however, significant differences. Goroutines are scheduled on a *processor* rather than worker (OS thread). This decoupling improves remarkably over a naive architecture with runqueue per OS thread. Firstly, task may not be able to make progress if in a system call or a synchronization primitive. In such a case worker constitutes the minimal state that *processor* has to park if blocked and unpark if runnable. Secondly, attaching runqueue to a processor maintains the local invariant (even though runqueue changes hands) and keeps the overall number of queues much lower. Thanks to this rotation there is no tasks lingering in inactive runqueues and necessitating more stealing. On the other hand, if worker runs out of work and decides to steal there is less fragmentation and lower chance of selecting other empty queues. Finally, with such a design autoscalability emerges very naturally as it boils down to minimizing the number of runnable workers within available resources. Runqueues have constant size of 256 items and any extra items are pushed into a global locked queue [? ].

## 3.2 Tokio (Rust)

Rust is another modern imperative programming language. It encompasses a much different philosophy than Go. Historically, performance and safety were a trade-off. For example, memory management was either manual (error-prone, zero-overhead) or handed to the garbage collector (safe, non-trivial overheads). Rust strives to achieve both: safety and performance on par with C++. It applies a number of novel ideas, e.g. ownership system, but they typically come in the form of zero-cost abstractions or static analyzers as adding runtime overheads would quickly make the goal of matching C++ performance unattainable.

Therefore, threading in Rust's standard library largely maps to OS threads and leaves building out more advanced management to the user. One particularly successful attempt is Tokio [45]. An external library, which provides multithreaded runtime and an asynchronous version of standard library.

Just like Go runtime, Tokio also relies on work stealing to share work. However, it does not abstract out OS threads and relies on a much simpler scheme with a single worker per OS thread and number of OS threads corresponding to number of processing units. As the total number of threads is limited and external library cannot do preemption, such a scheme is prone to liveness issues. Thus, Tokio provides a special `spawn_blocking` call to spawn new OS threads on demand for blocking or CPU-intensive computations. Tokio also features a global overflow queue if there is no space in the local runqueue. Notably, Tokio relies on a CDSChecker-inspired lock-free validation as well - Loom [? ].

## 3.3 OCaml

As touched on in 1.2.2, OCaml introduces a new flavor of multiprocessing. It brings a number of benefits, but the most important one from the point of view of this dissertation is decoupling of scheduling and runtime [59]. In simple words, a developer of an external OCaml library now has the ability to write a powerful, built-in-like runtime scheduler without requiring any changes from the standard library, the runtime system or OCaml compiler. The rest of this section describes mechanisms enabling this decoupling and other notable features of Multicore OCaml.

### 3.3.1 Threading

Starting with version 5.0, OCaml will offer access to OS threading through the domain interface. System threads are heavy due to high memory footprint, the overhead of systemcalls and context-switching. Domains are similar, in that a single domain consist of an OS thread and additional OCaml structures such as local minor heap [57]. Generally, domains offer course-grained parallelism and they are optimized for the number of domains to not exceed number of processing units [64].

To use system threads efficiently, we also need concurrency. Historically, OCaml did not provide any explicit support for writing concurrent programs. Thus, developers using Lwt/Async were forced to divide programs into small functions, which are then scheduled dynamically and executed on the same stack.

Multicore OCaml features fibers - light, heap-allocated, resizable stacks. They are non-preemptible. Number of concurrent fibers is limited by memory and may exceed number of logical cores by orders of magnitude without any issues. Furthermore, context-switching between fibers is extremely cheap. It requires saving stack and exception pointers of the fiber and loading ones of the target frame [58]. Therefore, they are a perfect foundation for efficient, fine-grained concurrency.

### 3.3.2 Effects

Effects represent a novel way of modelling not just concurrency but all kinds of computational effects. The concept is a product of research on theory of computation [54] and, even though the formal underpinnings are far beyond the scope of this report, it is introduced the easiest by analogy.

An imperative programmer may consider it a generalisation of exceptions [19]. Using exception language, effects allow throwing an exception (performing effect), processing it, and optionally returning to the location of throwing (continuation) to resume execution. For a functional programmer, effects provide similar capabilities to monad transformers but in direct-style, without imposing monadic form on [58]. This is especially important

for concurrency, as with Async and Lwt real-world applications end up having almost all code written in the monadic form to prevent liveness issues.

In the case of threading, effects provide a composable mean of stopping and resuming execution. Once effects handlers are set up higher up the stack, any function may perform a yield effect, which immediately returns the control back to the handler. Up until now, the logic is implementable with exceptions. However, the handler also receives a *delimited continuation*, which resumes execution of the yielding function. In the case of a scheduler, handler may put received continuation into a queue, take the oldest element and resume it instead. This logic is implemented efficiently using fibers described in the previous section (3.3.1). Whenever effects handler is set up, it instantiates a new fiber for contained logic. If said logic performs an effect, said fiber is unchained from the top of the stack and a new one is created to execute the handler. Naturally, this harmony is by design, and fibers and effects have been strongly optimized to support high-performance concurrency.

In the current shape, effects do not mix with preemption. True preemption is limited to OS-level scheduling. There is no way around this restriction as true preemption requires special privileges and it is not acceptable for ordinary applications to have them due to security concerns. In the case of Go 3.1, runtime simulates preemption by having compiler automatically insert optional yields into strategic places (checkpoints) in the code. Similar solution does not really fit OCaml out of the box because it would pollute entire program with Yield effect, what defeats the purpose of effects system. In Go, such logic is hidden within the runtime and compiler. In the case of OCaml, suddenly pure or even no-op functions would emit effects.

### 3.3.3 Garbage collection

OCaml features a new garbage collector created with multicore in mind [57]. It has two generations: local minor heaps and a shared global major heap. Both minor and major heap collections require stop-the-world pause. The minor collection is fully parallel [57].

### 3.3.4 Domainslib

OCaml ecosystem already has a scheduler endorsed by the core developers [66] - Domainslib. However, multicore OCaml has not been widely adopted yet and thus, Domainslib is not as mature as in the case of other ecosystems. The design of Domainslib closely follows classic deque (§2.1.4). Therefore, Domainslib is not dissected as one of the state of the art schedulers but its performance is compared against this project in (§5.6).

# Chapter 4

## Design

This chapter describes the design space of a scheduler. First, §4.1 states the desirable properties and metrics to evaluate scheduling policies. Afterwards, major approaches are described and other design choices are discussed in the remainder.

### 4.1 Objectives

During the early days of computing, throughput of a system was significantly more important than the interactivity. After all, user interface could be as minimal as punch cards for input and a printer for output. Properties like latency or fairness were in the hands of human operator. Over time, however, computers became ubiquitous and networked. People and businesses started to depend on automatic processes terminating timely and reliably. In fact, major software companies such as Amazon, Google have measured the direct impact of latency on their revenue [42].

Therefore, in line with the literature (e.g. [23], [73], [60]), a scheduler should aim to achieve high throughput and some desired latency characteristics. Typically low tail latency.

- **Throughput.** A scalar value of tasks (or some other unit of work) processed over a time unit.
- **Latency.** The distribution of processing time of tasks over some workload. Naturally, while queueing theory deals with entire distribution, in software engineering practice it is more common to select one or multiple percentiles as the key metrics. For example Amazon chose to optimize 99.9th percentile of latency and deemed higher ones not cost-effective [42]. But a test-suite of a distributed system may consider both 99.9th percentile and median to have a better picture of the system, while a low-latency market maker may focus primarily on top 20% since those are the orders that win the race with their competitors - for everything slower the actual size of the delay is not important.

Preemptive schedulers may also explicitly prioritize fairness. Naively, the lower latency the more fair scheduling, however, this does not hold if jobs have different sizes. For example, a scheduler may prefer to starve a rare long-running job rather than keep thousands of small tasks waiting, since the latter is going to have a higher impact on mean or (sufficiently small)  $n$ th percentile latencies. However, as OCaml does not feature any preemption mechanisms (§3.3.2) and the CPU time is effectively specified by the developer, the schedulers developed in this project treat all tasks as drawn from the same distribution. As discussed further in (§4.4.2), staged architecture does allow developer to more explicitly choose between fairness, latency and throughput.

## 4.2 Benchmarks

This section describes the three main benchmarks used to evaluate different scheduler designs on realistic workloads with respect to described objectives (§4.1). Each benchmark aims to simulate the true characteristics of the core of each workload (e.g. branching factors, dependencies between tasks, memory patterns) but at the same time aggressively mock all dependencies such as networking and other IO. This significantly reduces noise in the benchmarks and improves replicability.

### 4.2.1 Packet processing

TCP is a backbone of the internet as we know it. It is used by countless applications, from the world wide web to low-latency trading infrastructure. Thus, the algorithms for processing TCP packets became very sophisticated. At any step they tend to only parse the parts of the packet that are necessary to proceed. Such lazy behavior is critical for performance and security.

Therefore, the first benchmark imitates a TCP server, e.g. webserver. Firstly, it accepts a packet and spawns a lightweight thread for its processing, which in turn scans for newlines. The first necessary step in order to do more selective and possibly parallel parsing afterwards.

### 4.2.2 Audio mixer

Telecommunication is a relatively low latency endeavour. Research in human-computer interaction generally recommends keeping latency of (any) response below 100ms for it to feel instantaneous ([48], [15]). Beyond that, users begin to notice the delay and the experience deteriorates. [13] focuses on mouth-to-ear latency in softphones (software phones) and classifies everything below 150ms as excellent, 150ms-300ms good and over 300ms as poor.

While 150ms round-trip time may seem like an undemanding deadline for a modern server



but there is a lot that can go wrong (and goes frequently, as we have experienced during the pandemic).

- **Network latency.** In practise vast majority of the RTT budget is consumed by the transport. Packet may have to cross multiple carrier networks or simply a large geographical distance. In practise, the less latency is imposed by the server the better average user experience.
- **Throughput.** Typical VoIP call-leg sends 20ms frames, each containing a few hundred bytes of audio. In my professional experience, a typical FreeSWITCH [46] installation was expected to handle up to 1000 concurrent calls without quality loss. 1000 concurrent calls with 2 legs each and bidirectional media means processing 100k packets a second. Each with a real deadline much smaller than the mentioned 150ms.
- **Audio complexity.** Moreover, standard VoIP signalling protocol SIP allows every participant of a conference to negotiate different codecs. Thus, the server has to decompress all incoming audio, mix it together, compress, and only then send out. In the case of mentioned FreeSWITCH, each call-leg has a separate thread for IO and codecs, and another one per conference to mix audio. That results in audio being passed around and buffered a lot, potentially causing contention.

Thus, the second benchmark imitates a VoIP server with a number of conferences and participants, and FreeSWITCH-like [46] audio-processing, audio-mixing threads. This benchmark aims to simulate an enterprise application with extremely complex scheduling patterns and a perhaps not perfectly optimized code, where individual threads frequently need to yield to wait for others.

### 4.2.3 Binomial option pricing

The last benchmark delves into the world of finance. The livelihood of trading shops depends on their ability to accurately and timely price instruments.

finish me

Moreover, binomial option pricing requires an interesting dynamic processing computation. It builds a lattice of possible future prices and walks it backwards to propagate expected options payoff taking interest rates into account. The computation is non-trivial to effectively parallelize.

## 4.3 Ordering

This section describes the core ordering problem in scheduling.



Figure 4.1: The figure shows two ways to traverse tasks graph. Boxes with blue shade represent task physically present in run queue, run stack. The grey boxes constitute tasks that are going to be spawned by their respective parents. To simplify, assume that every task is drawn from the same distribution. In such a case, requests processed using FIFO order terminate at timestamps 7, 8, 9, while using LIFO at 3, 6, 9. Thus, even though FIFO optimizes per-task latency, the latency of actual requests favors LIFO.

### 4.3.1 FIFO and LIFO

First in, first out (FIFO, also FCFS) regime assumes running tasks in the order of arrival. Effectively, that corresponds to traversing the graph in figure 2.1 in a breadth-first manner. FIFO is commonly used in the real-world schedulers. As evidenced during writing of this report, it is extremely robust. The mechanism of always taking the oldest element naturally acts against high latencies and starvation. The latter, arguably, being one of the most undesirable failure modes in a scheduler. Thus, user can use a FIFO scheduler fairly freely and expect no adverse effects beyond a potential performance penalty. Moreover, additional features tend to be easier to implement (e.g. §4.5.4) as this kind of traversal remains robust even with minor reorderings caused by stealing, overflow structures, incautious workloads, etc.

Last in, first out (LIFO) order takes the opposite approach and always attempts to schedule the most recently added task. It results in a depth-first search traversal of the DAG, which hints at its potential for starvation if at least one of the scheduled workloads consist of an infinite graph. The main advantage, however, lies in LIFO scheduling respecting the principle of locality. Tasks tend to be related to each other and by running the most recently scheduled task, the scheduler maximizes the chance that required data is still in cache. Since memory is hierarchical, the benefits should be noticeable for a number of tasks executed in order and for a wide range of memory requirements.

These two fundamental strategies highlight the common trade-off between throughput and latency in scheduling (§4.1), which stems directly from policy on the lowest level: prioritising either oldest task (for latency) or newest (for locality).

## Fairness revisited

Previous section (§4.3.1) describes a common understanding of the FIFO and LIFO trade-off. However, as evidenced in §5, choosing LIFO does not need to negatively impact latency. Firstly, if some particular workload benefits significantly from locality, it is likely that both throughput and latency are going to improve. This is especially the case if either stack size is small or tasks move swiftly enough to effectively not spend much time awaiting processing. Secondly, LIFO does in fact prioritise latency, but measured between subtrees of particular tasks rather than tasks alone. Naturally, defining a unit of work to be a task is much more practical and concrete the point of view of software development practice. On the other hand, oftentimes we would actually rather optimize the actual observed latency of e.g. HTTP request rather than the latency between tasks. Thus, in some real-world workloads FIFO-order may actually degrade both key metrics by design. An example of this phenomenon is shown by figure 4.1.

A possible counterpoint to such example would say to simply run the entire request as a single task. Thus making the task-latency and request-latency overlap, what makes FIFO the optimal choice for this metric. However, the workload does not have to be sequential. It might be better expressed as an actual graph of tasks. Splitting the task is also the only way to take advantage of parallelism when system is less loaded. Finally, splitting the request into multiple tasks just lets the scheduler make the decision. This is desired as the scheduler has more information to make the correct call for entire system. Further, a real scheduler is likely far more sophisticated than just FIFO/LIFO and application should not need to make such assumptions.

## Allocations

Finally, it is worth noting that described effects may be over-powered if workload is simply more efficient to traverse in either depth-first (LIFO) or breadth-first (FIFO) manner. Such a bias may stem from a number of effects but memory is a common one. Overall, memory does not scale linearly. Hardware is strictly hierarchical with latency increasing exponentially at every stage.

```
let rec fib n =  
  if n < 2  
  then 1  
  else  
    (let a = schedule (fun () -> fib (n-1)) in  
     let b = schedule (fun () -> fib (n-2)) in  
     await a + await b);;
```

Figure 4.2: Naive Fibonacci implementation.

OCaml garbage collector is generational; minor heap is cheaper than major heap (§3.3.3). Sometimes faster scheduling method is simply the one that minimizes allocations. For

example, FIFO traversal of naive fibonacci implementation is going to quickly exhaust all available memory and experience a significant slowdown. The same workload under LIFO scheduling is trivial. Naturally, there are workloads that favour FIFO for the same reasons.

### 4.3.2 Hybrid

As discussed in the previous section (4.3.1), FIFO and LIFO regimes prioritize fundamentally different metrics. A natural next step is an attempt at amalgamation of the two approaches to identify new points in the trade-off plane and, perhaps, generalize or move towards an adaptable policy. The following heuristic were chosen for evaluation:

- **Stochastic.** Random selection between scheduling the youngest and oldest in the structure.
- **Switch every n.** Toggle between LIFO and FIFO every n tasks.
- **LIFO, reverse stack every n.** It is derived from the observation that LIFO offers superior performance but tail latency suffers due to elements lingering at the bottom of the stack. Thus, every n elements the scheduler can simply reverse the stack, sacrificing locality in that particular moment to execute overdue work.
- **LIFO slot.** Have a special slot for the most recently scheduled task. When a new task is scheduled, the item in the slot gets pushed onto the stack. It has the disadvantage that either the item in the slot cannot be stolen without using expensive synchronised operations on all accesses.

## 4.4 Contention and locality

Previous sections (§4.1, §4.3) describe global scheduling properties and mechanisms achieving them. However, a simple locked queue (or stack) do not scale beyond a single core. Firstly, imposing thread-safety requires costlier primitives, which are then slower even in the single-core case. Secondly, actual insert/remove operation, which sets in pointer in the array, is significantly faster than fastest atomic operations requiring cache line lock. Thus, as the actual parallelizable portion of the work is small, in line with Amdahl's law [38], little performance is gained as number of threads increases.

### 4.4.1 Core structure distribution

One way to fix the contention is through having multiple instances of the underlying structure and load balancing across them. In the case of FIFO, the multi-queue [53]. Such a distributed structure is no longer strictly FIFO. But, looking at all pairs of processed elements, the number of reorderings from the precise order is small, and enforcement of a hard bound on reorderings possible [41].

Now, let's focus on multi-queue. There is no need for load balancing on both ends. Instead of having all workers try to insert elements into all the queues and cause contention, each worker could fill its own designated queue. That also improves constant factors, as single-producer queue requires weaker synchronization than a multi-consumer one (§B.5). The core properties of a structure are still maintained by the load balancing on the dequeue side.

There is one more natural optimization in sight. Namely, if worker can take an item from multiple queues, it should prefer to run the work it has scheduled, as it is more likely to benefit from locality and finish faster. An implementation of such a bias is trivial, as worker simply needs prefer to take an element from its insertion queue. Such a local deque operation has a lower constant factor because it does not need to synchronize with insertion. Thus, we have arrived at a more distributed and scalable architecture, at the cost of enforcing strict ordering. This design forms the basis of many real-world schedulers, as described in §2.1.4. Similar optimization process applies to LIFO.

To summarize, sacrifice of the global order property for local, per-core structures aims to decrease contention and increase locality. While any deviation from global order acts to increase tail latencies, in this case, it may be balanced off by overall increase of throughput and tasks being finished sooner. Naturally, certain formal guarantees have been given up and that may become more pronounced under specific workloads and during system degradation. The next section discusses an alternative, more stable approach (§4.4.2).

#### 4.4.2 Staged

Staged architecture [70] constitutes a different realization of FIFO order. Different sets of system threads are assigned to handling particular kinds of logic. Those sets are joined with queues. For example, a web server may have separate thread pools for parsing packets, managing connection state changes, TLS, and a number for business logic, e.g. reading files and querying DBMS.

Such a model forfeits all the locality between tasks, as progressing to the next stage means being passed to a different system thread. Nonetheless, it offers other benefits.

- **Global state locality.** Task are local to global state of the particular stage. It is beneficial for data-intensive applications (as opposed to compute-intensive [42]) as most I/O operations require use and modification of global state at least at some stage. Here, the thread responsible for setting up TCP connection is able to maintain the list of TCP 4-tuples high in the cache hierarchy. In contrast with e.g. task-locality in FIFO, locality to global state is not going to perish as queue sizes grow.
- **Graceful degradation.** The scheduling architectures with a single structure per CPU described in the previous section (4.4.1) tend to degrade more unpredictably.

Work-sharing decreases when most threads become overwhelmed and focus on local queues (for much-needed throughput), what leads to higher variance in their backlogs and thus latencies. Staged architecture performs worse at low and optimal load but degrades more gracefully when overloaded. This stability comes from its simplicity. Overload causes queues to grow but there is no adverse effects on the execution model that compound the issue [70].

Therefore, even though such a setup is unlikely to provide the top performance for compute-heavy or highly predictable applications, it is a compelling strategy for modern auto-scaling distributed systems. They can increase the number of servers as load grows but spinning up new machines always comes with some delay, and it is crucial for service not to degrade before reinforcements arrive.

Finally, staged architecture does not have to be implemented from first principles. While such an approach would yield the biggest performance benefits for some workloads, it is also the most constraining one. Throughout this thesis, staged architecture is instead used as a useful model for composing different schedulers. In this case, the main benefits of staged architecture are still reaped but tasks may still benefit from e.g. LIFO scheduling for small tasks within a single pool.

## 4.5 Other design choices

### 4.5.1 Work distribution

Work stealing is a widely used realisation of the idea described in the previous section (4.4). It assumes a thread focusing on the tasks in its local structure for as long as possible. Then, it randomly select another queue and steals some of its items. Work stealing has proven to be an effective and robust approach in the past. Nevertheless, this report argues that work stealing alone is not enough for the present and future.

Despite its simplicity work stealing can be assumed to serve two distinct roles in non-preemptive scheduling. Firstly, if only some cores spawn the work (e.g. single web-server), stealing helps to distribute the work among many cores. Secondly, it helps to reduce latency if a task unexpectedly occupies CPU for longer than expected. In theory, developer can prevent the latter. But in my experience of working with Async/Lwt, it still happens frequently as system evolves. Even assuming perfect code, once in a while an IO operation or database query hits scalability ceiling and suddenly becomes much slower (*falls of a cliff*). Could be due to suddenly not fitting into some level of memory hierarchy or DBMS changing optimization plan as more data is processed. Multicore does not solve the problem of CPU-hogging tasks but offers tools alleviate the issue.

The latency reduction case is difficult to improve upon. A thread simply cannot know when it is going to get blocked by a lengthy operation. Therefore, the unoccupied

threads have to conduct an active search for lingering work and *stealing* needs to be non-cooperative. (Alternatively, some additional bookkeeping could be imposed on every thread but incurring a constant penalty is troublesome to justify for a feature that is most important when overall system is not heavily loaded). Thus, a random search conducted by work stealing is, arguably, a reasonable mechanism to soften the impact of extensive blocking on latency. If predictable latency is necessary, the staged architecture might be a better choice (§4.4.2).

On the other hand, this same mechanism becomes harder to accept for regular work distribution case. Any increase in number of threads is balanced off with lowered chances of finding work for an individual. Thus, a software engineer needs to be increasingly cautious to achieve high utilization. Even more importantly, in this case the information is available and high-spawner can issue a call for help, or offload accumulated tasks into a common data structure. A number of work distributions extensions and improvements have been developed and evaluated (§5.3.1).

### 4.5.2 Overflow queue

### 4.5.3 Resizing

Having a runtime scheduler gives its users a freedom to express workloads concurrent far beyond what is reasonable with pure system threads. This leads to some algorithms creating large numbers of tasks, large enough to rapidly fill up their local data structures. Now, a common strategy is to simply send new tasks to a slower, global overflow queue (§3.1, §3.2).

In this report, I argue that this algorithm ultimately bottlenecks task creation as the creator is forced to do offloading. Ultimately, if the workload is imbalanced and there are others threads ready to help, high-spawner should focus entirely on creation and leave the distribution to others. Therefore, a different strategy has been tested: resizing of local structure. Here, spawner increases its local structure to a size that lets others steal work effectively. This takes the burden of transfer off the spawner and makes the process quicker, since it does not have to contend or even synchronize with any other writers on its local structure.

Notably, the structure can scale either vertically or horizontally. The latter has the benefit of decreasing contention between stealers.

### 4.5.4 Yield

Yield operation allows fiber to request descheduling. Commonly, a well-behaved program is expected to call yield to leave CPU, while awaiting an event. That avoids wasting resources and blocking other threads, which are able to make program. However, if the

event is a result of a computation done by another thread, yield is necessary to allow both threads to terminate and becomes a key part of the logic. Thus, the design decisions behind this small part of the mechanism have a profound effect on the usefulness of the entire scheduler. As described below, they are also not trivial.

Firstly, pure LIFO-based scheduler is not practical as the underlying data structure prevents implementation of yield operation. LIFO can only operate on the top of the stack and thus yielded task is the only one that can be picked up afterwards. Thus, LIFO ordering needs to be relaxable when needed. Heuristics such as firstly popping the element and then pushing yielded task tend do not fix the underlying issue but increase the number of blocked threads required to deadlock. A more elaborate approach swaps yielded task with a random item in the stack and runs it instead. That fixes the underlying issues but overall performance deteriorates if most tasks in the stack are blocked, and adding swap operation to complex lock-free stack implementations may not be possible. Further, a naive work stealing scheduler may still deadlock. Work-stealing mechanisms tend to bias strongly towards use of local, per-processor queues and steal only if necessary. Such construction improves locality of workload, limits contention and superfluous passing of tasks between processors. However, processor of such a scheduler prefers to keep spinning on a single yielding task rather than take more work from peers. Therefore, the final implementation puts yielded tasks into a completely separate, global queue, which is periodically checked by processor. Such an approach avoids described issues at a cost of small extra cost in handling yielded tasks, which is acceptable because yielded tasks are unlikely to be latency sensitive or constitute majority of the workload. If that was not the case, either the scheduler can decide whether slow or fast path should be used or, perhaps, more than one yielding mechanism should be exposed - one for fast yields (*soft yield*) and one to be called every-n yields to break the deadlocks.

Notably, Linux kernel developers have made a choice in a similar vein in 2003 when yielded tasks were changed to enqueue to expired queue rather than run queue [25].

#### 4.5.5 Promise

Promise is a placeholder value returned by a scheduler for a newly added task. It is similar to an option type, which is automatically filled with returned value upon completion of respective task. If another thread attempts to read the promise when empty, it becomes blocked until the result is known [62].

Fundamentally, promises do not have to be implemented in the runtime. They are a convenient abstraction to avoid busy waiting, that can be built entirely from standard synchroni-

```
val await : 'a Promise.t -> 'a
val schedule : (unit -> 'a) -> 'a Promise.t
```

Figure 4.3: Implemented promise interface.



sation primitives. It is the case in Go [22] as with preemption, goroutines tend to be more long-lived than tasks in Rust or OCaml, and similar mechanism is offered by channels. Conversely, they have been included in this project as tasks are very granular and their presence allowed audio mixer 4.2.2 and option pricing 4.2.3 benchmarks to be written in a more idiomatic way. Figure 4.3 shows the implemented interface. Naturally, it is an eager mechanism - scheduled tasks are executed even if nothing awaits the promise.

# Chapter 5

## Implementations and evaluation

This chapter begins with an overview of implemented schedulers (§5.1) and the principles behind them (§5.1.1). The next section describes methods (§5.2) and what follows is a detailed analysis of evidence gathered to verify the hypothesis (§1.1).

First, it shows that some classic ideas like work stealing with an overflow queue (used in Go and Rust) do not scale to modern hardware and concurrency (§5.3.1). This result is analyzed in depth (§5.3.1) and a number of extensions favoring fine-grained concurrency are discussed (§5.3.1) and evaluated (§5.3.1). Further, the section focuses on more complex workloads and studies emerging trade-offs between schedulers: web-server with cache effects (§5.3.2), bimodal task processing time (§5.3.3) and LIFO heuristics (§5.3.3), mixer benchmark (§5.4), and binomial option pricer (§5.5).

### 5.1 Schedulers

As explained in §4.3.1, there is an inherent trade-off between FIFO and LIFO scheduling. These two mechanisms have been implemented from scratch as baseline choice. Their design follows the scheme with local data structures (§4.4.1) and relies on work stealing for work distribution (§4.5.1). They are built from first principles and supported by high-performance lock-free structure (§5.1.1).

As shown by §5.3.3, §5.3.1 the fundamental FIFO and LIFO schedulers can be limiting or outright fall short of expectation. Thus a large number of alternatives, modifications and extensions has been tested. They can be divided into 3 groups:

- **Staged architecture.** The staged architecture utilizes the baseline schedulers as micropools and provides scalable means for scheduling tasks across them (§4.4.2). This implementation uses a scalable, lock-free multiqueue (§B.8).
- **Resizable FIFO.** Work stealing FIFO scheduler, which grows local queues to match the load rather than push items into an overflow queue. It uses the same lock-free

queue as FIFO scheduler extended with resizing mechanism (§B.7).

- **LIFO heuristics.** LIFO order may produce extremely bad tail latency for workloads with deep task graph. §5.3.3 evaluates a number of heuristics that try to capture the benefits of LIFO, while avoiding the edge case by doing FIFO removal at strategic times. They are implemented as a wrapper around LIFO scheduler.
- **Work distribution extensions.** Work stealing is a classic work distribution method, which, nonetheless, has a number of disadvantages. A number of work distribution extensions has been considered. §5.3.1 discusses both the disadvantages and proposed extensions.

### 5.1.1 Design principles

Lock-free data structures tend to be difficult to use in the real-world. Firstly, all assumptions about their naive counterparts are off once fine-grained concurrency is in place. For example, such a trivial `size()` method may turn out to be non-trivial to make linearizable (§2.2.4) when size of a linked-list or circular queue changes in multiple places at once. This makes the trade-off space far more complex and points in that space (algorithms) more varied than in the single-threaded world.

Secondly, the effort required to add new features increases as a function of current complexity. With a single-threaded code, developer implementing a new function can focus solely on the state before and after its execution. That alone is enough to examine whether the new function fits with the rest of the codebase. Moreover, if the code has no side effects and illegal states are not representable, even just a type check provides high confidence. Such a function is correct as long as its input and output pairs follow requirements. This could not be further from the truth with lock-free concurrency, wherein every new function has to be made safe to execute concurrently with entirety of existing codebase. Further, even if the new function is correct, it may render seemingly unrelated portions of existing code non-linearizable.

Therefore, this thesis relies on lock-free structures inspired by Kappes and Anastasiadis [37]. Their designs are described in detail in §B. In general, they are optimized for the following properties rather than constant factors.

- **Scalability.** Prefer to use fetch and add operation (FAD) for modifying indices. CAS and FAD operations take approximately the same time [56] but traditional CAS-based queues scale poorly due to number of CAS retries growing proportionally with number of threads. Here, (despite additional item-level CAS) the contention is resolved by FAD and the whole structure scales similarly to state-of-the-art FAD queues (e.g. [72]).
- **Flexibility.** Lock-free implementations can be unwieldy and tricky to debug. This posed a particular threat to this dissertation. The described queue has the benefit

of being extremely flexible for a lock-free algorithm and allowed exploration of many modifications at relative ease, e.g. LIFO pop operation, non-blocking returns, and single-producer relaxations. Extra features, e.g. immediate removal of cancelled items, are also feasible (§A.2).

- **Simplicity.** The base case of this structure takes a little over 10 lines of lines of code. It is unusually concise for a practical multi-producer multi-consumer LF queue.

## 5.2 Methods

This section describes details of the experimental setup, procedures and the statistical methods.

### 5.2.1 System

The hardware features two AMD EPYC 7702 processors ([12]) plugged into a dual-socket Gigabyte MZ92-FS0-00 motherboard ([67]). It has 1082 GiB of RAM and caches have the following sizes L1 64 KiB, L2 512 KiB and a shared L3 256 MiB. Cache line is 64 bytes wide.

The installed system is Ubuntu 22.04 LTS. All the schedulers and benchmarks in this project have been compiled with OCaml 4.12.0+domains [24], which has a hard limit of 128 domains (OS threads, §3.3.1).

### 5.2.2 Procedures

Each benchmark has been run 11 times. The first sample has been dropped as it tends to be more noisy than others due to lazy loading of libraries, cold caches, etc.

Performance sensitive data was collected on thread-local counters. Benchmarking latency required many calls to clock, which were ensured to be served by vDSO [39] and take less than 50ns on average.

### 5.2.3 Statistical methods

Since it is quite hard to assume any particular distribution (esp. gaussian) when it comes to performance measurements, the statistical methods used throughout this project are distribution agnostic. Charts feature medians, and 25th, 75th as error bars.

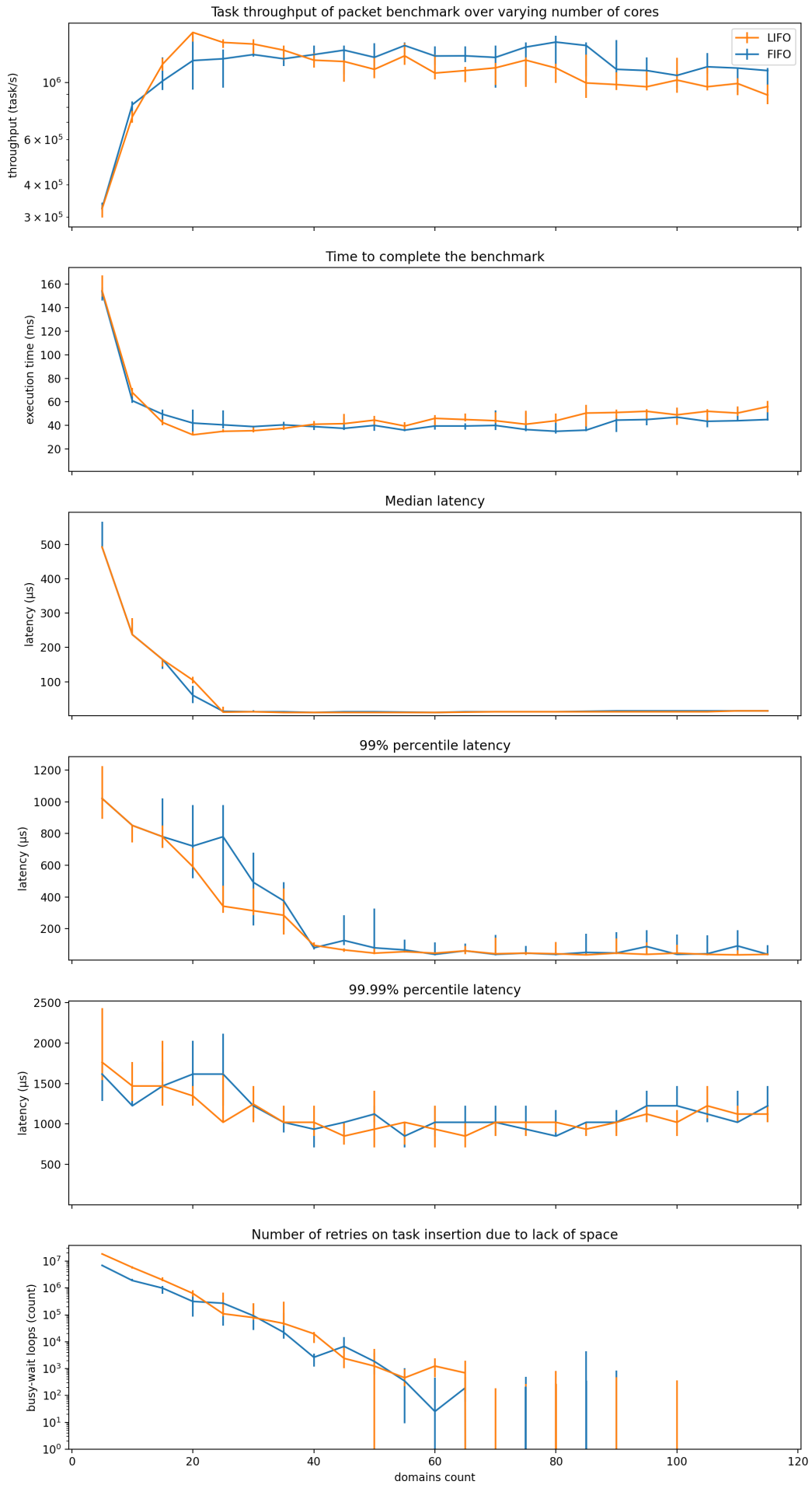


Figure 5.1: 50k.

## 5.3 Server benchmark

### 5.3.1 General case

The first benchmark mimics a minimal TCP packet processing scenario. The workload relies on a single spawner as it would be the case with a server listening on IP and port tuple. To minimise the noise, actual network is not used. Instead, the *server* thread chooses one out of 10 pre-made packets, copies it and schedules a handler task by performing an appropriate effect. The benchmark focuses on max load scenario and spawns tasks as fast as only possible, until the requested total is reached. Then, it monitors an array of thread-local counters, which are increased by successfully terminating tasks. Notably, the packets are between 2 and 3 KiB, and the spawner thread focuses solely on task creation. Therefore, cache locality does not significantly affect the performance. Likewise, the performance advantage of LIFO stealing does not apply to stealing from the spawner, as spawner never removes elements from its local structure. Local structure sizes are held constant at 128 elements, which is enough to keep the spawner from waiting with such small tasks.

The results of conducting a test with 50k packets in such a controlled setting are shown by figure 5.1. The first panel shows mean processing speed on a logarithmic scale. In the first part of the graph, between 5 and 20 domains, the performance rapidly accelerates with increase in the number of threads. 20 domains witness peak performance by LIFO scheduler and as more threads are added, the performance degrades. At first slowly, then faster beyond 80 domains. FIFO scheduler sees its peak at 80 domains but its performance is also relatively stable between 20 and 80, with a pronounced drop afterwards. This is surprising; adding more resources may not translate to a significant improvement [38] but it should not be detrimental either. Discussed further in §5.3.1.

The second panel shows actual time required to complete the benchmark. Next, the charts from three to five show 50%, 99% and 99.99% latency percentiles. Again, up to 20 domains there is a steep decrease in all latencies. There are local differences around 20 domains, with FIFO offering a better median latency for one point and LIFO better tail latencies for 2-3 points. Afterwards, the results converge. The tail latencies are partly driven by LIFO simply being faster to process tasks but, likely, not as much as it looks since error bars indicate more noisy measurement than elsewhere.

Finally, the last panel shows number of retries of the main spawner (*listener*) when trying to insert elements into the queue or stack. The retry loop is cheap to execute. Clearly, for lower core counts the spawner frequently throttles adding tasks due to lack of space. However, the number of busy-waiting cycles lowers with extra cores, quickly reaching point where magnitudes are too small to affect overall performance. At last, it ceases to occur in the median case beyond 45 cores.

Both FIFO and LIFO perform very similarly on all metrics and exhibit similar phe-

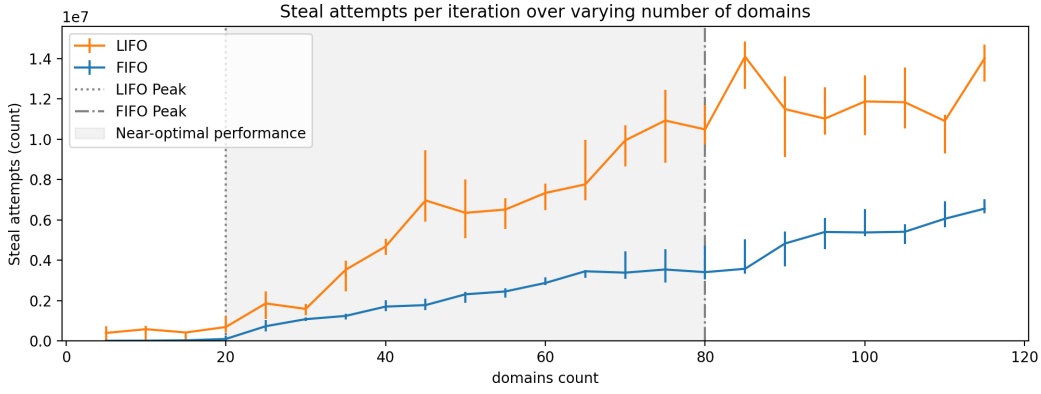


Figure 5.2: The throughput achieved on 50k tasks over varying number of domains.

nomenons. As hinted in the first paragraph, the workload is so simple that it does not let some of the individual advantages prevail. There are slight differences, i.e. LIFO peaks at 20 domains, while FIFO at 80. But their overall performances (throughput and latencies) in ranges 0-20, 20-80, 80-115 are comparable. Overall, it shows that both implementation are fairly balanced and a good foundation for benchmarking more complicated workloads.

## Degradation

In line with Amdahl’s law [38], it is expected that any application will stop being able to take advantage of more resources at some point. After all, scalability is something to explicitly design for and even then, may not always be possible. However, there is no justification for more workers actually causing harm to the performance. Such a characteristic is highly undesirable in such a foundational system as scheduler. However, why would it arise in such a battle-tested idea as work stealing?

In the LIFO case, as shown by 5.2, the scheduler is unable to maintain the increase in stealing attempts. Such increase is necessary for scalability, as each worker will need more stealing attempts to find the tasks. For example, if 1 out of 2 threads is spawning, the other thread has 100% hit rate. On the other hand, if it is 1 out of 10 threads, then there are are likely going to be some misses or inefficiencies (minimal amount of work acquired). The diagram shows, that above 80 domains, as the information becomes more dispersed, the scheduler does not increase stealing attempts. In theory, there should be no scalability issues since each new worker also adds a new queue but in practise contention increases significantly as starved cores keep retrying and impeding the actual useful attempts. Conversely, if there is significantly more work and more time, the tasks saturate the system enough to prevent excessive stealing and observed negative effect ceases to exist (5.4).

In the FIFO case, the problem also relates to work distribution. FIFO is not able to steal as much as LIFO because all steals compete with local dequeues, and, by design, those steals tend to lose the race more often since local dequeue uses a FAD rather than CAS to increment the index (§B.5). Therefore, while stealing alone is not maxed out, it

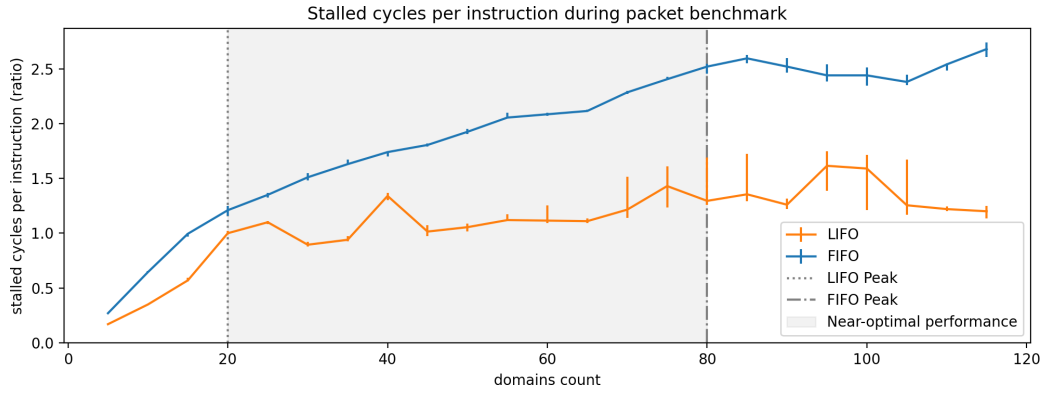


Figure 5.3: The throughput achieved on 50k tasks over varying number of domains.

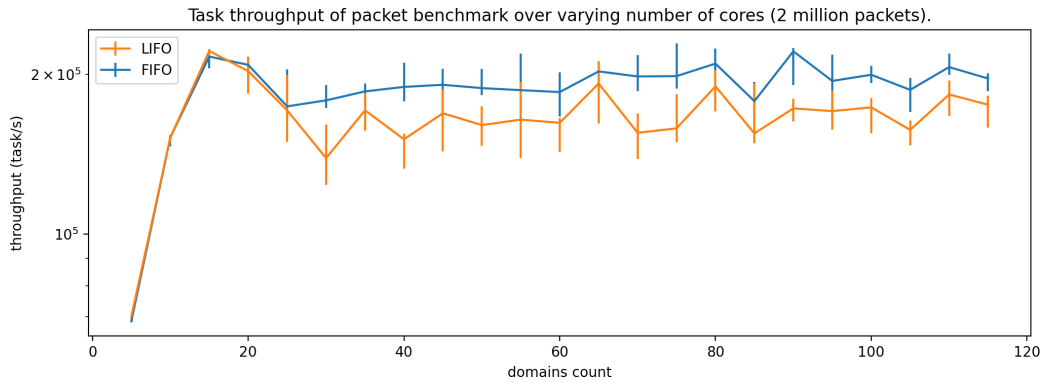


Figure 5.4: The throughput achieved on 10 million tasks over varying number of domains.

significantly affects the rest of the logic. As shown by figure 5.3, FIFO reaches over 2.5 stalls per instruction beyond 80 domains and this number keeps raising. The effect is also visible for LIFO but its pressure on the entire caching subsystem is much subtler.

Finally, it is worth stating that this result should not be interpreted as work stealing being generally inappropriate for saturating 100 cores. Packet benchmark creates a short burst of 50k items, which reaches a throughput of over 1.5m items per second. Each task has its own logic and requires allocations but it does constitute a very fine-grained concurrency. That puts a high pressure on the scheduler and exposes its inefficiencies. Further, there is an unusual amount of distribution required. It is far easier to distribute tasks onto 10 threads than 100 due to e.g. atomic operations taking constant time and load information is being less dispersed. If the workload is simplified just a little, the scalability improves.

- **Heavier task.** See figure 5.5 for the performance when each task has had an extra 500ns sleep to perform. This is enough time to extend the stability beyond 100 domains.
- **More spawners.** See figure 5.6 for the performance under more spawners, i.e. developer made an explicit change to help the scheduler.
- **Higher workload.** In fact, increasing the number of tasks also stabilizes the scaling. See figure 5.4 for the case with 10 million task instead of 50k. This case saturates



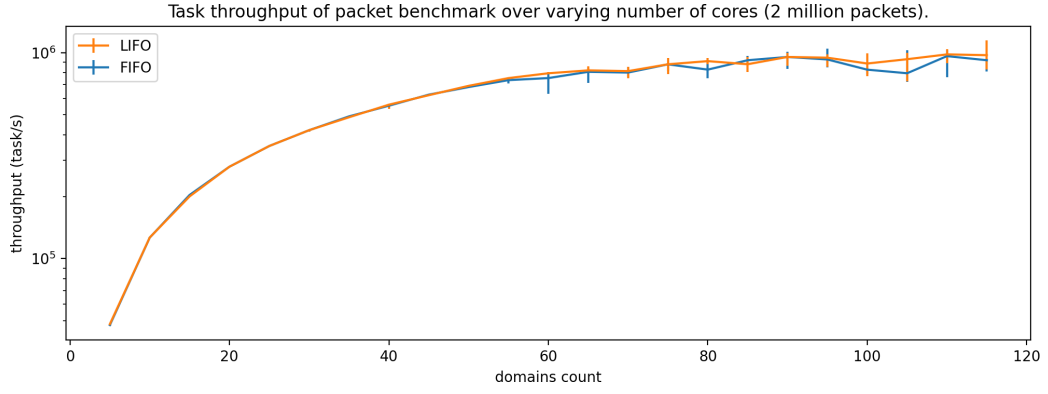


Figure 5.5: The throughput achieved on 50k tasks with over varying number of domains. Each tasks loaded with additional 500ns wait.

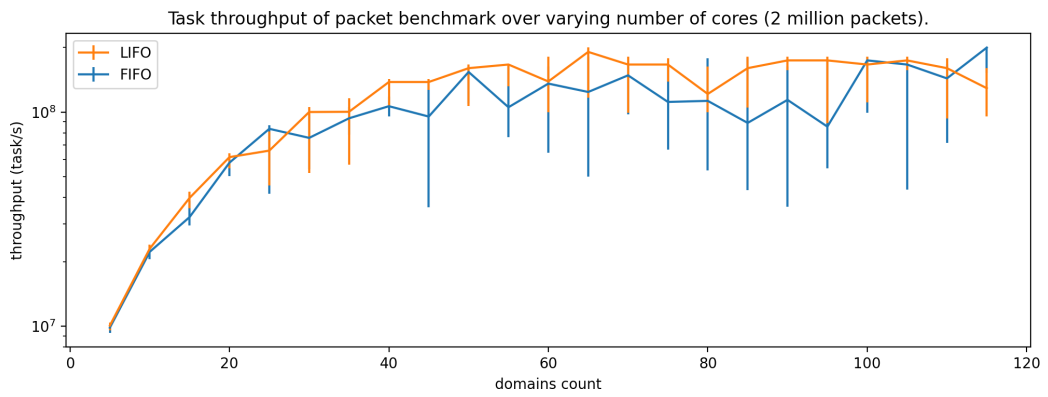


Figure 5.6: The throughput achieved on 50k with 4 spawners. (2 spawners in the 5-core case).

many more workers, and while overall throughput is lower, the performance remains stable beyond 30 cores.

These differences in scalability highlight the need for new methods to take advantage of high parallelism and very fine-grained concurrency (§1.1).

## Work Distribution Extensions

Work stealing is robust but not perfect, e.g. the scalability ceiling describes in the §5.3.1 or blind victim selection. As described in §4.5.1, regardless of primary work distribution mechanism, some sort of work stealing is necessary to prevent starvation in non-preemptive design with local structure. Otherwise, with a purely cooperative scheduling, any long tasks is going to significantly increase latency of all awaiting items. Therefore, a number of extensions to the core work stealing have been considered. They are motivated by the following observations.

- **Contention.** The scalability of atomic operations and inter-processor communication is limited. Therefore, the design of stealing should be wary of those budgets as additional threads are added.
- **Misplanning.** It might not make sense to steal a small number of tasks. If victim does not have enough work, the thief should simply look for another victim rather than conduct a steal and cause the victim to have to steal soon in effect. This could lead to starvation on blocking, thus the implementation simply has a bias to steal small number of items rarely.
- **Blindness.** Random exploration of other threads local structures is inefficient. It causes contention and suboptimal steals. Instead, busiest threads should request help.

The aforementioned observations translated into the following implementations.

- Reduce the contention on critical threads by having each thread only target its  $n$  nearest neighbours in threads array. That creates a hierarchy along which tasks move and prevents the underloaded threads at the bottom of hierarchy from contending in the hotspots. Implemented with nearest 15 and 20 threads.
- **Steal at least 10.** Do not steal less than 10 elements as they will be efficiently processed by owner. Instead, look for work elsewhere.
- **Work-shedding.** A polar opposite of work stealing. Busy worker randomly chooses another thread, which is forced to steal some of the busy thread's load. Notably, [47] found improvement of tail latency stemming from extending work-stealing with work-shedding on a 32-core machine. Implemented as non-sticky, only a single steal is forced, and sticky, in which the victim keeps the target until another thread overrides it.

be there  
way to  
algamate  
se two?

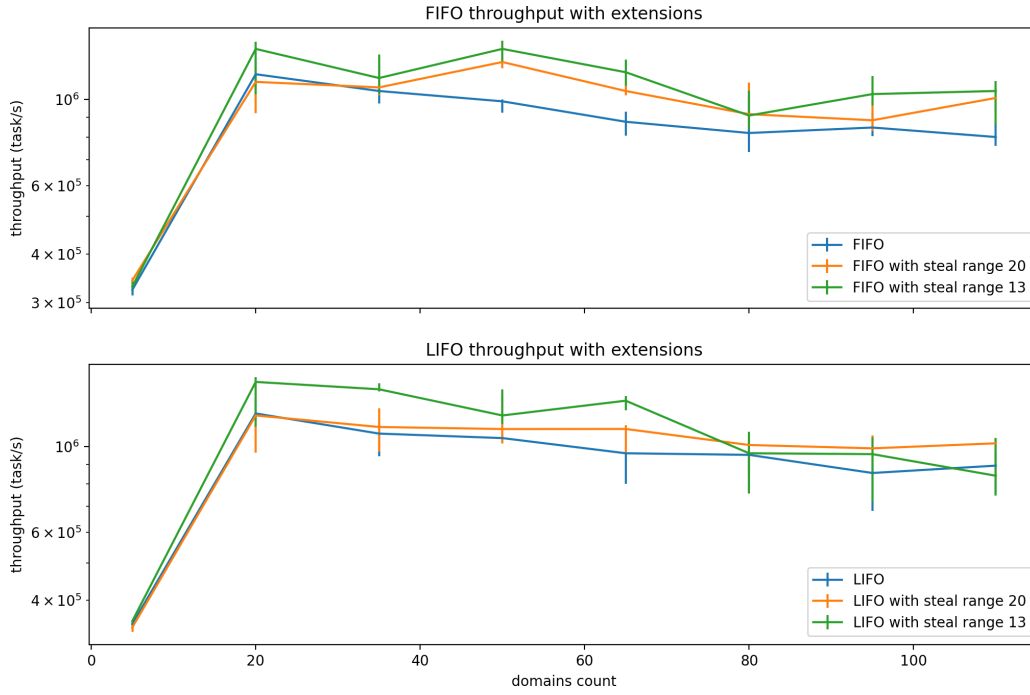


Figure 5.7: The throughput achieved on 50k tasks with over varying number of domains. Each tasks loaded with additional 500ns wait.

- **Work-sharing from busy to free.** Requestor publishes its id in a highly-scalable lock-free queue. Threads looking for work picks up the id from the queue and steals work. This has the benefit of closing the information gap of pure work stealing or work-shedding. Only the overloaded and underloaded threads participate, while others remain unaffected.
- **Overflow queues.** Use extra structure for work distribution and put some tasks into a global queue. Here, spawner thread has a chance of inserting a tasks into global structure. Both Tokio [44] and Go [63] feature global, locked queues for any tasks overflowing local queue. Three queues have been implemented: locked, lock-free, lock-free, 5-multi-queue.
- **Local resizing.** Stealers aims to take half of its victims tasks. Increasing the size of local structure and the number of awaiting itmes may in turn lead to higher number of items distributed per steal.

## Work Distribution Extensions Result

§5.3.1 discussed a number of alternative work distribution mechanisms. The positive results are shown in figure 5.7. Only neighbourhood stealing has shown a clear improvement over the baseline, which is in line with the diagnosis stated in the previous section §5.3.1. It is worth pointing out that such an optimization is going to have a negative effect on coarse-grained workload, where threads higher in the hierarchy are going to be stuck processing tasks for longer and thus not replenish its local structure quick enough. This result shows that very fine-grained workloads may require novel approaches and support

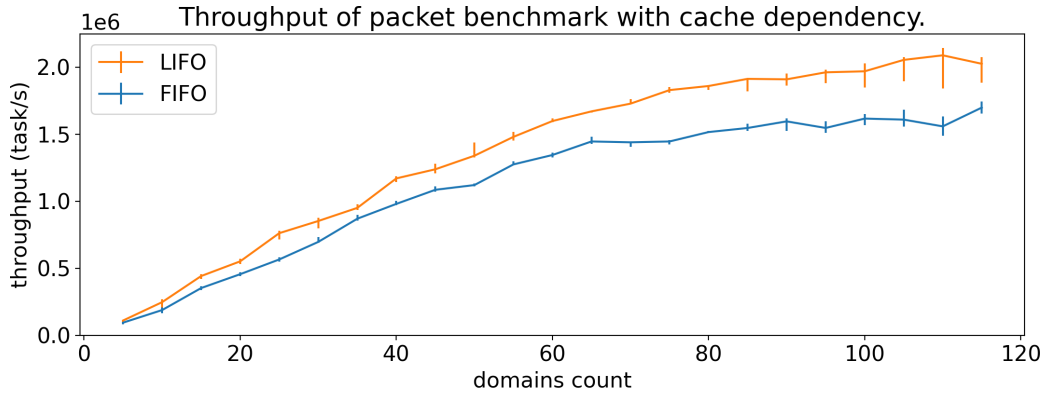


Figure 5.8: The throughput achieved on packet processing benchmark with 500k packets and 10 spawners. The benchmark was modified to have a non-trivial cache effect. Once the task was processed as in the original benchmark, it would spawn a chain of 10 tasks. Each task reads 20 with each reading 20 random bytes from the packet buffer, what simulates picking out different parts of the received data at different stages. LIFO offers a very clear throughput advantage (shown in the figure) and median-tail latency improvement.

the hypothesis (§1.1).

Only stealing at least 10 elements had a strong negative effect, as it paradoxically increased the number of underloaded threads and contention on the loaded ones. Similarly, work-shedding approaches (sticky and non-sticky) resulted in other threads overloading the spawner and causing less efficient work distribution than random work-stealing. In pure work-stealing case tasks often flow through multiple hops, thus reducing the contention in the hotspot. The same effect was observed for simple locked overflow queues (as in Go and Rust) as the spawner has to compete with the workers for the lock.

Finally, scalable overflow structures (lock-free queue, multi-queue) and work-sharing from busy to free had an insignificant positive effect. They would likely yield a stronger benefit for less heterogenous workload. Likewise, local resizing would be beneficial if starting size of the local structure was small enough.

### 5.3.2 Server benchmark with cache effects

This section presents results of a more complicated workload, wherein once the packet is parsed, its contents are passed to the business logic. Namely, each packet from the general case benchmark (§5.3.1) also spawns a 10-sequence of consecutive tasks. Each reads 20 random locations in the packet or does a simple computation (checksum), and spawns its child. That's until the depth of 10 is reached. Moreover, there are 5 spawners instead of 1, and individual queue sizes have been increased to 1024 to accommodate that. The results are shown by figure 5.8. Such a workload naturally favours LIFO, which can make a much better use of hierarchical memory and reduce cache misses. Thus, achieved throughput is far superior with LIFO ordering. Further, in line with the mechanics described in §4.3.1, using LIFO also significantly improves tail latencies.

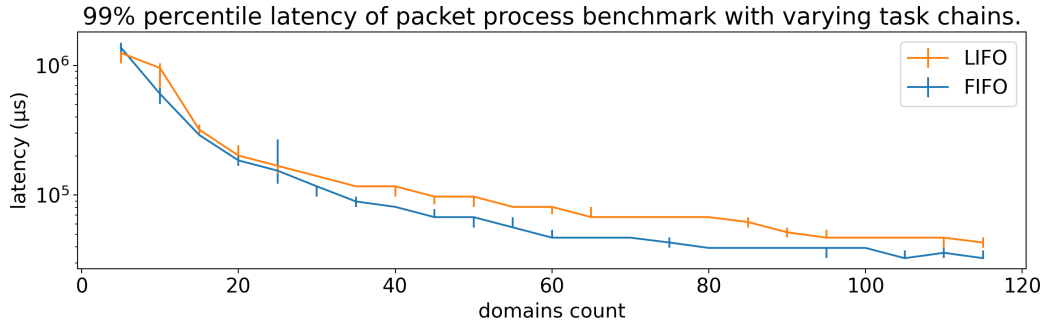


Figure 5.9: The throughput achieved on packet processing benchmark with 100k packets and 10 spawners. The benchmark has a weaker cache effect than 5.8 but still significant enough for LIFO to have a better throughput. However, there is a 0.1% chance that packet causes heavy work to be done, which spawns two tasks taking 20ms to complete. Akin to a network call to a different service. As shown in the figure, FIFO clearly offers a much better tail latency for most domain-counts in such a scenario. For example, at 115 domains LIFO saves over 30% of tail latency.

Notably, extra care is required to replicate this result on modern architecture. For example, simple iteration over the packet in consecutive task does not experience memory inefficiencies due automatic memory prefetching. After a few first cache fails, the rest of the packet is going to be loaded into cache preemptively. This effect breaks down with any more complex memory patterns, which microarchitecture cannot predict.

### 5.3.3 Server benchmark with multi-modal processing latency

Figure 5.9 shows results of a different twist to the packet benchmark than previous section. Here, instead of 10-sequence, only 3-sequence is spawned and the last task has a 0.1% chance to spawn another task with lengthy operation. The 3-sequence still has cache-dependencies, which favour LIFO, but optional final task sets a trap. If scheduler processes the lengthy operations without care, it risks massively increasing latency of awaiting tasks. This favours FIFO, which is able to process lightweight sequences concurrently with the heavy ones, and have the lightweight tasks terminate early. The heavy operations wait longer but in their case the processing time is big enough for extra waiting to have a marginal impact.

#### LIFO heuristics

Pure LIFO scheduling is prone to poor tail latencies. Therefore, a number of heuristic has been implemented. They aim to integrate FIFO and LIFO ordering in a way that improves upon both, or at least provides a new point in the trade-off space.

- **Alternating.** LIFO and FIFO removal alternately. Simple combination of pro-latency and pro-locality choices.
- **Random.** Just as alternating, but the ratio does not have to be 50:50. Instead, it has been tested as 80% LIFO, 20 % FIFO and vice versa.

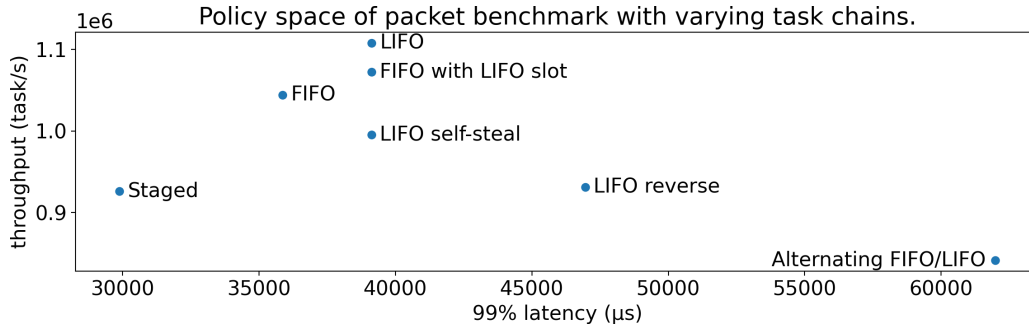


Figure 5.10: The throughput and latency trade-off space achieved by various schedulers on the packet processing benchmark with varied task chains. The frontier consists of pure LIFO, FIFO and staged architecture, whereas various hybrid heuristic offered no benefits.

- **Temporary FIFO ordering.** To prevent any items from lingering at the bottom of the stack, every 256 items, do as many FIFO removals as was the number of elements in the stack. Such approach is commonly known as reversing the stack but this implementation avoids the edge case of an item getting stuck between two infinitely deep tasks.
- **LIFO slot.** Rely on FIFO ordering but have a special slot for the most recently scheduled task. When looking for work, check this slot first. This violates FIFO and helps preserve locality between a chain of tasks. Featured in real-world projects, e.g. 3.1.

Notably, these heuristic have been built as a wrapper around LIFO data structure but have no direct support. Thus, by design, they are costlier to use.

The baseline approaches described in §5.1 and a number of heuristics described above have been tested. See figure 5.10. Staged architecture was the only one to offer a useful point in the trade-off space, in the form of significantly superior tail latency at the cost of throughput. This gain stems from having a pool of threads dedicated directly to the lightweight tasks. The fall in throughput is difficult to avoid in a workload that work stealing is able to scale efficiently. Nonetheless, if the final task was lighter, it is possible for staged architecture to improve on both metrics, as observed with artificial benchmarks.

This result, in line with hypothesis (§1.1), strongly support the benefits of customizing schedulers. Even for the same workload different policies may be appropriate depending whether throughput or latency is more important. In fact, just the fact of composing multiple baseline schedulers using queues significantly improved tail latency.

## 5.4 Mixer benchmark

The mixer benchmark represents a complicated real-world use case. It derives from architecture of a VoIP softswitch - FreeSWITCH ([46]), which heavily relies on threading. FreeSWITCH runs multiple OS threads for a single call. Firstly, there is a thread for the

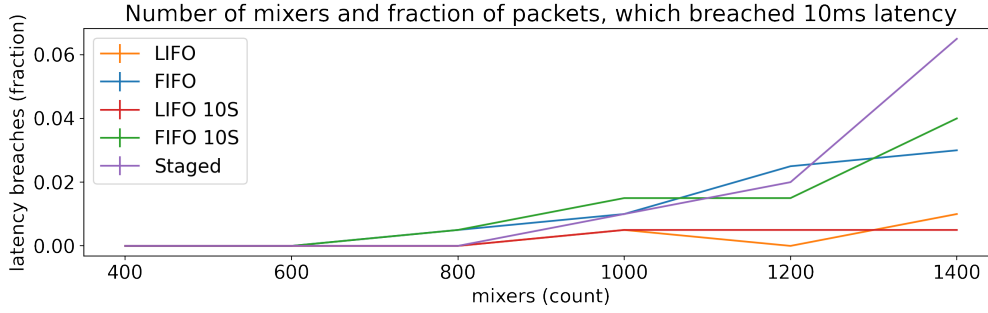


Figure 5.11: The number of latency breaches achieved by different schedulers as the load (number of mixers) increases.

conference. It collects audio from all participants, mixes it (hence mixer), and passes the output to all participants. However, there is a number of things that needs to happen for every participant. At the minimum, each participant can have a different codec setting, thus the incoming audio has to be decompressed for mixing and outgoing audio has to be compressed before sending. In practise, there is a lot of other per-call-leg logic as well, e.g. signalling, jitter buffer. Therefore, there is also a single thread per participant. In such a setting, mixer thread only interacts with participant threads, which in turn interact with external world.

Such a workload is particularly difficult to optimize for due to its complexity. It features short-lived and long-lived tasks. Some tasks benefit from cache locality, while others not. Many details will vary over time, e.g. distribution of number of people in mixers (as conference could have anywhere from 3 to hundreds people), length of calls, codec selection, media routes (IP-to-IP) [69], etc. Moreover, the workload makes heavy use of many high-level features such as yielding (§4.5.4) and promises (§4.5.5), which are not the typical targets of high-performance optimizations. Therefore, in this case, the robustness is just as important as overall performance.

Crucially, participant and conference threads yield frequently. They simply cannot make progress without data. Yielding threads have to be placed in an external global structure (§4.5.4), here a multi queue. This seems suboptimal because locality is not respected, and centralization of tasks is rarely the desired state. Therefore, on top of FIFO, LIFO and Staged, one more approach has been evaluated. It is a composition of 10 individual schedulers (FIFO or LIFO) in order to silo particular sets of conferences and participant threads. This imposes boundaries on stealing and yielding since whatever happens the task remains in its silo. Akin, in spirit, to neighbourhood stealing (§5.3.1).

Figure 5.11 shows the ratio of latency breaches of different schedulers over increasing number of conferences. This metric behaves similar tail latencies. LIFO performs the best as it is able to capitalize on mixed down audio by immediately sending data back. FIFO sees significant number of fails early, what is explained by the (effectively) constant depth of task graph (§4.3.1). Staged is unable to capitalize on having multiple stages

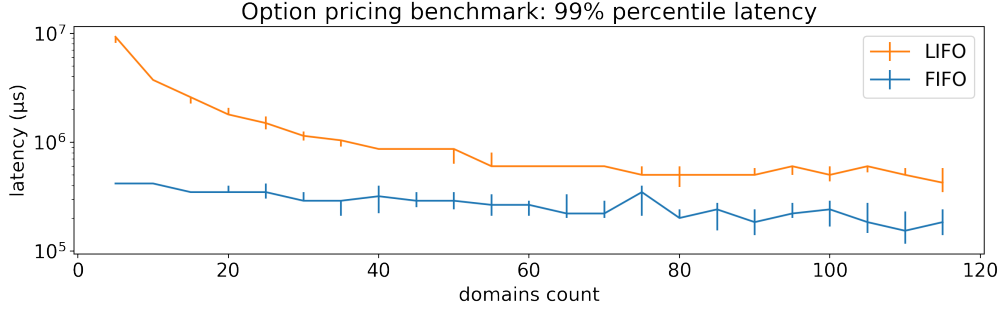


Figure 5.12: The number of latency breaches achieved by different schedulers as the load (number of mixers) increases.

because even in the base FIFO and LIFO case, most of the scheduling goes through the global multiqueue. In such a case, Staged only has extra overheads and adds another queue before sending out mixed down audio, where it is not beneficial to prioritise other work. Curiously, siloed FIFO and LIFO did not differ significantly from core implementations. Locality benefits are not big enough to make a difference in this case. It is also a point of in favour of scalability of the base designs. Notably all schedulers have been fitted with highly-scalable multi-queue for overflow queue. A scheduler with lock-based overflow structure (as in the case of §3.1, §3.2) does not terminate in reasonable time.

Finally, this result shows that even when optimizing for latency, there is no ready-made solutions. In the case of server benchmark with multi-modal packet processing latency (§5.3.3), the best latency was offered by Staged architecture and the worst by LIFO. The mixer benchmark is exactly opposite: LIFO is the best choice for latency and Staged the worst. This phenomenon is another example of the hypothesis (§1.1).

## 5.5 Binomial option pricer benchmark

Option contracts are financial derivative instruments, which provide the holder with an option (but not an obligation) to enter a specific trade a some point in the future. For example, a european call option on Google with strike price of \$2600 and expiration date of 2022-03-06 lets the holder buy a specified number of Google shares at the strike price and on the expiration date [34]. Value of an option at expiration depends strictly on the the price of its underlying.

$$\text{call option value} = \max(\text{underlying price} - \text{strike price}, 0).$$

Binomial model first estimates value of the underlying by assuming that at each time step the price may either move up by a specific amount or down by the inverse, at specific probabilities. This creates a lattice, whose leaves represent prices at some final timestep. Then, the binomial pricer calculates payoff at every final timestep and propagates the expected value backwards using known probabilities of up and down moves. The first stage - stock prices at final timestep - can be computed analytically, while propagation



of expectation must actually walk the lattice to take interest rates into account. Despite its simplicity, the binomial model is widely used in practise as it is far easier to include extra features than in the analytical approaches.

In contrast with other asset classes, computation of option prices is computationally intensive due to sheer number of instruments. For example, NYSE has 2400 stock listings. Each of them has hundreds of options at different strike prices, expiration dates and contract terms. Thus, the benchmark recomputes values of 30k options. Different instruments have varying importance to a trading company due to the size of current position, available infrastructure, other models, etc. Thus, most instruments are computed at a fairly low accuracy (20 timesteps) and 0.5% at high accuracy (2k steps), what results in a bimodal distribution of work per instrument. Pricing of each option is not parallelized but each timestep of the lattice is computed concurrently to avoid long stalls.

- Despite an overall comparable throughput the latency is much better with FIFO
- The latency improves because more workers mean that queues are overall shorter, which means fewer items are stuck behind heavy tasks. Moreover, there is more stealing and the stealing always takes the oldest elements.
- Increasing the number of spawners is tragic for latency on LIFO as that leads to more tasks being processed locally and in a more LIFO manner.

Figure 5.12 shows the tail latency under FIFO and LIFO scheduling. Even though that the overall throughput is comparable, the latency is significantly better with LIFO. It follows from FIFO processing each task-tree concurrently and taking advantage of terminating the shallow ones early. This benchmark may seem specific and inconsequential to most workloads, yet this is what commonly occurs in complex systems. Distribution of tasks is not just bimodal but multimodal, and thus impact all range of tail latencies. In fact, in the limit, if some subtrees have infinite depth, other tasks may never get to run with LIFO. Practical runtime or systems schedulers typically follow FIFO to avoid starvation in heterogeneous workloads.

As more workers are added, the tail latency improves. Predominantly because the overall queue sizes are shorter (fewer items are stuck behind heavy tasks) and stealing always takes the oldest elements. However, as soon as more spawners are added, the spawning becomes more efficient and processing more local, what collapses the tail latency even with many domains.

Notably, this results contrasts with the other complex benchmark (§5.4), where LIFO scheduler is the way to optimize latency. Furthermore, in the previous cases (e.g. §5.6) increasing the number of spawner improved both throughput and latency since it helped to loose the bottleneck in work distribution. In the case of options pricer benchmark, adding more spawner does improve throughput but hurts tail latency as queues grows and processing becomes more LIFO. Again, these phenomena show importance of customized

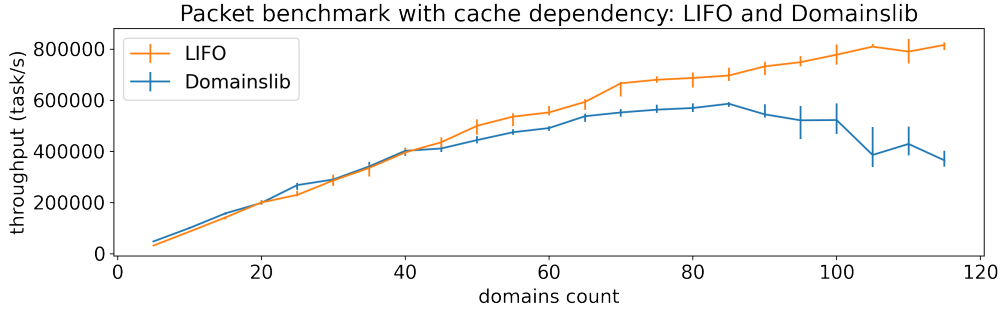


Figure 5.13: T 100k packets with cache dependency / just 1 spawner.

scheduling (§1.1).

## 5.6 Comparison with Domainslib

Note, the LIFO in this section refers to the LIFO scheduler developed for this project. Domainslib (which is also locally-LIFO) is referred to explicitly.

The performance with Domainslib has been compared using the last packet processing benchmark (5.3.3) as it features bimodal work distribution, cache effects and is assessed using a simple metric - throughput. Further, to highlight the effect, the benchmark was run with just 1 spawner. It is worth pointing out that Domainslib is able to auto-resize, while for LIFO, although possible, auto-resizing has not been implemented. To account for that difference both Domainslib and LIFO were set to have a minimum size of 1024 items, which Domainslib can expand further if needed.

The results are shown in figure 5.13. The Domainslib is significantly faster at low core counts. Between 20 and 40 cores the benefit vanishes and both implementations offer a similar performance. Beyond 40 cores the Domainslib is unable to scale. It peaks at 85 cores and sees a very strong reduction in throughput for more cores. The LIFO implementation, on the other hand, sees diminishing results but continues to improve all the way to the last data point at 115 cores.

The detailed analysis is beyond the scope of this section but the three key factors are described below.

- **Optimization.** The constant factors of Domainslib are more optimized. It requires only a single atomic operation per insertion or removal. Moreover, Domainslib uses a number of advanced techniques that are possible to apply to the LIFO implementation, but are risky to use in the early development, e.g. *Obj.magic*, forcing inlining, unsafe array operations. This aligns with figure 5.13 showing a constant improvement over LIFO in the 5-15 range.
- **Scalability.** Domainslib only steals a single element at the time, while LIFO steals half of the items. LIFO only needs a single index operation per steal, thus the

actual number of atomic operations per steal of both implementations converges as more elements are stolen. But all Domainslib's atomic operations focus on the *head* index. LIFO, on the other hand, in proportion uses the *head* less and less. Furthermore, stealing multiple elements creates a load-balancing network, where elements propagate through multiple hops and ease stress on the spawner. With the steal-one policy, load on the spawner increases linearly with the number of domains.

# Chapter 6

## Summary and conclusions

This dissertation focused on exploring the trade-offs between various real-world scheduling policies. The core of the work included lock-free FIFO and LIFO schedulers, which were then composed (§4.4.2) and extended in various ways (§5.3.3, §5.3.1). The evaluation involved a number of real-world benchmarks and common industrial metrics: throughput and tail latency (§4.1).

The results highlight vast differences between different scheduling policies. There is no single optimal scheduling policy. Instead, it depends on the workload, available parallelism and optimized metrics. Other findings include:

- The selection of optimal policy is non-trivial. For example, there is no clear-cut answer for whether FIFO, LIFO or other is better for latency. It depends on the interaction of a number of factors such as shape of the task-graph and locality benefits. In one case, staged architecture was shown to improve above both (§5.4, §5.3.3).
- Scaling fine-grained workloads to high-core machines is non-trivial. In fact, an algorithm that scales well up to 20 cores can in fact lose performance as more resources are added due to contention (§5.3.1).
- Locked overflow queue used in Go and Tokio limits performance of some workloads (§5.3.1, §5.4).
- The default OCaml scheduler - Domainlib [66] - has room for improvement on high core counts (§5.6). Notably, the OCaml team has already expressed interest in upstreaming some of the lock-free data structures developed for this project.

Therefore, there are clear benefits from customization and composition of schedulers for the workload and the desired metrics. Thus, the hypothesis stated in the introduction is accepted (§1.1).

Finally, OCaml multicore improves over classic runtime-integrated scheduling. Histori-

cally, built-in scheduling necessitated FIFO ordering to avoid the bad latency characteristics of LIFO under very heterogenous workloads. LIFO is, nonetheless, the optimal choice for many homogenous workloads (e.g. §5.11). The newfound capabilities of OCaml let developers tap into the benefits of using custom scheduling policies. In the case of mixer benchmark, the homogenous workload can be isolated on LIFO scheduler, while other diverse work remains on traditional FIFO discipline.

Future work can take a number of different directions, e.g.: further optimization of task distribution mechanisms (e.g. horizontal scaling of local structures), integration of effects and preemption, and exploration of viable lock-free structures (inc. development of DSCheck 2.2.5).

# Bibliography

- [1] 2nd gen amd epyc™ 7702 — server processor — amd. <https://www.amd.com/en/products/cpu/amd-epyc-7702>. (Accessed on 05/17/2022).
- [2] Amd announces industry's first x86 dual-core processor. <https://phys.org/news/2004-08-amd-industry-x86-dual-core-processor.html>. (Accessed on 05/17/2022).
- [3] How web browsers use processes and threads — by jeewantha lahiru — level up coding. <https://levelup.gitconnected.com/how-web-browsers-use-processes-and-threads-9f8f8fa23371>. (Accessed on 05/17/2022).
- [4] Ocaml library : List. <https://v2.ocaml.org/api/List.html>. (Accessed on 05/18/2022).
- [5] Power4 - wikipedia. [https://en.wikipedia.org/wiki/POWER4#cite\\_ref-stallings\\_2-0](https://en.wikipedia.org/wiki/POWER4#cite_ref-stallings_2-0). (Accessed on 05/17/2022).
- [6] Opam documentation. URL [https://ocaml.janestreet.com/ocaml-core/111.28.00/doc/async\\_kernel/#Pipe](https://ocaml.janestreet.com/ocaml-core/111.28.00/doc/async_kernel/#Pipe).
- [7] The Go Programming Language. URL <https://go.dev/>.
- [8] Goroutines vs os threads. URL <https://groups.google.com/g/golang-nuts/c/j51G7ieoKh4/m/wxNaKkFEfvcJ>.
- [9] LKML: Steve Sistare: [PATCH v3 00/10] steal tasks to improve CPU utilization. URL <https://lkml.org/lkml/2018/11/9/1173>.
- [10] memory\_order - cppreference.com. [https://en.cppreference.com/w/c/atomic/memory\\_order](https://en.cppreference.com/w/c/atomic/memory_order). (Accessed on 05/18/2022).
- [11] Transmission Control Protocol. RFC 793, Sept. 1981. URL <https://www.rfc-editor.org/info/rfc793>.
- [12] I. Advanced Micro Devices. 2nd gen amd epyc™ 7702 — server processor — amd. <https://www.amd.com/en/products/cpu/amd-epyc-7702>. (Accessed on 06/08/2022).

- [13] C. Agastya, D. Mechanic, and N. Kothari. Mouth-to-ear latency in popular voip clients. 2009. doi: 10.7916/D80Z794V. URL <https://academiccommons.columbia.edu/doi/10.7916/D80Z794V>.
- [14] J. Alglave, L. Maranget, S. Sarkar, and P. Sewell. Litmus: Running tests against hardware. In P. A. Abdulla and K. R. M. Leino, editors, *Tools and Algorithms for the Construction and Analysis of Systems*, pages 41–44, Berlin, Heidelberg, 2011. Springer Berlin Heidelberg. ISBN 978-3-642-19835-9.
- [15] R. Amin, F. Jackson, J. E. Gilbert, J. Martin, and T. Shaw. Assessing the impact of latency and jitter on the perceived quality of call of duty modern warfare 2. In *Human-Computer Interaction. Users and Contexts of Use*, pages 97–106. Springer Berlin Heidelberg, 2013. doi: 10.1007/978-3-642-39265-8\_11. URL [https://doi.org/10.1007/978-3-642-39265-8\\_11](https://doi.org/10.1007/978-3-642-39265-8_11).
- [16] R. D. Blumofe and C. E. Leiserson. Scheduling multithreaded computations by work stealing. *Journal of the ACM*, 46(5):720–748, Sept. 1999. doi: 10.1145/324133.324234. URL <https://doi.org/10.1145/324133.324234>.
- [17] R. D. Blumofe, C. F. Joerg, B. C. Kuszmaul, C. E. Leiserson, K. H. Randall, and Y. Zhou. Cilk. In *Proceedings of the fifth ACM SIGPLAN symposium on Principles and practice of parallel programming - PPOPP '95*. ACM Press, 1995. doi: 10.1145/209936.209958. URL <https://doi.org/10.1145/209936.209958>.
- [18] M. Bohr. A 30 year retrospective on dennard's MOSFET scaling paper. *IEEE Solid-State Circuits Newsletter*, 12(1):11–13, 2007. doi: 10.1109/n-ssc.2007.4785534. URL <https://doi.org/10.1109/n-ssc.2007.4785534>.
- [19] J. I. Brachthäuser, P. Schuster, and K. Ostermann. Effects as capabilities: effect handlers and lightweight effect polymorphism. *Proceedings of the ACM on Programming Languages*, 4(OOPSLA):1–30, Nov. 2020. doi: 10.1145/3428194. URL <https://doi.org/10.1145/3428194>.
- [20] A. Castello, A. J. Pena, S. Seo, R. Mayo, P. Balaji, and E. S. Quintana-Orti. A review of lightweight thread approaches for high performance computing. In *2016 IEEE International Conference on Cluster Computing (CLUSTER)*. IEEE, Sept. 2016. doi: 10.1109/cluster.2016.12. URL <https://doi.org/10.1109/cluster.2016.12>.
- [21] D. Chase and Y. Lev. Dynamic circular work-stealing deque. In *Proceedings of the 17th annual ACM symposium on Parallelism in algorithms and architectures - SPAA'05*. ACM Press, 2005. doi: 10.1145/1073970.1073974. URL <https://doi.org/10.1145/1073970.1073974>.
- [22] chebyrash. chebyrash/promise: Promise / future library for go. <https://github.com/chebyrash/promise>, 2022. (Accessed on 06/01/2022).

- [23] D. Cheng, Y. Chen, X. Zhou, D. Gmach, and D. Milojicic. Adaptive scheduling of parallel jobs in spark streaming. In *IEEE INFOCOM 2017 - IEEE Conference on Computer Communications*, pages 1–9, 2017. doi: 10.1109/INFOCOM.2017.8057206.
- [24] O. Contributors. ocaml-multicore/ocaml-multicore at 4.12+domains. <https://github.com/ocaml-multicore/ocaml-multicore/tree/4.12+domains>. (Accessed on 06/06/2022).
- [25] J. Corbet. The right way to yield [lwn.net]. <https://lwn.net/Articles/31462/>. (Accessed on 06/08/2022).
- [26] A. Drebes, A. Pop, K. Heydemann, N. Drach, and A. Cohen. NUMA-aware scheduling and memory allocation for data-flow task-parallel applications. In *Proceedings of the 21st ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming*. ACM, Feb. 2016. doi: 10.1145/2851141.2851193. URL <https://doi.org/10.1145/2851141.2851193>.
- [27] H. Esmailzadeh, E. Blem, R. S. Amant, K. Sankaralingam, and D. Burger. Dark silicon and the end of multicore scaling. In *2011 38th Annual International Symposium on Computer Architecture (ISCA)*, pages 365–376, 2011.
- [28] H. Esmailzadeh, E. Blem, R. S. Amant, K. Sankaralingam, and D. Burger. Power challenges may end the multicore era. *Commun. ACM*, 56(2):93–102, feb 2013. ISSN 0001-0782. doi: 10.1145/2408776.2408797. URL <https://doi.org/10.1145/2408776.2408797>.
- [29] M. Faverge and P. Ramet. A NUMA Aware Scheduler for a Parallel Sparse Direct Solver. In *Workshop on Massively Multiprocessor and Multicore Computers*, page 5p., Rocquencourt, France, Feb. 2009. INRIA. URL <https://hal.inria.fr/inria-00416502>.
- [30] D. Geer. Chip makers turn to multicore processors. *Computer*, 38(5):11–13, May 2005. doi: 10.1109/mc.2005.160. URL <https://doi.org/10.1109/mc.2005.160>.
- [31] HashiCorp. Nomad by hashicorp. <https://www.nomadproject.io/>. (Accessed on 06/01/2022).
- [32] M. Herlihy, N. Shavit, V. Luchangco, and M. Spear. Chapter 1 - introduction. In M. Herlihy, N. Shavit, V. Luchangco, and M. Spear, editors, *The Art of Multiprocessor Programming (Second Edition)*, pages 1–18. Morgan Kaufmann, Boston, second edition edition, 2021. ISBN 978-0-12-415950-1. doi: <https://doi.org/10.1016/B978-0-12-415950-1.00009-4>. URL <https://www.sciencedirect.com/science/article/pii/B9780124159501000094>.
- [33] Y. Ho and D. Pepyne. Simple explanation of the no-free-lunch theorem and its implications. *Journal of Optimization Theory and Applications*, 115(3):549–570,



- Dec. 2002. doi: 10.1023/a:1021251113462. URL <https://doi.org/10.1023/a:1021251113462>.
- [34] J. Hull. *Options, Futures And Other Derivatives*. Pearson College Div, hardcover edition, 6 2005. ISBN 978-0131499089. URL <https://lead.to/amazon/com/?op=bt&la=en&cu=usd&key=0131499084>.
- [35] C. Igel and M. Toussaint. A no-free-lunch theorem for non-uniform distributions of target functions. *Journal of Mathematical Modelling and Algorithms*, 3(4):313–322, Jan. 2005. doi: 10.1007/s10852-005-2586-y. URL <https://doi.org/10.1007/s10852-005-2586-y>.
- [36] S. Jaffer. `sadiqj/dscheck`. <https://github.com/sadiqj/dscheck>. (Accessed on 06/01/2022).
- [37] G. Kappes and S. V. Anastasiadis. A lock-free relaxed concurrent queue for fast work distribution. In *Proceedings of the 26th ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming*. ACM, Feb. 2021. doi: 10.1145/3437801.3441583. URL <https://doi.org/10.1145/3437801.3441583>.
- [38] R. K. Karmani, G. Agha, M. S. Squillante, J. Seiferas, M. Brezina, J. Hu, R. Tuminaro, P. Sanders, J. L. Träffe, R. A. Geijn, J. L. Träff, R. A. Geijn, M. B. Sander, J. L. Gustafson, R. O. Dror, C. Young, D. E. Shaw, C. Lin, J.-K. Lee, R.-G. Chang, C.-B. Kuan, G. Kollias, A. Y. Grama, Z. Li, R. C. Whaley, and R. W. Vuduc. Amdahl’s law. In *Encyclopedia of Parallel Computing*, pages 53–60. Springer US, 2011. doi: 10.1007/978-0-387-09766-4\_77. URL [https://doi.org/10.1007/978-0-387-09766-4\\_77](https://doi.org/10.1007/978-0-387-09766-4_77).
- [39] M. Kerrisk. `vdso(7)` - linux manual page. <https://man7.org/linux/man-pages/man7/vdso.7.html>. (Accessed on 06/06/2022).
- [40] H. N. Khan, D. A. Hounshell, and E. R. H. Fuchs. Science and research policy at the end of moore’s law. *Nature Electronics*, 1(1):14–21, Jan. 2018. doi: 10.1038/s41928-017-0005-9. URL <https://doi.org/10.1038/s41928-017-0005-9>.
- [41] C. M. Kirsch, M. Lippautz, and H. Payer. Fast and scalable, lock-free k-FIFO queues. In *Lecture Notes in Computer Science*, pages 208–223. Springer Berlin Heidelberg, 2013. doi: 10.1007/978-3-642-39958-9\_18. URL [https://doi.org/10.1007/978-3-642-39958-9\\_18](https://doi.org/10.1007/978-3-642-39958-9_18).
- [42] M. Kleppmann. *Designing data-intensive applications*. O’Reilly Media, Sebastopol, CA, Mar. 2017.
- [43] N. M. Lê, A. Pop, A. Cohen, and F. Zappa Nardelli. Correct and efficient work-stealing for weak memory models. *SIGPLAN Not.*, 48(8):69–80, feb 2013. ISSN 0362-1340. doi: 10.1145/2517327.2442524. URL <https://doi.org/10.1145/2517327.2442524>.

- [44] C. Lerche. Making the tokio scheduler 10x faster — tokio - an asynchronous rust runtime. <https://tokio.rs/blog/2019-10-scheduler>, . (Accessed on 06/01/2022).
- [45] C. Lerche. Tokio - an asynchronous rust runtime. <https://tokio.rs/>, . (Accessed on 06/01/2022).
- [46] G. Maruzzelli. *FreeSWITCH 1.8*. Packt Publishing, Birmingham, England, July 2017.
- [47] S. McClure, A. Ousterhout, S. Shenker, and S. Ratnasamy. Efficient scheduling policies for Microsecond-Scale tasks. In *19th USENIX Symposium on Networked Systems Design and Implementation (NSDI 22)*, pages 1–18, Renton, WA, Apr. 2022. USENIX Association. ISBN 978-1-939133-27-4. URL <https://www.usenix.org/conference/nsdi22/presentation/mcclure>.
- [48] R. B. Miller. Response time in man-computer conversational transactions. In *Proceedings of the December 9-11, 1968, fall joint computer conference, part I on - AFIPS '68 (Fall, part I)*. ACM Press, 1968. doi: 10.1145/1476589.1476628. URL <https://doi.org/10.1145/1476589.1476628>.
- [49] B. Modelski. bartoszmmodelski/ebsl. <https://github.com/bartoszmmodelski/ebsl>. (Accessed on 06/01/2022).
- [50] G. E. Moore. Cramming more components onto integrated circuits, reprinted from electronics, volume 38, number 8, april 19, 1965, pp.114 ff. *IEEE Solid-State Circuits Society Newsletter*, 11(3):33–35, Sept. 2006. doi: 10.1109/n-ssc.2006.4785860. URL <https://doi.org/10.1109/n-ssc.2006.4785860>.
- [51] B. Norris and B. Demsky. CDSchecker. *ACM SIGPLAN Notices*, 48(10):131–150, Nov. 2013. doi: 10.1145/2544173.2509514. URL <https://doi.org/10.1145/2544173.2509514>.
- [52] M. L. Pinedo. *Scheduling*. Springer US, 2012. doi: 10.1007/978-1-4614-2361-4. URL <https://doi.org/10.1007/978-1-4614-2361-4>.
- [53] A. Postnikova, N. Koval, G. Nadiradze, and D. Alistarh. Multi-queues can be state-of-the-art priority schedulers. In *Proceedings of the 27th ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming*. ACM, Mar. 2022. doi: 10.1145/3503221.3508432. URL <https://doi.org/10.1145/3503221.3508432>.
- [54] M. Pretnar. An introduction to algebraic effects and handlers. invited tutorial paper. *Electronic Notes in Theoretical Computer Science*, 319:19–35, Dec. 2015. doi: 10.1016/j.entcs.2015.12.003. URL <https://doi.org/10.1016/j.entcs.2015.12.003>.
- [55] K. Rupp. karlrupp/microprocessor-trend-data: Data repository for my blog series on microprocessor trend data. <https://github.com/karlrupp/microprocessor-trend-data>. (Accessed on 06/08/2022).

- [56] H. Schweizer, M. Besta, and T. Hoefler. Evaluating the cost of atomic operations on modern architectures. 2020. doi: 10.48550/ARXIV.2010.09852. URL <https://arxiv.org/abs/2010.09852>.
- [57] K. Sivaramakrishnan, S. Dolan, L. White, S. Jaffer, T. Kelly, A. Sahoo, S. Parimala, A. Dhiman, and A. Madhavapeddy. Retrofitting parallelism onto OCaml. *Proceedings of the ACM on Programming Languages*, 4(ICFP):1–30, Aug. 2020. doi: 10.1145/3408995. URL <https://doi.org/10.1145/3408995>.
- [58] K. Sivaramakrishnan, S. Dolan, L. White, T. Kelly, S. Jaffer, and A. Madhavapeddy. Retrofitting effect handlers onto OCaml. In *Proceedings of the 42nd ACM SIGPLAN International Conference on Programming Language Design and Implementation*. ACM, June 2021. doi: 10.1145/3453483.3454039. URL <https://doi.org/10.1145/3453483.3454039>.
- [59] D. Stephen, W. Leo, S. KC, Y. Jeremy, and M. Anil. Effective Concurrency through Algebraic Effects, 2015.
- [60] D. Sun, H. He, H. Yan, S. Gao, X. Liu, and X. Zheng. Lr-stream: Using latency and resource aware scheduling to improve latency and throughput for streaming applications. *Future Generation Computer Systems*, 114:243–258, Jan. 2021. doi: 10.1016/j.future.2020.08.003. URL <https://doi.org/10.1016/j.future.2020.08.003>.
- [61] H. Sundell, A. Gidenstam, M. Papatrifiantafilou, and P. Tsigas. A lock-free algorithm for concurrent bags. In *Proceedings of the 23rd ACM symposium on Parallelism in algorithms and architectures - SPAA '11*. ACM Press, 2011. doi: 10.1145/1989493.1989550. URL <https://doi.org/10.1145/1989493.1989550>.
- [62] J. Swalens, S. Marr, J. D. Koster, and T. V. Cutsem. Towards composable concurrency abstractions. *Electronic Proceedings in Theoretical Computer Science*, 155:54–60, June 2014. doi: 10.4204/eptcs.155.8. URL <https://doi.org/10.4204/eptcs.155.8>.
- [63] G. Team. go/proc.go at master · golang/go. <https://github.com/golang/go/blob/master/src/runtime/proc.go#L2597>, 2022. (Accessed on 06/01/2022).
- [64] O. Team. Concurrency and parallelism design notes · ocaml-multicore/ocaml-multicore wiki. <https://github.com/ocaml-multicore/ocaml-multicore/wiki/Concurrency-and-parallelism-design-notes>, . (Accessed on 06/08/2022).
- [65] O. Team. ocaml-multicore/eio: Effects-based direct-style io for multicore ocaml. <https://github.com/ocaml-multicore/eio>, . (Accessed on 06/08/2022).
- [66] O.-M. Team. ocaml-multicore/domainslib: Parallel programming over domains. <https://github.com/ocaml-multicore/domainslib>, . (Accessed on 06/01/2022).
- [67] G. Technology. Md90-fs0 (rev. 1.1) — server motherboard - gigabyte u.k. <https://www.gigabyte.com/Server/Motherboards/Md90-FS0>.

[//www.gigabyte.com/uk/Enterprise/Server-Motherboard/MD90-FS0-rev-11](http://www.gigabyte.com/uk/Enterprise/Server-Motherboard/MD90-FS0-rev-11).  
(Accessed on 06/08/2022).

- [68] D. L. Tennenhouse. Layered multiplexing considered harmful. In *In First International Workshop on High Speed Networking*, 1989.
- [69] Twilio. Frequently asked questions — twilio. <https://www.twilio.com/docs/stun-turn/faq>. (Accessed on 05/30/2022).
- [70] M. Welsh, D. Culler, and E. Brewer. SEDA. *ACM SIGOPS Operating Systems Review*, 35(5):230–243, Dec. 2001. doi: 10.1145/502059.502057. URL <https://doi.org/10.1145/502059.502057>.
- [71] D. Wolpert and W. Macready. No free lunch theorems for optimization. *IEEE Transactions on Evolutionary Computation*, 1(1):67–82, Apr. 1997. doi: 10.1109/4235.585893. URL <https://doi.org/10.1109/4235.585893>.
- [72] C. Yang and J. Mellor-Crummey. A wait-free queue as fast as fetch-and-add. In *Proceedings of the 21st ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming*. ACM, Feb. 2016. doi: 10.1145/2851141.2851168. URL <https://doi.org/10.1145/2851141.2851168>.
- [73] Q. Zhang, D. Feng, F. Wang, and Y. Xie. An interposed i/o scheduling framework for latency and throughput guarantees. *Journal of Applied Science and Engineering*, 17: 193–202, June 2014. ISSN 1560-6686. doi: 10.6180/jase.2014.17.2.10. URL <https://doi.org/10.6180/jase.2014.17.2.10>.

# Appendix A

## Rejected design choices

This section expands on §4.5 and discusses features, which did not make it into the implementation.

### A.1 Backpressure

One of the primary use cases that a scheduler needs to handle is akin to the producer-consumer pattern. For example, in the case of packet processing benchmark (4.2.1), one long-lived task listens to network traffic and spawns a task per request to be picked up by other threads for execution. A common problem in systems organized in that way occurs when producer is faster than consumers, as this fills up queues. This increases mean latency and impedes responsiveness. Moreover, it effectively slows down the system due to intense memory pressure and adverse cache effects.

This can be solved using backpressure, which assumes either providing producers with an explicit signal to slow down or even blocking the producers until consumer catches up. Both approaches have proven successful in e.g. TCP flow control [11] or Async’s pipe implementation [6].

Implementing backpressure in the core of the scheduler would indisputably improve some use-cases, even distributed ones. E.g. assume an operation of reading a file, sending it over network, and writing to disk. If the last step is the bottleneck, backpressure can be propagated from writing to disk to the network stack. Then with TCP, all the way to the sender, who can now allocate less resources to this operation. Nonetheless, this feature has not been implemented as coming up with a robust backpressure mechanism for a scheduler, which needs to support very diverse workloads and has local data structures, is non-trivial. It could be added as an optional feature, as is the case in the mentioned pipe implementation [6].

## A.2 Cancellations

Cancellations are common in modern workloads. It is a function of more thought being given to latency but also, a convenient programming model. Program may want to do multiple requests and only process the first one to return, or at some point determine that other results are not needed. Further, cancellations are critical to recovering from an overload. Their implementation has to be particularly robust since a large wave of propagating cancellations may be the actual event to tip a system over the edge and cause meltdown. While this problem has not been considered explicitly in this work, all the core lock-free implementations behind schedulers (§5.1) allow out-of-order eviction of cancelled items, what immediately helps reduce memory footprint.

## A.3 Blocking operations

As discussed (§4.5.1), developer should strive to create lightweight, fine-grained tasks for a non-preemptive scheduler to avoid latency issues. In some cases, however, it may not be possible. A long system-call may not have an alternative, non-blocking API. While blocking is unavoidable, scheduler may still offer some support.

- One approach is to simply let user manually specify long-running operations and have them scheduled on a new system thread (§3.2). Such approach suffers from the same disadvantage as Async/Lwt, as in developer will occasionally fail to annotate a function.
- Alternatively, scheduler may feature a monitoring thread, which automatically adds new threads to the pool whenever there is a risk of starvation.

This has not been explicitly handled in the current implementation but both approaches are feasible. There is a question of how important is such a measure, given that the proposed IO library (eio [65]) tries to use the fast, non-blocking interfaces like `io_uring`.

## A.4 Dynamic pool size

## A.5 Batching

# Appendix B

## Core data structures

This section describes the underlying lock-free structures.

### B.1 MPMC queue on infinite array

As the first step, it is crucial to understand the main idea behind any multi-producer or multi-consumer FAD-based queue. A particularly intuitive exposition is offered by [72] (which takes a wait-free direction afterwards). The key simplification is to assume that said queue is built on top of an array of infinite length.

Functions operating on such an array are presented in listing B.1 and B.2. The enqueueuer simply increases the *tail* index by one using an atomic operation, receives the index of its slot in return and stores the enqueued element there. Since the queue is infinite, every slot is only used once. Likewise, the dequeuer begins by increasing the *head* index and receives the index of its slot in return. However, since there is no synchronisation with the tail, the actual slot can still be empty. Either because head overshoot the tail or enqueueuer is simply slow to finish operation. Thus, dequeue needs to make sure that the slot actually contains an element and once that's established, it returns the element.

With such a setup both enqueue and dequeue need do two atomic operations (FAD+GET/SET) but the contention is resolved at the *head*, *tail* indices with FAD. Thus, there is never any need to retry in the hotspots. Once past the FAD, each dequeuer is essentially matched up with an enqueueuer and a slot to pass the information through. Accesses to the slot need to

```
let enqueue {array; tail; _} element =  
  let index = Atomic.fetch_and_add tail 1 in  
  let cell = Array.get array index in  
  Atomic.set cell (Some element)  
;;
```

Figure B.1: Basic enqueue function.

be atomic as enqueueuer and dequeuer may work in parallel. This extra atomic operation is avoidable in some cases and convenient in others (e.g. implementation of cancellations

A.2 or lightweight yielding 4.5.4). Overall, it is a constant factor and does not impact scalability of the structure.

## B.2 MPMC queue blocking

While the infinite array is a powerful idea and some designs actually simulate that using a linked list of arrays (e.g. [61]), use of circular buffer is far more common. Luckily, the design described in the previous section almost works on a circular array. The major difference is that now enqueue wraps around the array and may encounter a full slot, thus enqueue function requires some mechanism to not overwrite another item, e.g. a busy wait akin to that in figure B.2. Secondly, both enqueue and dequeue must use CAS to edit the item slot as the queue can overlap multiple times.

```
let dequeue {array; head; _} element =
  let index = Atomic.fetch_and_add head 1 in
  let cell = Array.get array index in
  while Option.is_none (Atomic.get cell) do ()
  Atomic.get cell
;;
```

Figure B.2: Basic deque function.

Such an implementation constitutes a fully-functional multi-producer multi-consumer unbounded queue as described in [37]. Importantly, such an implementation remains somewhat inconvenient for a modern system. Busy waiting is generally bad for performance. Especially since this busy waiting is not the typical busy waiting in lock-free algorithm, which is expected to take a few tens of cycles. Further, such a structure is no longer strictly FIFO as two overlapping enqueueers or dequeuers are essentially racing on the same slot. Both issues are fixed in the following sections.

## B.3 MPMC queue non-blocking dequeue

Using FAD eliminates the contention in dequeue() but it trades-off consistency. Now, dequeue() may increment the *head* only to discover that it has over-overshot the *tail*. In fact, multiple threads can overshoot at once causing the *head* to be multiple slots higher than *tail*, where the maximum discrepancy is bounded by number of independent processing units. There is a number of possible ways to recover from an overshoot.

- Use CAS to rollback the increment. The CAS operation has to precisely revert the increment done by FAD. The CAS-rollback can fail if there is another dequeuer than incremented the *head* even further. There are two possible cases. Firstly, it is going to try to rollback too and said CAS should be retried. Alternatively, in the meantime enqueueers have appeared and the other thread actually found an item in the higher slot. In such a case the latter thread is not going to rollback but the current slot has a paired enqueueer and should have item shortly.



- Analogically, CAS could be used to increment the *tail* to cover the overshoot. This has the benefit that overshooters free themselves in FIFO order but adds load onto the *tail* index and negatively affects enqueueers in a busy loop.
- Mark slot as burned and return immediately. Then once enqueueer gets assigned that slot, it discovers that dequeuer is no longer active, recycles it by removing the mark and retries the insertion from scratch. Again, this puts extra stress on the enqueueer and requires additional logic to prevent too much of the queue to be marked (as it inflates queue true size).

The implementations in this project rely on the first solution. The third one is useful in the wait-free algorithms as it helps bound maximal number of retries of other methods.

## B.4 MPMC queue non-blocking enqueue

Similar rollback methods apply as to the dequeue. Moreover, items can be queued up as a linked list in a single slot (what may also be used to bring back strict FIFO order) or the enqueueer may simply resize the queue. There is perhaps an additional consideration from the perspective of work-distribution, if the queue is already overloaded, one should not cause additional rollback synchronisation and cause further slowdown (in e.g. passing the tasks to overflow queue).

For the purpose of this dissertation there was no need to use any of these methods. MPMC is only used as the global overflow structure, which generally means that it is not commonly full and at that point putting halt on the spawner is likely better than letting it allocate endless memory. The local work stealing queues, on the other hand, are single-producer, which means that the enqueueer is able to use additional assumptions to avoid the need to rollback.

## B.5 Work stealing queue

In the case of work stealing, there is no need to employ a full MPMC queue. Local queues only have a single enqueueer and that eliminates the possibility of races on the *tail* index. This has two important benefits.

- Any accesses to *tail* index have to be atomic as other functions read *tail* but updates may occur in multiple steps, i.e. atomic get, modification, atomic set. Such updates are cheaper to execute than fully-fledged atomic updates using FAD or CAS.
- The single enqueueer is able to ensure that it is not going to overlap the queue. It can simply check whether there is space in the queue and act accordingly as no other enqueueer can act in between. A natural single-core approach would be to simply check whether *head* index is sufficiently far. But this does not guarantee that

the dequeuer has already removed item from the slot. Thus, an easier and more approach is to simply check whether the slot in  $tail+1$  is empty. If it is, enqueuer can safely place an item and increase the  $tail$ .

That gives rise to a fast local enqueue. It is an ideal choice for a work stealing system, in which threads under heavy load focus entirely on their local work. Likewise, to fully optimize the local queue, it would be nice to have a fast local dequeue. Again, such a local dequeue can utilize the knowledge that no new elements are inserted during its execution. Therefore, it can begin by executing a FAD operation on the  $head$  index and if it has overshoot the  $tail$ , it necessarily has to roll back.

Possible rollback methods have been described in §B.3. None of them is perfect. In particular, rolling back any of the indices may be stuck behind other overshoot dequeuers. Potentially paralyzing the local dequeuer until another thread gets scheduled. Thus, the actual SPMC Queue implementation has two dequeue methods:

- **Local deque.** It takes a single element from the local queue. It is the only method that is allowed to speculate (and possibly overshoot) using FAD. Therefore, whenever local dequeue overshoots, it is guaranteed that no other thread can overshoot further. With such knowledge the rollback is trivial. Local deque simply sets the  $head$  to what is used to be, and terminates. Local deque only needs to synchronize with steal function.
- **Steal.** It attempts to transfer half of the elements of the victim's queue to its local structure. To simplify the local dequeue logic, it is not allowed to speculate on the  $head$  index. Instead steal uses a CAS operation, which guarantees no overshooting as  $tail$  cannot decrease. CAS is naturally slower and more contentious but, importantly, there is only one CAS operation required to take many elements. Therefore, the extra cost of index CAS is in effect very small on per-item basis if queue had a considerable number of items in it. Notably, steal can be called from any thread and needs to synchronize with both local enqueue and local dequeue.

Such a design gives rise to a practical implementation of SPMC structure. It is scalable and follows the philosophy of work stealing by prioritising local operations.

## B.6 Work stealing stack

Assume that a stack with synchronized local functions exists. A natural question to ask is whether the steal should also follow LIFO ordering or just take the elements from the top of the stack (FIFO). The latter has two considerable benefits over LIFO. Firstly, FIFO steal means that stealers contend for the bottom of the stack, while local thread works only the top. Such a setup is very convenient since local and global methods work on different indices, what reduces complexity and contention. Secondly, the alleged benefit

of LIFO is locality. However, a different thread is not going to benefit from that as much as the owner. Thus, instead, it can take the oldest elements in the stack (FIFO) and significantly improve tail latencies.

Fast local push and FIFO steal are already implemented in the previous section (§B.5). The only missing bit is local (FIFO) pop. One way to implement it is akin to the FIFO push, i.e. firstly act on the array and only update the *top* pointer afterwards. With such a procedure, local pop simply tries to remove item from the array. It either succeeds (and updates *top*) or not, and terminates. Stealer, on the other hand, may have its item taken by pop operation, and needs to be able to roll back.

This design is similar to the classic work stealing deque [21]. However, this implementation:

- Steals half of the queue rather than a single element. Stolen elements are transferred directly into the thief's queue rather than returned.
- Local pop does never needs to rollback an index. Instead, it executes CAS on a less contended item slot.
- Has the extra flexibility coming from atomic slots. That helps with implementation of e.g. cancellations (§A.2) and soft yields (§4.5.4).

## B.7 Resizing

The work stealing queue has been extended to allow resizing. It lets the owner of the queue increase the size of the local structure rather than push overflowing elements into a separate queue. This, in theory, may translate to better work distribution. Such a functionality may seem daunting to implement in the lock-free world but, curiously, the implementation of FIFO work stealing lets the owner lock out all other users.

The mechanism relies on the fact that non-owners do not speculate when stealing. Therefore, owner can simply overshoot the *head* index to ensure that no new stealers appear. Once the *head* is higher than *tail*, owner needs to wait for all in-flight stealers to terminate. It can detect it by checking whether all *claimed* elements have been removed from the array. Once that's done, the elements are transferred to a new array, the array and mask are updated in the record, and then *head*, *tail* indices are carefully corrected.

## B.8 Sharded queue

Performance of staged architecture (§4.4.2) and overflow queue (§4.5.2) depends on the scalability of the underlying queue. Lock-free MPMC queue (§B.2) allows enqueueers and dequeuers to work faster and more independently than with a locked structure. However, the throughput of such a queue hardly improves beyond more than a few workers.

A common technique used to extend the scalability in such circumstances is horizontal scaling. Instead of a single queue with two points of contention (indices), the structure can encapsulate multiple queues and load balance across them. Such a construction is called multi queue and can alone constitute a core structure of a scheduler [53]. This dissertation uses a simple multiqueue wherein enqueueers insert items into randomly selected MPMC queue, and dequeuers start with random queue and iterate until a non-empty queue is found (or all queues have been checked).

## B.9 False sharing

A common optimization pattern in low-level programming involves separating contended variables into separate cache lines to ensure they can be owned independently. This was taken into consideration only in the places where it was natural (e.g. size of runqueue). OCaml does not allow to explicitly specify padding or memory layout of a record type. It is possible to force some particular layout by adding unused variables in critical places but that would be quite fragile as compiler updates or changes to the record itself may impact the layout in unpredictable ways. Thus, implementations were not optimized for such subtle behavior.

## B.10 Validation

The core lock-free queue and stack have been extensively load tested and verified using DSCheck (§2.2.5). DSCheck has been modified to put a hard limit on exploration of infinite loops in algorithms requiring busy-waiting. The modified version is vendored in the main repository [49].

## B.11 Limitations

Since this project has a limited timescale, the schedulers were optimized for scalability but their implementations do not eliminate all constant factor overhead. In part since these overheads are not critical to the hypothesis (§1.1) but also because it requires a far more error-prone code in OCaml.