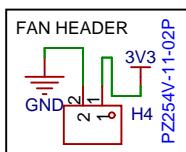
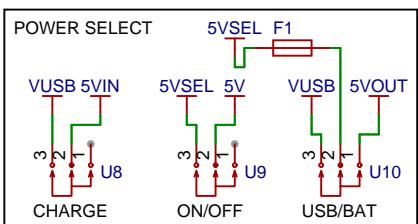
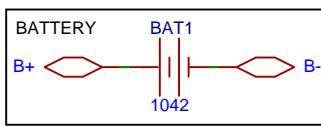
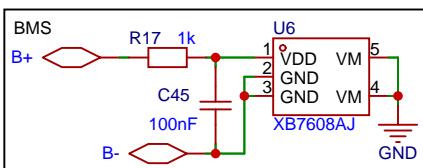
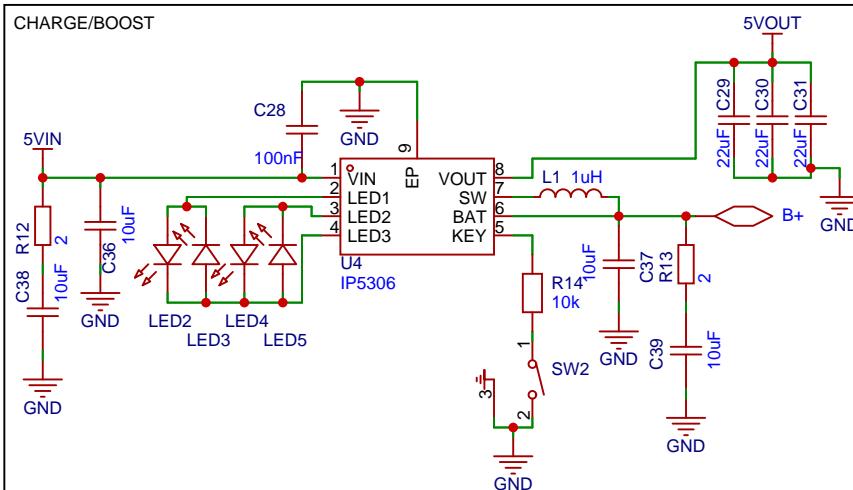
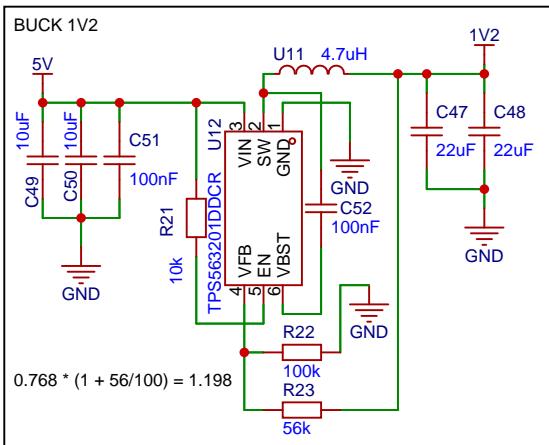
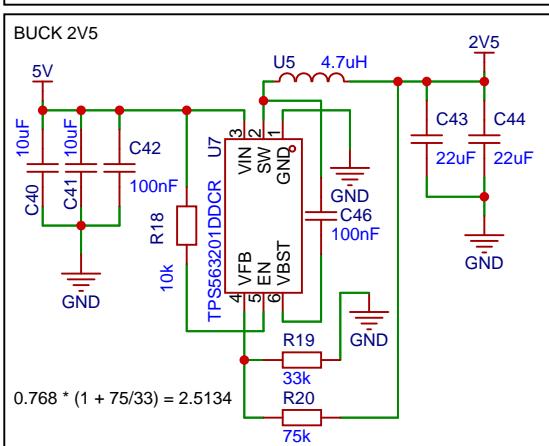
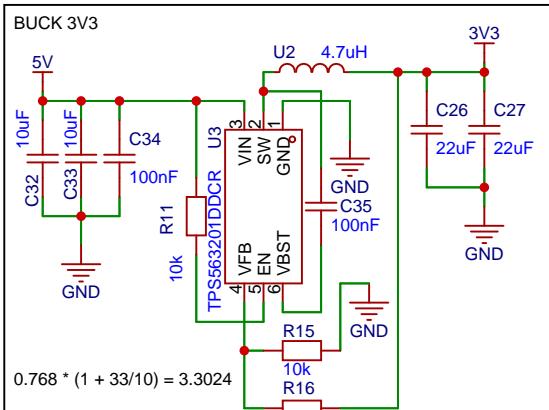
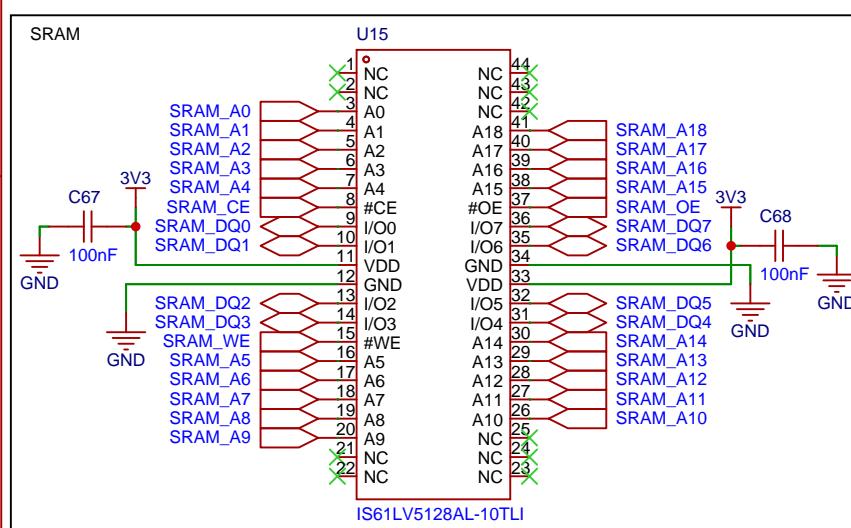
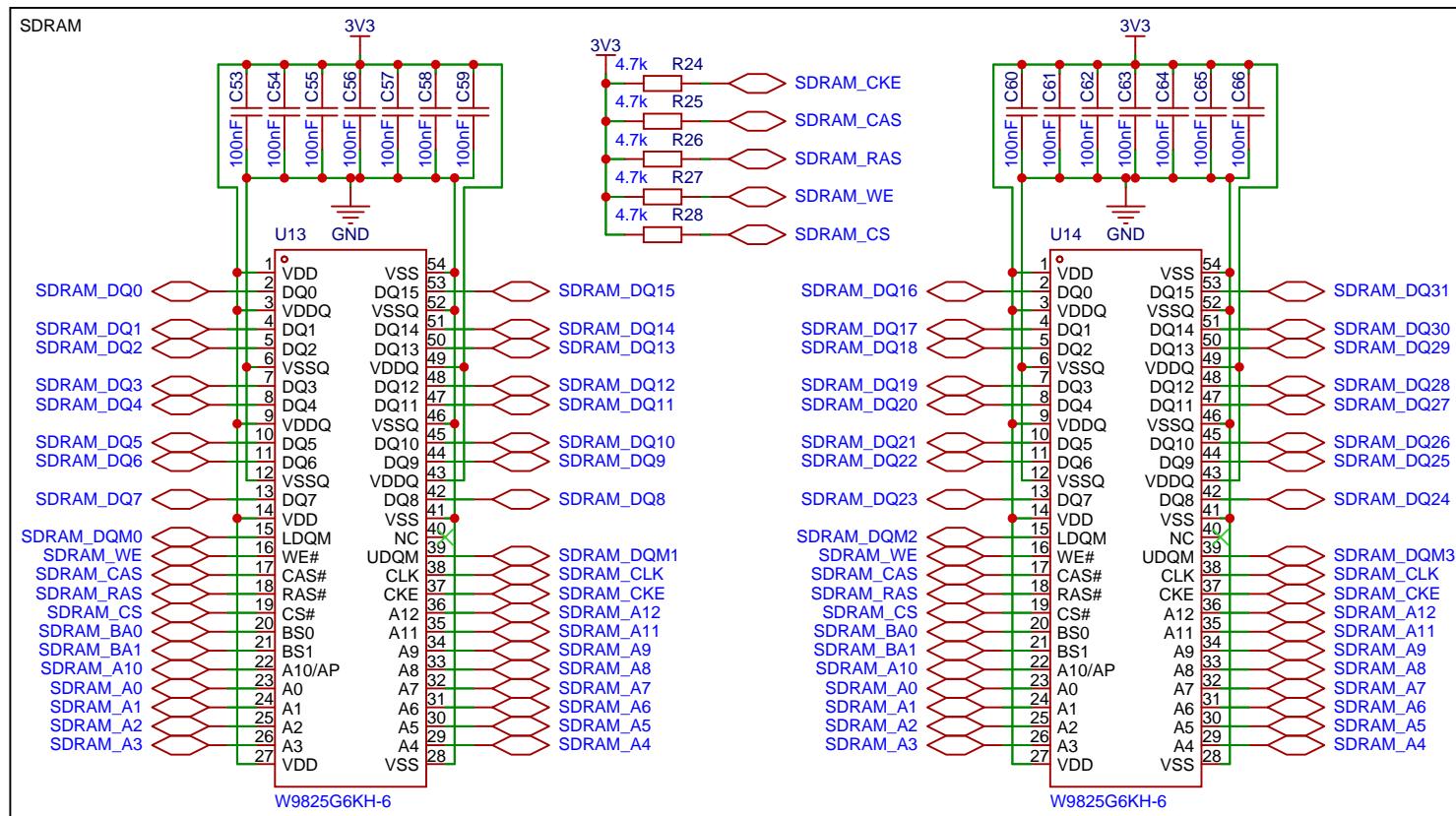


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			Update at	2025-11-24
Board	Board1		Page	FPGA
Drawn		FPGC-CycloneIV-AIO		
Reviewed				
		Version	Size	Page 1 Total 4
 EDA				



Schematic	Schematic1		Create at	2025-10-28
Board	Board1		Update at	2025-11-24
Drawn			Page	Power
Reviewed				
			Version	Size
				Page 2 Total 4
			V1.0	A4
				EasyEDA.com



Schematic	Schematic1		Create at	2025-10-28
Board	Board1		Update at	2025-11-24
Drawn			Page	RAM
Reviewed				
	Version	Size	Page 3 Total 4	
		V1.0	A4	EasyEDA.com

