

CONTACT INFORMATION	<i>Address:</i> Los Angeles, CA, USA <i>E-mail:</i> basel921@gmail.com	<i>WWW:</i> basels.github.io
EDUCATION	<b>University of Southern California;</b> Los Angeles, CA, USA PhD Student, Computer Science <u>Research Interests:</u> Knowledge Graphs, Semantic Web, Linked Open Data, Machine Learning, Information Extraction, Information Technology, Computer Networks  <b>Tel Aviv University;</b> Tel Aviv, Israel BSc, Electrical and Electronic Engineering, <i>magna cum laude</i> (Cumulative GPA: 93.33/100) Major Coursework Areas: Computer-Science, Communications and Electronic Devices Honor's Thesis: " <i>IPoIB Router in the Switch-X Product Family: from Firmware Design to Implementation of the Verification Environment and the Debug Tools</i> " (Supervisor: Mr. Ami Marelli)	<b>Aug 2018 - Present</b>       <b>Feb 2011 - Jan 2015</b>
ACADEMIC EXPERIENCE	<b>Information Sciences Institute;</b> Marina del Rey, CA, USA <b>Research Assistant,</b> <i>Center on Knowledge Graphs</i> <ul style="list-style-type: none"><li>• Working on knowledge graphs and the semantic web with an emphasis on data normalization as a means to solve complex information integration problems</li><li>• Investigating new methodologies to leverage machine learning techniques to establish automatic information extraction and knowledge graph construction</li></ul>	<b>Aug 2018 - Present</b>
PROFESSIONAL EXPERIENCE	<b>Apple;</b> Herzliya, Israel <b>Embedded Firmware Engineer,</b> <i>Flash Storage Software Department</i> <ul style="list-style-type: none"><li>• Designed and developed complex software modules for ultra high performance, real-time embedded systems in a multiprocessor environment for Apple products</li><li>• Defined firmware features and led HW-FW integrations</li><li>• Completed vertical integration with other modules in storage stack (i.e. drivers, file-system)</li></ul> <b>Mellanox Technologies;</b> Tel Aviv, Israel <b>Senior Firmware Engineer &amp; Team Leader,</b> <i>Switch Silicon Core Department</i> <ul style="list-style-type: none"><li>• Managed team of 5 engineers</li><li>• Led the 100GbE Switch Systems firmware development process and software infrastructure activities - an operation involving 16 engineers</li><li>• Developed a distributed functional testing environment; debug tools and performance testing in C++ in Unix for both Ethernet and InfiniBand fabrics in OSI Data-Link and Network layers</li><li>• Implemented complex verification architectures consisting of static and dynamic analysis</li><li>• Delivered technical presentations to 50+ engineers and developers from various teams including: software, hardware, production, and qualification</li><li>• Awarded for excellence and ranked "Superior" (top 5% out of 3000)</li></ul> <b>Team Leader Firmware Engineer,</b> <i>Switch Silicon Core Department</i> <ul style="list-style-type: none"><li>• Managed team of 3 engineers</li><li>• Contributed to end-to-end development and defined version-release procedures for the company's 100GbE Switch products</li><li>• Conducted personal training, code reviews; defined coding-style and methodologies of software engineering for team of 30</li><li>• Optimized shared library cross-platform code, resulting in 40% reduction in project compilation time for 90+ department developers</li><li>• Led full silicon bring-up process, both Pre-Silicon and Post-Silicon stages</li><li>• Negotiated integration process with leaders of partner companies such as Cumulus and Microsoft</li><li>• Supervised remote testing processes operating in Beijing (China), Seattle and Sunnyvale (US)</li><li>• Interviewed and effectively participated in the hiring and termination of employees</li></ul> <b>Firmware Engineer,</b> <i>Switch Silicon Core Department</i> <ul style="list-style-type: none"><li>• Achieved 2nd place in Hackathon with "Scaling FW Switch Reference Model to Higher Layers"; The simulation tool saved up to 50% time in FW development cycle &amp; customer support</li></ul>	<b>Jan 2018 - Jun 2018</b>         <b>2011 - 2017</b> <b>Apr 2017 - Jun 2017</b>          <b>May 2015 - Mar 2017</b>          <b>Mar 2014 - Apr 2015</b>

- Created training-program for new employees, including dedicated code library and practical tasks
- Led the planning and priorities coordination procedure with software architecture and SDK teams
- Developed "Stress" tool that has become major tool for system production/screening, power measurements and debug process
- Conducted daily technical and verification-architectural guidance and coding assistance

**Firmware Student**, *Switch Silicon Core Department*

**Oct 2011 - Feb 2014**

- Developed ANSI-C compiler-specific code to run on Switch Systems RISC
- Managed continuous support and optimization in Switch System Python-based regression scripts
- Implemented cross-platform error-injection and functional-validity tool used by both departments of Switch Systems and Channel Adapters
- Collaborated with chip-design and software engineers to create a Post-Silicon Random Verification Environment for the 56GbE and EDR (100Gbps Infiniband) technologies

## TEACHING EXPERIENCE

**University of Southern California**; Los Angeles, CA, USA

**Spring & Fall 2020**

**Teaching Assistant**, *DSCI 558: Building Knowledge Graphs*

- Designed and evaluated course examinations, written assignments, and weekly quizzes
- Held weekly office hours
- Presented several sessions of 1-2 hour lectures & introduced my work & research to the class

## INTERNSHIPS

**General Electric Global Research**; Niskayuna, NY, USA (online)

**2021**

**Research Fellow Intern**

- Worked at the Artificial Intelligence Technical Domain
- Applied Semantic Web Technologies and Machine Learning algorithms to extract knowledge from structured and unstructured data sources for Knowledge Graph population

**Research Science Institute**; Cambridge, MA, USA

**2009**

**Research Intern (Computer-Science)**

- Studied Statistical Natural Language Processing, Automatic Speech Recognition and Machine Learning; performed model training and testing with Python
- Wrote paper titled "*Improving Text-Independent Speaker Identification Performance Using Gaussian Mixture Speaker Models*" under direction of Dr. David Palmer (HP Autonomy (Virage), Inc).
- Conducted on-campus coursework in scientific theory and off-campus work in scientific research over 6 weeks under sponsorship of the Center for Excellence in Education and MIT

## AWARDS AND SCHOLARSHIPS

**Alexa Prize Socialbot Grand Challenge 4**; Seattle, WA, USA

**2021**

*Our team was the recipient of a research grant valued \$250k as part of the competition*

**Modeling and Managing Complicated Systems Institute**; Pittsburgh, PA, USA

**2019**

*Recipient of the Ford Foundation MWS19 Graduate Student Travel Grant (value \$1.6k)*

**Tel Aviv University**; Tel Aviv, Israel

**2013**

*Faculty Dean's List of Outstanding Undergraduate Students*

**Tel Aviv University**; Tel Aviv, Israel

**2013**

*Recipient of the Freescale Semiconductor Israel Excellence Scholarship (value \$1.2k)*

## CONFERENCES AND OFFICIAL EVENTS

**The 17th Extended Semantic Web Conference (ESWC)**; Online

**2020**

*Conference on semantic technologies*

- Published paper and delivered an oral presentation

**The 10th International Conference on Knowledge Capture**; Marina del Rey, CA, USA

**2019**

*Conference on knowledge representation, acquisition and extraction (a.k.a. K-CAP)*

**The 15th IEEE eScience Conference**; San Diego, CA, USA

**2019**

*Workshop on Advanced Knowledge Technologies for Science in a FAIR World (AKTS)*

- Published paper and delivered an oral presentation at the workshop

**The 2019 Modeling the World's Systems Conference**; Washington, D.C., USA

**2019**

*Conference on the technology and policy of modeling and managing interacting complex systems*

	<ul style="list-style-type: none"> <li>Published paper and delivered an oral presentation and poster</li> </ul>	
	<b>The 2016 VSF Interoperability Event;</b> The Woodlands, TX, USA <i>Video Service Forum annual video broadcast networking interoperability event</i>	<b>2016</b>
	<ul style="list-style-type: none"> <li>Performed testing of HBRMT (High Bit Rate Media Transport over IP)</li> </ul>	
	<b>Haifa Verification Conference (HVC);</b> Haifa, Israel <i>Haifa Verification Conference hosted in IBM Research &amp; Development Labs</i>	<b>2015</b>
	<b>The 26th IBTA InfiniBand and RoCE Plugfest;</b> Durham, NH, USA <i>Infiniband Trade Association event to measure compliance of vendors with the IBTA specifications</i>	<b>2014</b>
	<ul style="list-style-type: none"> <li>Performed interoperability testing of Infiniband and RDMA over Converged Ethernet</li> <li>Collaborated with various vendors to improve testing plan design</li> </ul>	
EXTRACURRICULAR ACTIVITIES	<b>PhD Student Supervisor, IUSSTF at USC;</b> Los Angeles, CA, USA <ul style="list-style-type: none"> <li>Supervised and mentored an intern student (IIT Delhi) in the Indo-U.S. Science and Technology Forum (IUSSTF) at USC-ISI working on leveraging semantics to geospatial data using reverse-geocoding services (i.e. OpenStreetMap), instance matching and ranking algorithms</li> </ul>	<b>2019</b>
	<b>Kosmic Kamels/Middle East Dreamers at Burning Man;</b> Black Rock City, NV, USA <ul style="list-style-type: none"> <li>Co-formed a movement seeking to create new possibilities for makers, activists, entrepreneurs, artists, engineers, business consultants, NGO directors, healers from the MENA region</li> </ul>	<b>2019</b>
	<b>Midburn: Israeli Burning Man Regional Organization;</b> Negev, Israel <ul style="list-style-type: none"> <li>Supervised art installation procedures</li> <li>Contributed code to development of "Dreams", an open-source platform to help community plan co-created events</li> </ul>	<b>2016 - 2018</b>
	<b>Tira Academics: Students Volunteering Association;</b> Tira, Israel <ul style="list-style-type: none"> <li>Co-initiated association to expand academic opportunities for freshmen and high-school students</li> <li>Tutored freshmen engineering students, provided 1-on-1 guidance with Algebra, Calculus, C/Python programming and conducted workshops on a quarterly basis</li> </ul>	<b>2013 - 2017</b>
PUBLICATIONS	<b>B. Shbita,</b> C. A. Knoblock, W. Duan, Y. Chiang, J. H. Uhl, and S. Leyk. Building Linked Spatio-Temporal Data from Vectorized Historical Maps. In <i>European Semantic Web Conference</i> , pages 409–426. Springer, 2020.  <b>B. Shbita,</b> B. Vu, D. Feldman, M. Pham, A. Rajendran, C. Knoblock, J. Pujara, and Y. Chiang. Creating a FAIR Data Catalog to Support Scientific Modeling. In <i>Workshop on Advanced Knowledge Technologies for Science in a FAIR World (AKTS)</i> , 2019.  <b>B. Shbita,</b> A. Rajendran, J. Pujara, and C. Knoblock. Parsing, Representing and Transforming Units of Measure. In <i>Modeling the World's Systems</i> , 2019.	
LANGUAGES	English • Arabic • Hebrew • Russian	
TECHNICAL SKILLS	<ul style="list-style-type: none"> <li>Python, C, C++, C#, MATLAB, VHDL, Cadence PSpice and Assura (Virtuoso)</li> <li>RDF, Turtle and SPARQL</li> <li>SQL, PostgreSQL and PostGIS</li> <li>Unix shell, Bash and Perl scripting</li> <li>Ruby on Rails, HTML, CSS, Node.js and Flask</li> <li>Win32 applications, Dot-Net C# Web, Console applications and Batch scripting</li> <li>PC platforms – MS Visual Studio, Eclipse, Adobe Photoshop, Adobe Premiere and Ableton Live</li> </ul>	
HOBBIES AND INTERESTS	Graphic Design • Figure Drawing • Music Production • Soccer • Combined Martial Arts	