

# Basel Shbita

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## EDUCATION

### TEL AVIV UNIVERSITY

#### B.Sc. ELECTRICAL ENGINEERING

2011 - 2015 | Tel Aviv, Israel

Specialization Areas: Computer-Science,  
Communications and Electronic Devices  
magna cum laude. GPA: 93.33

### ISRAEL ARTS AND SCIENCE ACADEMY

2007 - 2010 | Jerusalem, Israel

High-School Major in Computer-Science,  
Physics and Chemistry  
cum laude

## INTERNSHIPS

### RESEARCH SCIENCE INSTITUTE

RESEARCH INTERN (COMPUTER-SCIENCE)

2009 | Cambridge, MA, USA

Studied and practiced Statistical Natural  
Language Processing, Automatic Speech  
Recognition and Machine Learning.  
Research hosted by HP Autonomy & MIT

## AWARDS

### TEL AVIV UNIVERSITY

FACULTY DEAN'S LIST OF OUTSTANDING  
UNDERGRADUATE STUDENTS

2013 | Tel Aviv, Israel

"FREESCALE SEMICONDUCTOR ISRAEL"

EXCELLENCE SCHOLARSHIP

2013 | Tel Aviv, Israel

## SKILLS

### LANGUAGES

Arabic • Hebrew • English • Russian

### PROGRAMMING

Proficient:

C++ • C • Python • Unix Shell & Bash

Experienced:

C# • MATLAB • VHDL • Assembly

Familiar:

Ruby on Rails • HTML • CSS • JavaScript

## HOBBIES

Arduino • Processing IDE • Soccer •  
Music Production • Graphic Design •  
Combined Martial Arts

## EXPERIENCE

### MELLANOX TECHNOLOGIES

#### SENIOR FIRMWARE ENGINEER, SWITCH SILICON CORE DEPARTMENT

Apr 2017 – June 2017 | Tel Aviv, Israel

- Managed team of 5 engineers
- Led the 100GbE Switch Systems firmware development process and verification activities - an agile operation involving 16 engineers
- Developed a distributed functional testing environment; debug tools and performance testing in C++ in Unix for both Ethernet and InfiniBand fabrics in Data-Link and Network layers of the OSI
- Implemented complex verification concepts and architectures consisting of static and dynamic analysis
- Conducted technical presentations to 50+ developers from various teams
- Awarded for excellence and ranked "Superior" (top 5% out of 3000)

#### TECHNICAL LEAD FIRMWARE ENGINEER, SWITCH SILICON CORE DEPARTMENT

May 2015 – Mar 2017 | Tel Aviv, Israel

- Managed team of 3 engineers
- Contributed to end-to-end development and defined version-release procedures for the company's 100GbE Switch products
- Conducted personal training, code reviews; defined coding-style and methodologies of software engineering for team of 30
- Optimized shared library cross-platform code, resulting in 40% reduction in project compilation time for 90+ department developers
- Led full silicon bring-up process, both Pre-Silicon and Post-Silicon stages
- Negotiated integration process with leaders of partner companies
- Supervised remote testing processes operating in China & USA
- Interviewed and effectively participated in the hiring of employees
- Represented company in the 2016 VSF Interoperability Event (TX, USA)

#### FIRMWARE ENGINEER, SWITCH SILICON CORE DEPARTMENT

Mar 2014 – Apr 2015 | Tel Aviv, Israel

- Achieved 2nd place in Hackathon; Project introduced simulation tool saving up to 50% time in development cycle & customer support
- Created training-program for new employees
- Led the planning and priorities coordination procedure with software architecture and SDK teams
- Developed "Stress" tool that has become major tool for system production/screening, power measurements and debug process
- Conducted daily technical and architectural guidance and coding assistance
- Represented company in the 26th IBTA Plugfest (NH, USA) & HVC15 (Haifa)

#### FIRMWARE STUDENT, SWITCH SILICON CORE DEPARTMENT

Oct 2011 – Feb 2014 | Tel Aviv, Israel

- Developed ANSI-C compiler-specific code to run on Switch Systems RISC
- Managed continuous support and optimization in Switch System Python-based regression scripts
- Implemented cross-platform error-injection and functional-validity tool used by both departments of Switch Systems and Channel Adapters
- Collaborated with chip-design and software engineers to create a Post-Silicon Random Verification Environment for the 56GbE and EDR (100Gbps Infiniband) technologies