

## Automated Testing

The test files are available at /home/cs1911/public\_html/14s1/assignments/ass2/tests  
or <http://www.cse.unsw.edu.au/~cs1911/14s1/assignments/ass2/tests/>  
The number of tests passed is translated to a mark in a piecewise linear manner.

```
/home/cs1911/bin/dcc -o 2048_dcc 2048.c
/home/cs1911/bin/vcc -o 2048_vcc 2048.c
Test print_0 (0 0 0 0 0 0 0 0 0 0 0 2 0 0 0 0 0 ) - passed
Test print_1 (0 0 0 0 0 4 0 0 0 0 4 0 0 0 0 0 ) - passed
Test print_2 (0 0 0 0 0 0 0 0 0 0 0 8 0 0 0 8 0 ) - passed
Test print_3 (0 0 0 8 0 0 0 0 0 0 0 0 8 0 0 0 ) - passed
Test print_4 (8 0 0 8 0 0 0 0 0 0 0 0 8 0 0 8 ) - passed
Test print_5 (0 0 0 0 16 0 16 0 0 0 0 0 0 0 0 0 ) - passed
Test print_6 (0 0 32 0 0 0 0 0 0 0 0 0 0 0 32 0 ) - passed
Test print_7 (0 0 0 0 0 0 0 0 0 0 0 0 0 64 64 64 ) - passed
Test print_8 (128 0 0 0 0 128 0 0 0 0 128 0 0 0 0 128 ) - passed
Test print_9 (256 0 0 256 0 256 256 0 0 256 256 0 256 0 0 256 ) - passed
Test won_0 (16 4 2 8 2 8 64 2 32 16 2048 4 64 8 64 32 q) - passed
Test won_1 (2048 2048 2048 2048 2048 2048 2048 2048 2048 2048 2048 2048 2048 2048 2048 2048 q) - passed
Test won_1 (16 4 2 8 2 8 64 2 32 16 2048 4 64 8 64 32 q) - passed
Test won_2 (0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 2048 q) - passed
Test over_0 (16 4 2 8 2 8 64 2 32 16 8 4 64 8 64 32 q) - passed
Test over_1 (0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 q) - passed
Test move_0h (0 0 0 0 0 0 0 0 0 0 0 2 0 0 0 0 0 h) - passed
Test move_0j (0 0 0 0 0 0 0 0 0 0 0 2 0 0 0 0 0 j) - passed
Test move_0k (0 0 0 0 0 0 0 0 0 0 0 2 0 0 0 0 0 k) - passed
Test move_0l (0 0 0 0 0 0 0 0 0 0 0 2 0 0 0 0 0 l) - passed
Test move_1h (0 0 0 0 0 4 0 0 0 0 4 0 0 0 0 0 0 h) - passed
Test move_1j (0 0 0 0 0 4 0 0 0 0 4 0 0 0 0 0 0 j) - passed
Test move_1k (0 0 0 0 0 4 0 0 0 0 4 0 0 0 0 0 0 k) - passed
Test move_1l (0 0 0 0 0 4 0 0 0 0 4 0 0 0 0 0 0 l) - passed
Test move_2h (0 0 0 0 0 0 0 0 0 0 8 0 0 0 8 0 0 h) - passed
Test move_2j (0 0 0 0 0 0 0 0 0 0 8 0 0 0 8 0 0 j) - passed
Test move_2k (0 0 0 0 0 0 0 0 0 0 8 0 0 0 8 0 0 k) - passed
Test move_2l (0 0 0 0 0 0 0 0 0 0 8 0 0 0 8 0 0 l) - passed
Test move_3h (0 0 0 8 0 0 0 0 0 0 0 0 8 0 0 0 0 h) - passed
Test move_3j (0 0 0 8 0 0 0 0 0 0 0 0 8 0 0 0 0 j) - passed
Test move_3k (0 0 0 8 0 0 0 0 0 0 0 0 8 0 0 0 0 k) - passed
Test move_3l (0 0 0 8 0 0 0 0 0 0 0 0 8 0 0 0 0 l) - passed
Test move_4h (8 0 0 8 0 0 0 0 0 0 0 0 8 0 0 8 0 h) - passed
Test move_4j (8 0 0 8 0 0 0 0 0 0 0 0 8 0 0 8 0 j) - passed
Test move_4k (8 0 0 8 0 0 0 0 0 0 0 0 8 0 0 8 0 k) - passed
Test move_4l (8 0 0 8 0 0 0 0 0 0 0 0 8 0 0 8 0 l) - passed
Test move_5h (0 0 0 0 16 0 16 0 0 0 0 0 0 0 0 0 0 h) - passed
Test move_5j (0 0 0 0 16 0 16 0 0 0 0 0 0 0 0 0 0 j) - passed
Test move_5k (0 0 0 0 16 0 16 0 0 0 0 0 0 0 0 0 0 k) - passed
Test move_5l (0 0 0 0 16 0 16 0 0 0 0 0 0 0 0 0 0 l) - passed
Test move_6h (0 0 32 0 0 0 0 0 0 0 0 0 0 0 0 32 0 h) - passed
Test move_6j (0 0 32 0 0 0 0 0 0 0 0 0 0 0 0 32 0 j) - passed
Test move_6k (0 0 32 0 0 0 0 0 0 0 0 0 0 0 0 32 0 k) - passed
Test move_6l (0 0 32 0 0 0 0 0 0 0 0 0 0 0 0 32 0 l) - passed
Test move_7h (0 0 0 0 0 0 0 0 0 0 0 0 0 64 64 0 64 h) - passed
Test move_7j (0 0 0 0 0 0 0 0 0 0 0 0 0 64 64 0 64 j) - passed
Test move_7k (0 0 0 0 0 0 0 0 0 0 0 0 0 64 64 0 64 k) - passed
Test move_7l (0 0 0 0 0 0 0 0 0 0 0 0 0 64 64 0 64 l) - passed
Test move_8h (128 0 0 0 0 128 0 0 0 0 128 0 0 0 0 128 h) - passed
Test move_8j (128 0 0 0 0 128 0 0 0 0 128 0 0 0 0 128 j) - passed
Test move_8k (128 0 0 0 0 128 0 0 0 0 128 0 0 0 0 128 k) - passed
Test move_8l (128 0 0 0 0 128 0 0 0 0 128 0 0 0 0 128 l) - passed
Test move_9h (256 0 0 256 0 256 256 0 0 256 256 0 256 0 0 256 h) - passed
Test move_9j (256 0 0 256 0 256 256 0 0 256 256 0 256 0 0 256 j) - passed
Test move_9k (256 0 0 256 0 256 256 0 0 256 256 0 256 0 0 256 k) - passed
Test move_9l (256 0 0 256 0 256 256 0 0 256 256 0 256 0 0 256 l) - passed
Test move_10h (512 512 512 512 512 512 512 512 512 512 512 512 512 512 512 512 h) - passed
Test move_10j (512 512 512 512 512 512 512 512 512 512 512 512 512 512 512 512 j) - passed
Test move_10k (512 512 512 512 512 512 512 512 512 512 512 512 512 512 512 512 k) - passed
Test move_10l (512 512 512 512 512 512 512 512 512 512 512 512 512 512 512 512 l) - passed
Test move_11h (1024 2048 1024 2048 2048 1024 2048 1024 1024 2048 1024 2048 1024 2048 1024 2048 1024 h) - passed
Test move_11j (1024 2048 1024 2048 2048 1024 2048 1024 1024 2048 1024 2048 1024 2048 1024 2048 1024 j) - passed
Test move_11k (1024 2048 1024 2048 2048 1024 2048 1024 1024 2048 1024 2048 1024 2048 1024 2048 1024 k) - passed
Test move_11l (1024 2048 1024 2048 2048 1024 2048 1024 1024 2048 1024 2048 1024 2048 1024 2048 1024 l) - passed
Test move_12h (1024 2048 1024 2048 2048 2048 2048 1024 1024 2048 2048 2048 2048 1024 2048 1024 1024 h) - passed
Test move_12j (1024 2048 1024 2048 2048 2048 2048 1024 1024 2048 2048 2048 2048 1024 2048 1024 1024 j) - passed
Test move_12k (1024 2048 1024 2048 2048 2048 2048 1024 1024 2048 2048 2048 2048 1024 2048 1024 1024 k) - passed
Test move_12l (1024 2048 1024 2048 2048 2048 2048 1024 1024 2048 2048 2048 2048 1024 2048 1024 1024 l) - passed
Test move_13h (4096 2048 8192 2048 2048 2048 2048 1024 1024 2048 2048 2048 2048 1024 2048 4096 h) - passed
Test move_13j (4096 2048 8192 2048 2048 2048 2048 1024 1024 2048 2048 2048 2048 1024 2048 4096 j) - passed
Test move_13k (4096 2048 8192 2048 2048 2048 2048 1024 1024 2048 2048 2048 2048 1024 2048 4096 k) - passed
Test move_13l (4096 2048 8192 2048 2048 2048 2048 1024 1024 2048 2048 2048 2048 1024 2048 4096 l) - passed
Test move_14h (100 100 100 100 100 0 0 100 100 0 0 100 100 100 100 100 h) - passed
Test move_14j (100 100 100 100 100 0 0 100 100 0 0 100 100 100 100 100 j) - passed
Test move_14k (100 100 100 100 100 0 0 100 100 0 0 100 100 100 100 100 k) - passed
Test move_14l (100 100 100 100 100 0 0 100 100 0 0 100 100 100 100 100 l) - passed
Test move_15h (13 15 17 19 13 15 15 19 13 115 115 19 13 115 115 19 h) - passed
Test move_15j (13 15 17 19 13 15 15 19 13 115 115 19 13 115 115 19 j) - passed
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Test move_15k (13 15 17 19 13 15 15 19 13 115 115 19 13 115 115 19 k) - passed
Test move_15l (13 15 17 19 13 15 15 19 13 115 115 19 13 115 115 19 l) - passed
Test move_16h (2 4 4 4 8 8 8 4 8 8 16 16 16 32 32 32 h) - passed
Test move_16j (2 4 4 4 8 8 8 4 8 8 16 16 16 32 32 32 j) - passed
Test move_16k (2 4 4 4 8 8 8 4 8 8 16 16 16 32 32 32 k) - passed
Test move_16l (2 4 4 4 8 8 8 4 8 8 16 16 16 32 32 32 l) - passed
Test move_17h (8 0 8 8 8 8 8 0 8 8 8 8 0 8 8 8 h) - passed
Test move_17j (8 0 8 8 8 8 8 0 8 8 8 8 0 8 8 8 j) - passed
Test move_17k (8 0 8 8 8 8 8 0 8 8 8 8 0 8 8 8 k) - passed
Test move_17l (8 0 8 8 8 8 8 0 8 8 8 8 0 8 8 8 l) - passed
Test move_18h (8 16 8 8 8 8 16 8 8 8 8 16 8 8 8 8 h) - passed
Test move_18j (8 16 8 8 8 8 16 8 8 8 8 16 8 8 8 8 j) - passed
Test move_18k (8 16 8 8 8 8 16 8 8 8 8 16 8 8 8 8 k) - passed
Test move_18l (8 16 8 8 8 8 16 8 8 8 8 16 8 8 8 8 l) - passed
Test move_19h (0 32 32 0 32 32 32 32 32 32 32 32 0 32 32 0 h) - passed
Test move_19j (0 32 32 0 32 32 32 32 32 32 32 32 0 32 32 0 j) - passed
Test move_19k (0 32 32 0 32 32 32 32 32 32 32 32 0 32 32 0 k) - passed
Test move_19l (0 32 32 0 32 32 32 32 32 32 32 32 0 32 32 0 l) - passed
Test move_20h (2 0 2 0 8 0 2 4 2 0 0 4 0 0 2 0 h) - passed
Test move_20j (2 0 2 0 8 0 2 4 2 0 0 4 0 0 2 0 j) - passed
Test move_20k (2 0 2 0 8 0 2 4 2 0 0 4 0 0 2 0 k) - passed
Test move_20l (2 0 2 0 8 0 2 4 2 0 0 4 0 0 2 0 l) - passed
Test complex0_seed0 (testcomplex0_seed0) - passed
Test complex0_seed1 (testcomplex0_seed1) - passed
Test complex0_seed2 (testcomplex0_seed2) - passed
Test complex0_seed3 (testcomplex0_seed3) - passed
Test complex0_seed4 (testcomplex0_seed4) - passed
Test complex0_seed5 (testcomplex0_seed5) - passed
Test complex0_seed6 (testcomplex0_seed6) - passed
Test complex0_seed7 (testcomplex0_seed7) - passed
Test complex0_seed8 (testcomplex0_seed8) - passed
Test complex0_seed9 (testcomplex0_seed9) - passed
Test complex1_seed0 (testcomplex1_seed0) - passed
Test complex1_seed1 (testcomplex1_seed1) - passed
Test complex1_seed2 (testcomplex1_seed2) - passed
Test complex1_seed3 (testcomplex1_seed3) - passed
Test complex1_seed4 (testcomplex1_seed4) - passed
Test complex1_seed5 (testcomplex1_seed5) - passed
Test complex1_seed6 (testcomplex1_seed6) - passed
Test complex1_seed7 (testcomplex1_seed7) - passed
Test complex1_seed8 (testcomplex1_seed8) - passed
Test complex1_seed9 (testcomplex1_seed9) - passed
Test complex2_seed0 (testcomplex2_seed0) - passed
Test complex2_seed1 (testcomplex2_seed1) - passed
Test complex2_seed2 (testcomplex2_seed2) - passed
Test complex2_seed3 (testcomplex2_seed3) - passed
Test complex2_seed4 (testcomplex2_seed4) - passed
Test complex2_seed5 (testcomplex2_seed5) - passed
Test complex2_seed6 (testcomplex2_seed6) - passed
Test complex2_seed7 (testcomplex2_seed7) - passed
Test complex2_seed8 (testcomplex2_seed8) - passed
Test complex2_seed9 (testcomplex2_seed9) - passed

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\*\* All tests passed. You are amazing!

130 tests passed

Mark from automatic testing 80.0/80.0

E-mail andrewt if you think the automatic testing of your program is incorrect.