Tutorial #07

Junction Field Effect Transistors.

Q1. The device parameters of n-channel JFETs are: $I_{Dss} = 10 \, \text{mA}$; $V_{pinch-off} = 4.0 \, \text{V}$

Calculate the drain current ID for Vas = DV; -1.0V and -4.0V

- Q2 A JFET produces gate current of 2nA when gate is reverse biased with 8V. Determine the resistance between gate and source
- Q3. A JFET is self biased with $V_{DD} = 20V$ and $I_{DSS} = 12mA$. The value of drain and gate resistance is $2k\Omega$ and $1m\Omega$ respectively.

 Determine the value of source-resistance for which the operating paint lies at the middle of the load-line.
- Q4. Determine the operating point corresponding to the voltage divider biased circuit shown below:

