

Emitter-Biased Amplifier Circuit

Recap: Small-signal amplifier utilizing base-bias circuit.

- We follow 10% rule

⇒ the amplitude of ac signal (current/voltage) $\leq 10\%$ of the dc bias current/voltage

Last class: Base-bias amplifier ckt.

- Here the BJT is base-biased

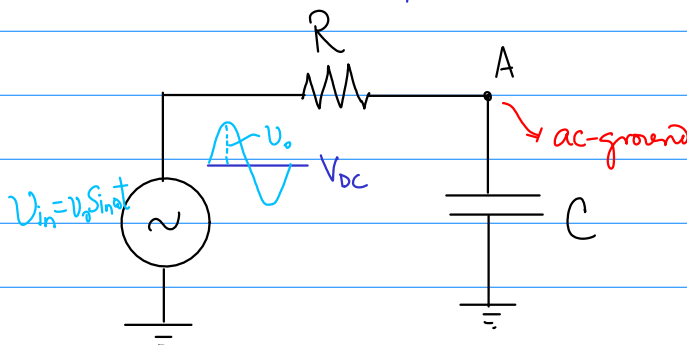
- In base-biasing, Q-point is "Unstable" (it depends on β_{DC})

Emitter-Bias Amplifier ckt:

- Stable Q-point (independent of β_{DC})

- Voltage-Divider Bias (VDB) Circuit / TSEB

- We are going to use additional capacitor connected ^{in parallel} to the emitter resistor (R_E).
This capacitor is termed as 'Bypass Capacitor'.



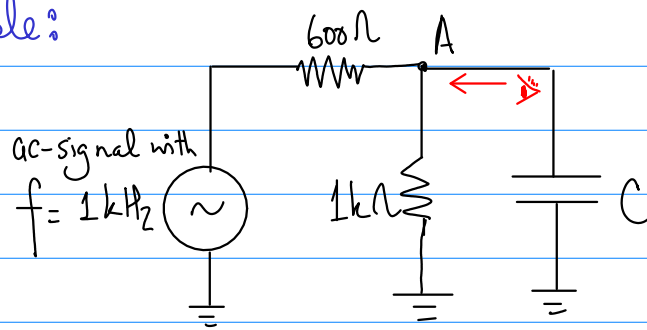
$$X_C = \frac{1}{\omega C}$$

Suppose, $R \geq 10 X_C$

⇒ All ac voltage drops across the resistor R .

⇒ The point 'A' is an ac ground.

Example:



For what value of the capacitor 'C', the point 'A' appears to be the ac-ground.

Equivalent resistance as seen by the capacitor at point 'A'.

$$R_{eq,A} = R_{Th} = 600\Omega \parallel 1k\Omega = \frac{600 \times 1000}{600 + 1000} = 375\Omega$$

For the capacitor to work as by-pass capacitor,

$$X_C \leq \frac{R_{eq}}{10}$$

$$X_C \leq 37.5\Omega$$

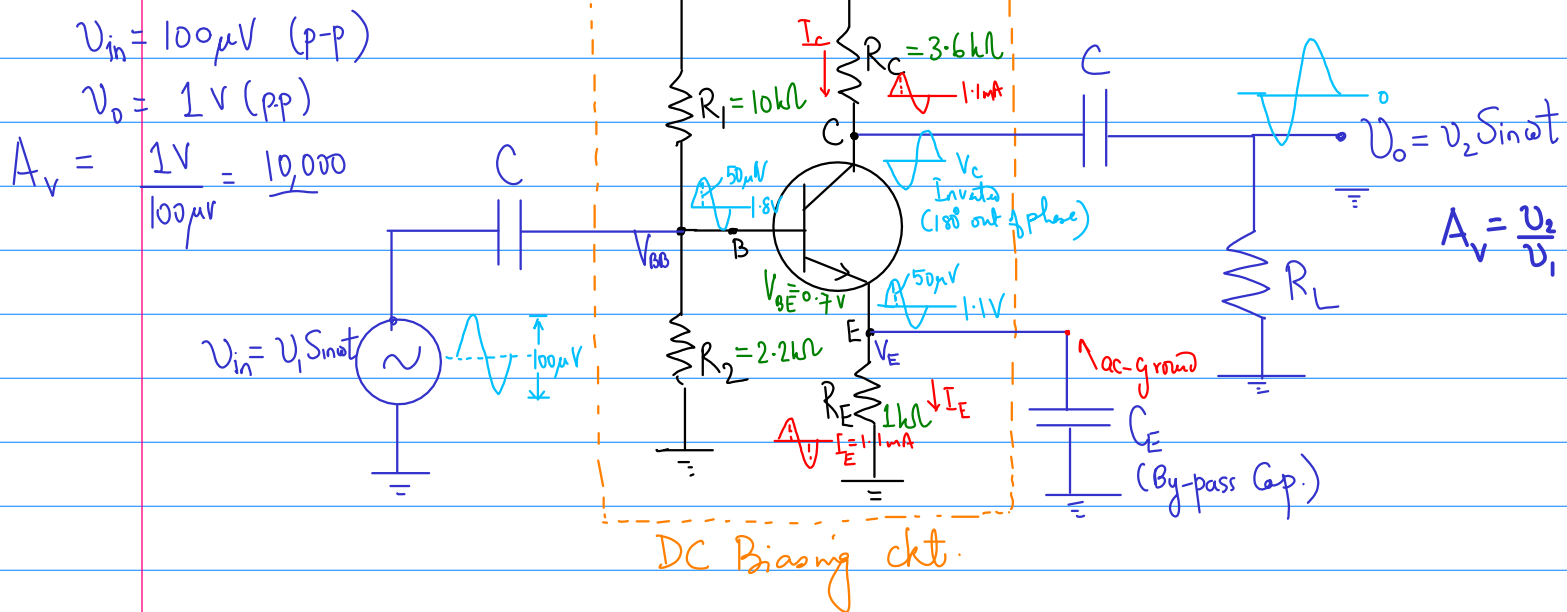
$$\frac{1}{2\pi f C} \leq 37.5\Omega$$

$$\Rightarrow C \geq \frac{1}{(2\pi f)(37.5)}$$

$$\Rightarrow C \geq \frac{1}{2 \times 3.14 \times 1000 \times 37.5}$$

$$\Rightarrow C \geq 4.2\mu F$$

Emitter-Bias Amplifier :



DC Analysis :

$$V_{BB} = \frac{R_2}{R_1 + R_2} \cdot V_{cc} = \left[\frac{2.2k\Omega}{10k\Omega + 2.2k\Omega} \right] 10V = 1.8V$$

$$V_E = V_{BB} - V_{BE} = 1.8V - 0.7V = 1.1V$$

$$I_E = \frac{V_E}{R_E} = \frac{1.1V}{1k\Omega} = 1.1mA$$

$$I_C \approx I_E = 1.1mA$$

$$V_C = V_{cc} - I_C R_C = 10V - (1.1mA)(3.6k\Omega)$$

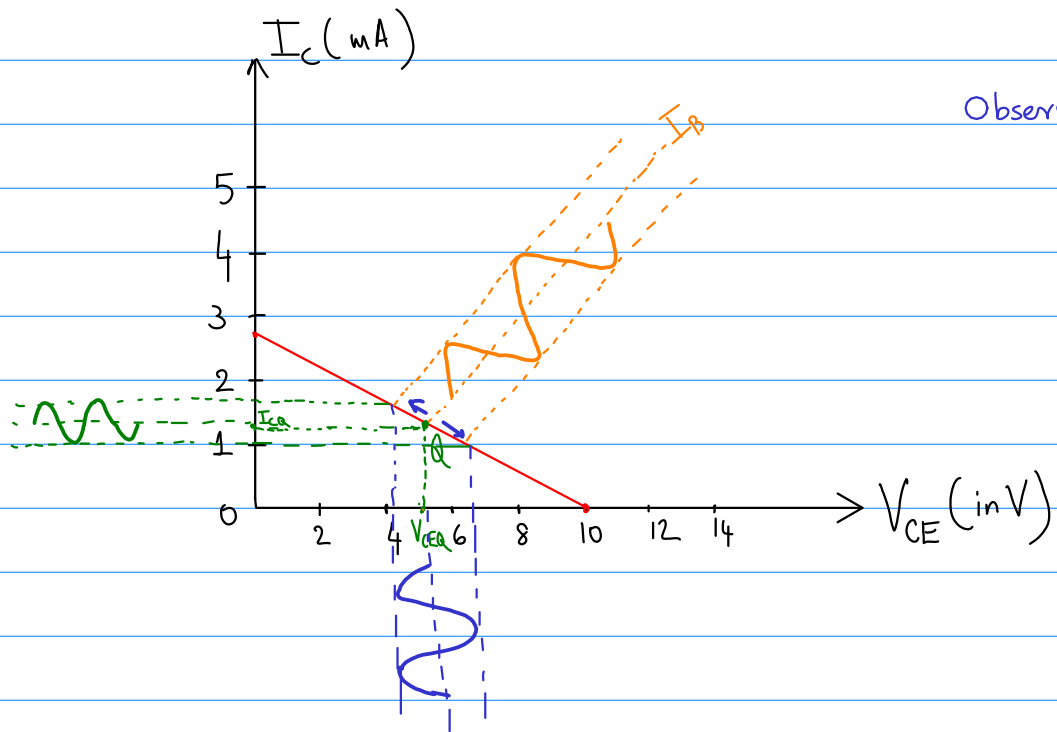
$$V_C = 10V - 3.96V = 6.04V$$

$$V_{CE} = V_C - V_E = 6.04V - 1.1V = 4.94V$$

Q-point : $4.94V$; $1.1mA$

$$I_{C,sat} = \frac{V_{CC}}{R_C} = \frac{10V}{3.6k\Omega} = 2.7mA$$

$$V_{CE, cut-off} = V_{CC} = 10V$$



Observation: Q-point
lies ^{near} at the
middle of
the load-line