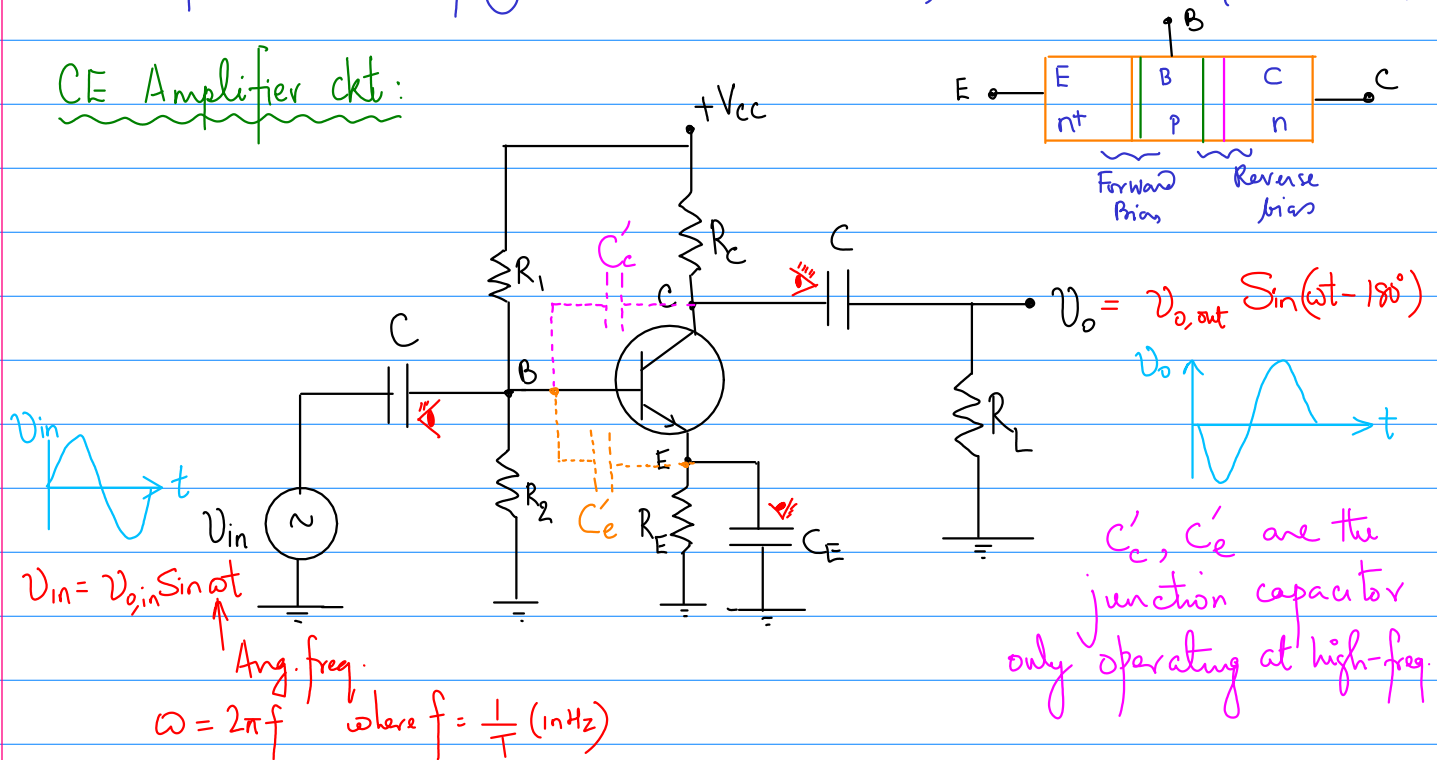


Frequency Response of CE Amplifier

(Reference: Chapter 16, Malvino & Bates)

1. How voltage gain (A_v) of the CE amplifier changes with varying the frequency of the input signal.
2. Representation of gain (voltage/power) in units of decibel (dB)

CE Amplifier dkt:



- As we know, for DC analysis:
 - the capacitors are 'OPEN'.
- For ac analysis: All the capacitors are 'SHORT'

$$X_C = \frac{1}{\omega C}$$

ω / $f \rightarrow$ Mid-freq. range (1 kHz - 500 kHz)

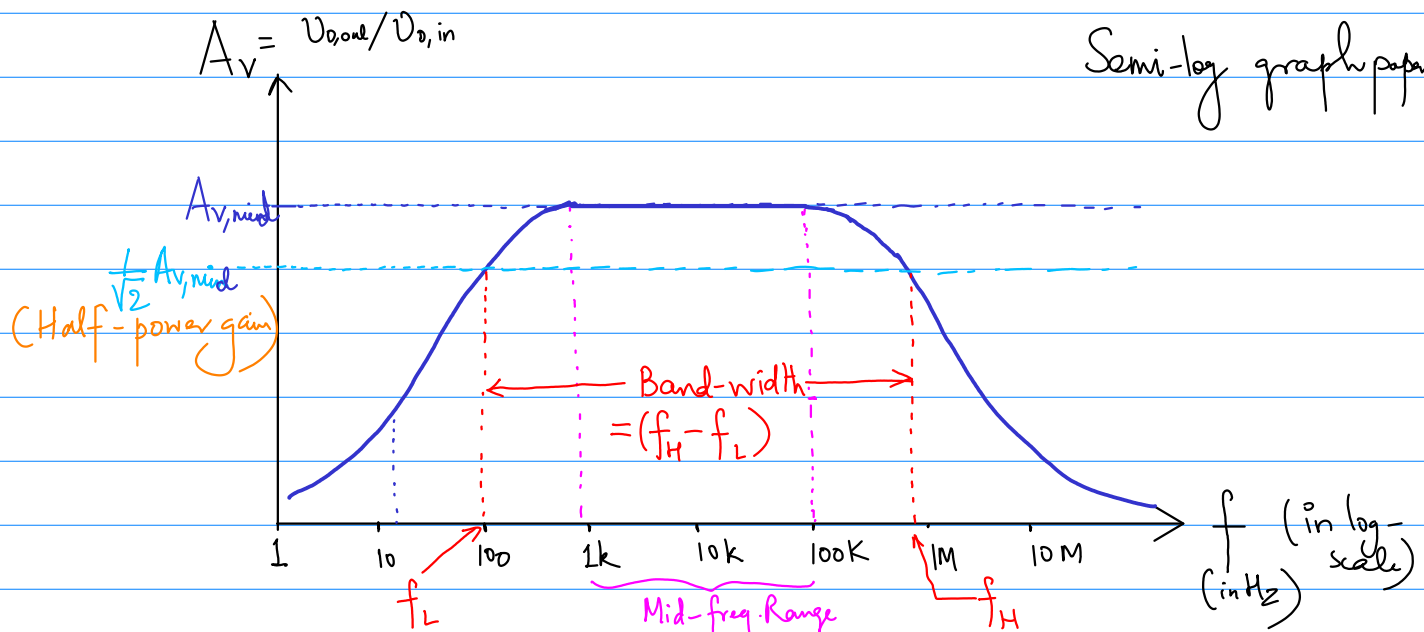
In mid-frequency range the value of X_c is small as compared to the equivalent resistance as seen the capacitor, and hence all the ac voltage is dropped across the resistor. Therefore, the capacitors are electrically short for the ac-signal.

Ques: Is this assumption valid in the low-frequencies?
 - NO - \Rightarrow effectively the gain reduces.

Ques: Is this assumption valid in the high-frequencies?
 - YES - \Rightarrow effectively the gain reduces.
 Why?

- At high-frequencies, the base-emitter junction and base-collector junction effectively behaves as a capacitors and affect the input & output ac voltages.

\Rightarrow Effectively the gain reduces.



Mathematically,

$$A_v(f) = \frac{A_{v(\text{mid})}}{\sqrt{1+(f_L/f)^2} \sqrt{1+(f/f_H)^2}}$$

where, f_L = Lower cut-off frequency

f_H = Higher cut-off frequency.

Case (i) $f < f_L$ (ie, the frequency is lower the low cut-off freq.)

$$\frac{f_L}{f} > 1 \quad \text{whereas} \quad \frac{f}{f_H} \approx 0$$

$$A_v(f) \approx \frac{A_{v, \text{mid}}}{\sqrt{1+(\frac{f_L}{f})^2}}$$

Case (ii) $f_L < f < f_H$ (ie, mid-freq. range).

$$\frac{f_L}{f} \approx 0 \quad \frac{f}{f_H} \approx 0$$

$$A_v(f) = A_{v \text{ mid}}$$

Case (iii) $f > f_H$ (high freq. range)

$$\frac{f_L}{f} \approx 0$$

$$\frac{f}{f_H} > 1$$

$$A_v(f) = \frac{A_{v \text{ mid}}}{\sqrt{1 + \left(\frac{f}{f_H}\right)^2}}$$

Unit of voltage gain in Decibel (dB)

$$A_v(\text{dB}) = \left[20 \log_{10} A_v \right] \text{dB}$$

known $A_v = \frac{V_o}{V_{in}}$

eg: if $A_v = 100$

$$\begin{aligned} A_v(\text{dB}) &= 20 \log_{10} 100 \text{ dB} \\ &= 40 \text{ dB} \end{aligned}$$

----- Continued

Unit of power gain in decibel (dB)

$$A_p(\text{dB}) = 10 \log_{10} (A_p) \text{ dB}$$

where $A_p = \frac{P_o}{P_{in}} = \frac{\text{ac power output}}{\text{ac power input}}$.

We have designed a multi-stage amplifier in which each stage is having voltage gain as

$$A_{v_1}, A_{v_2}, A_{v_3} \text{ ----- } A_{v_n}$$

Overall gain is given as :

$$A_v = A_{v_1} \cdot A_{v_2} \cdot A_{v_3} \text{ ----- } \cdot A_{v_n}$$

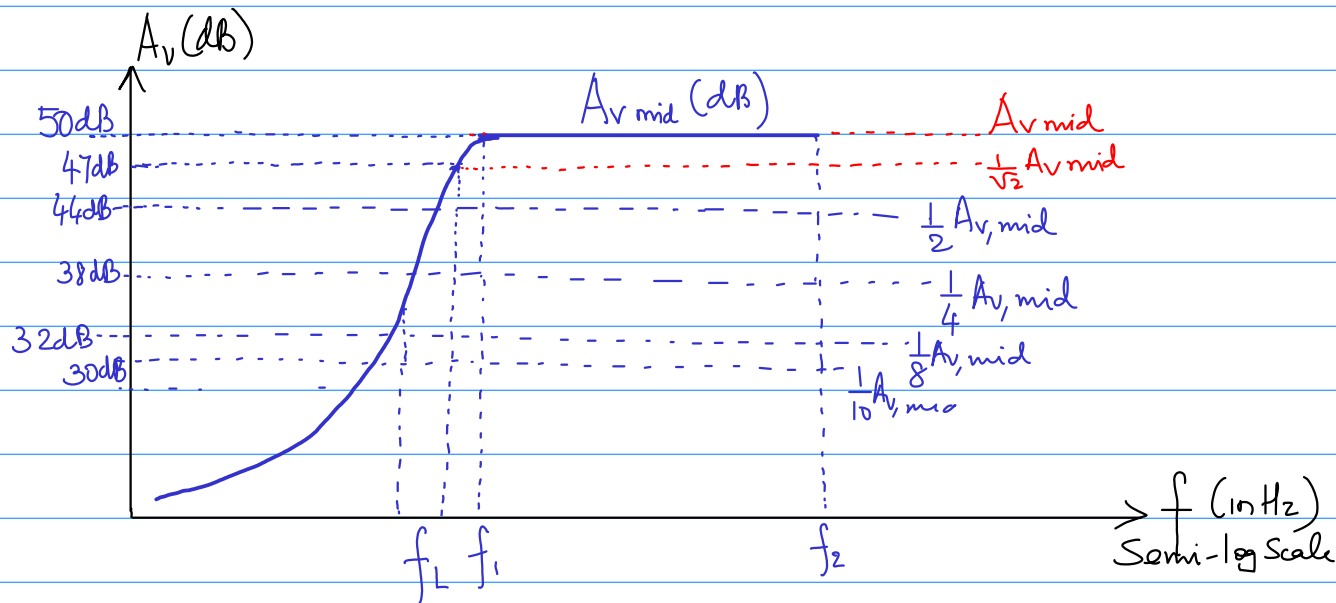
Overall gain in dB

$$A_v(\text{dB}) = 20 \log_{10} A_v$$

$$= 20 \log_{10} [A_{v_1} \cdot A_{v_2} \cdot A_{v_3} \text{ ----- } A_{v_n}]$$

$$= 20 \log_{10} A_{v_1} + 20 \log_{10} A_{v_2} + \text{-----} + 20 \log_{10} A_{v_n}$$

$$A_v(\text{dB}) = A_{v_1}(\text{dB}) + A_{v_2}(\text{dB}) + \dots + A_{v_n}(\text{dB})$$



At lower cut-off frequency the voltage gain is reduced as a factor $\frac{1}{\sqrt{2}}$.

What the value of this reduction in dB.

$$\begin{aligned} \text{Red}^n \text{ in gain (dB)} &= 20 \log_{10} \left(\frac{1}{\sqrt{2}} \right) \text{ dB} \\ &= \underbrace{20 \log_{10} 1}_{=0} - 20 \log_{10} 2^{1/2} \text{ dB} \\ &= -20 \cdot \frac{1}{2} \log_{10} 2 \text{ dB} \\ &= -3 \text{ dB} \end{aligned}$$

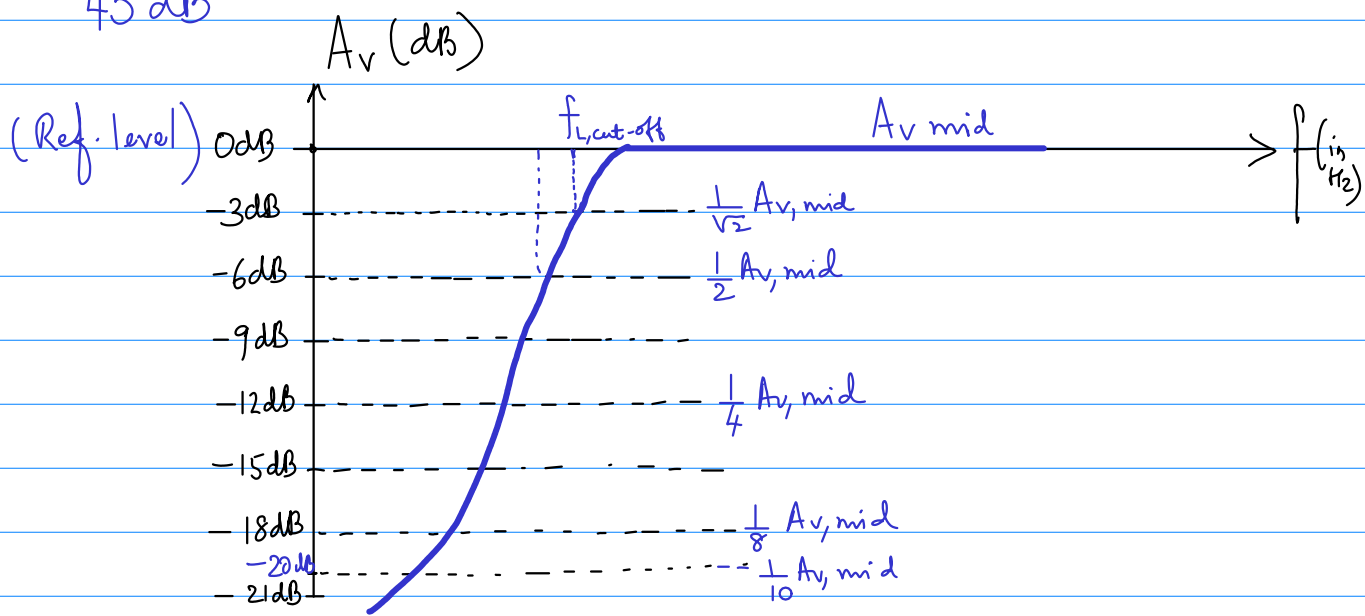
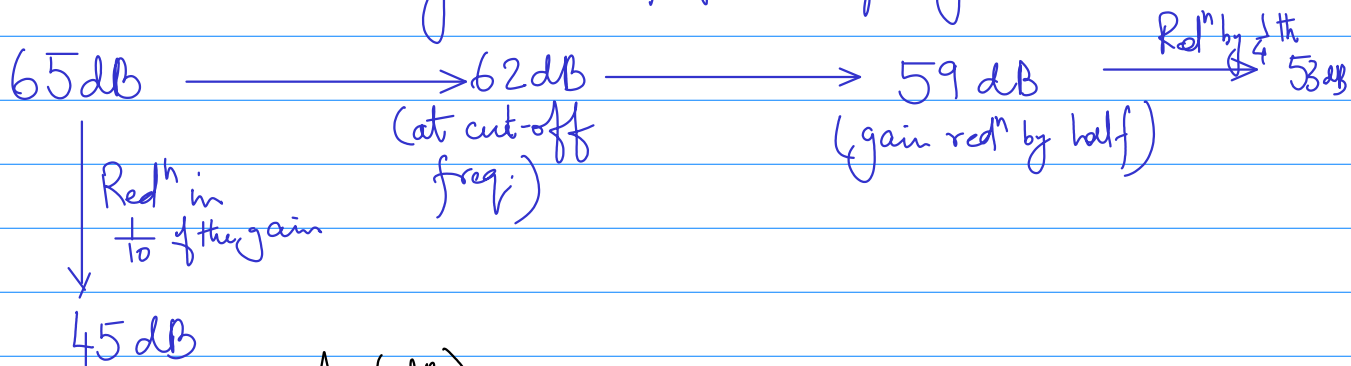
Redⁿ in gain at cut-off frequency is 3dB.

* Note : 1) Whenever the ^{voltage} gain is reduced by half ($\frac{1}{2}$) the ^{voltage} gain in dB is reduced by 6dB.

2) whenever the ^{voltage} gain is reduced by $\frac{1}{10}$ the, the ^{voltage} gain is reduced by 20dB

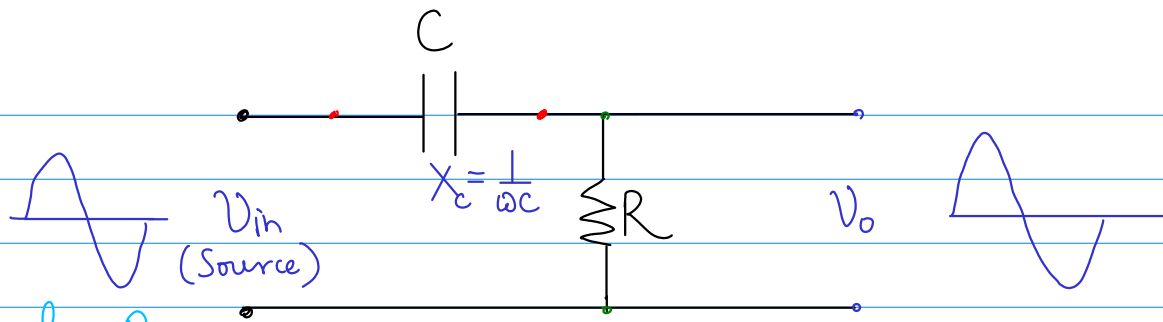
Example:

Let's say mid-freq. voltage gain is 65dB



Low frequency response of a CE Amplifier:

We know that at low frequencies ($f \rightarrow 0$), the reactance (X_c) of the coupling & by-pass capacitors are not approximated to zero. (ie, the capacitors are no more behaving as a "SHORT" to ac-signal.



High freq. Response.

⇒ The RC circuit allows high-frequency signal to pass through. In other words, it blocks the low-frequency signal.

Low freq. Response:

The capacitors are no more 'SHORT'.

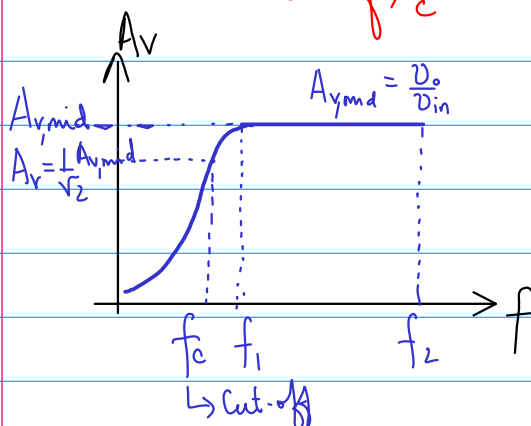
⇒

$$V_o = \frac{R}{\sqrt{X_c^2 + R^2}} \cdot V_{in}$$

$$X_c = \frac{1}{2\pi f C}$$

Low freq. If $X_c = R \Rightarrow V_o = \frac{1}{\sqrt{2}} V_{in}$ ✓

the freq 'f' at which
the value of $X_c = R$

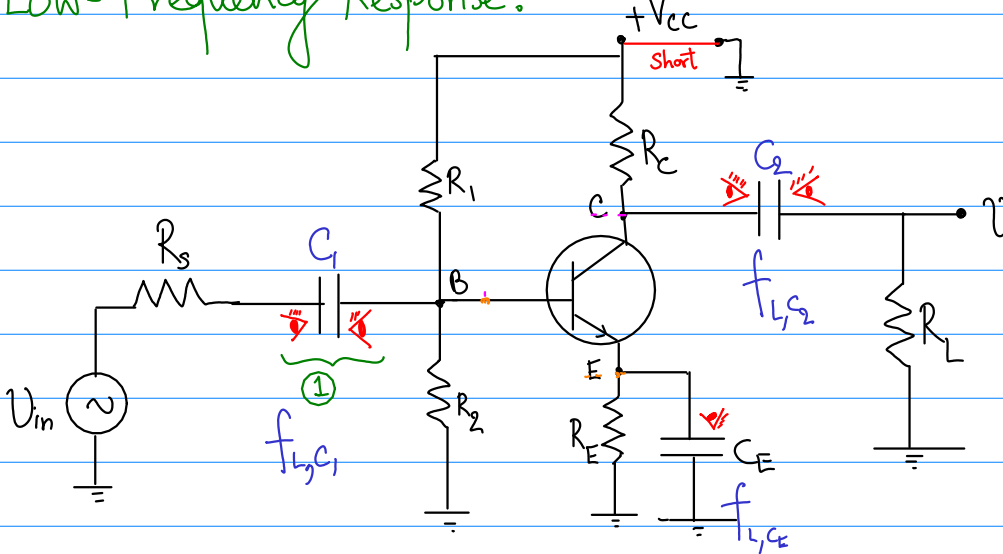


⇒ The $A_v = \frac{V_o}{V_{in}} = \frac{1}{\sqrt{2}}$ (A_{vmid}) ⇒ the

frequency at which $X_c = R$ is
the cut-off freq.

... Continued

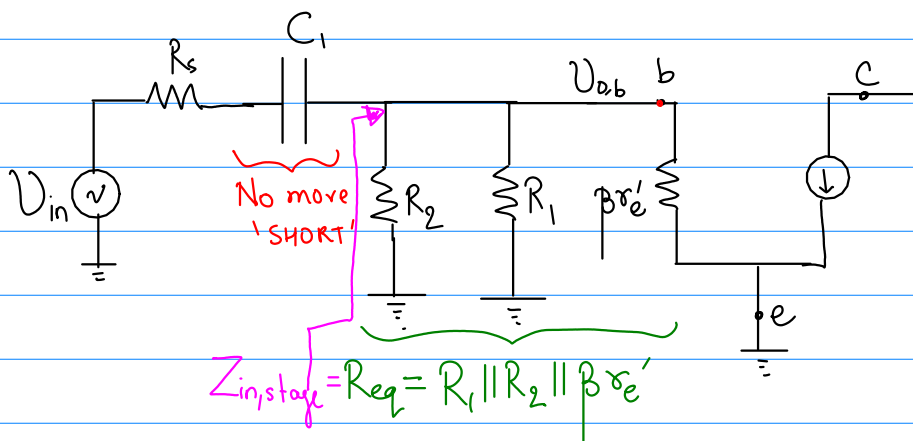
Low-Frequency Response:



$r_e' = \text{ac-emitter resistance}$

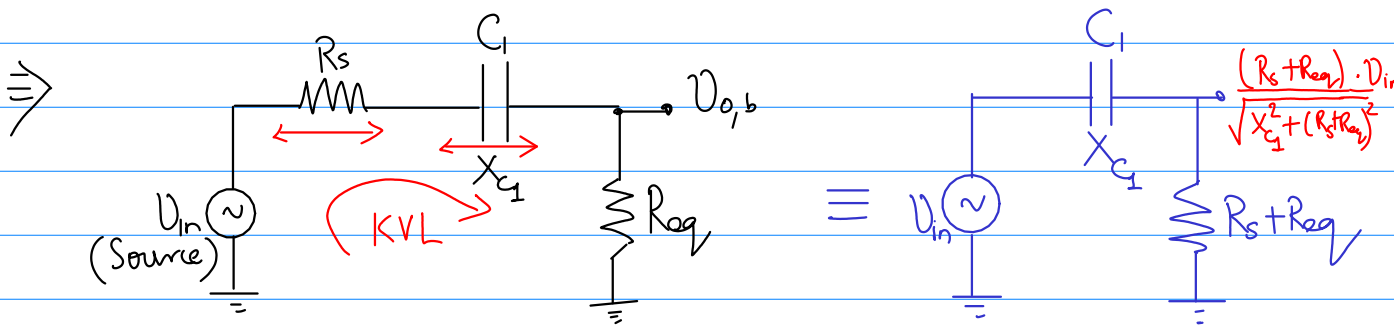
$$r_e' = \frac{25 \text{ mV}}{I_E}$$

① Effect of the capacitor C_1 :



$Z = X + jY$
 \uparrow Resistance
 \uparrow Reactance

$$Z_{in, stage} = R_{eq} = R_1 \parallel R_2 \parallel \beta r_e'$$

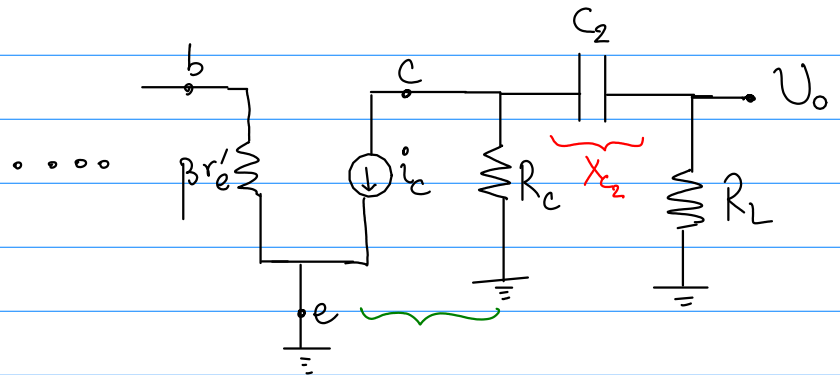


if $X_{C1} = R_s + R_{eq}$ then we are at the lower cut-off freq.

$$\frac{1}{2\pi f_{L,C1} C_1} = R_s + R_{eq}$$

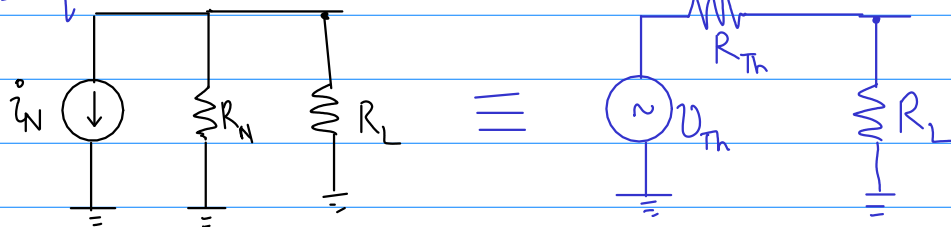
$$f_{L, C_1} = \frac{1}{2\pi (R_s + R_{eq}) C_1}$$

② Effect of the Capacitor C_2 :



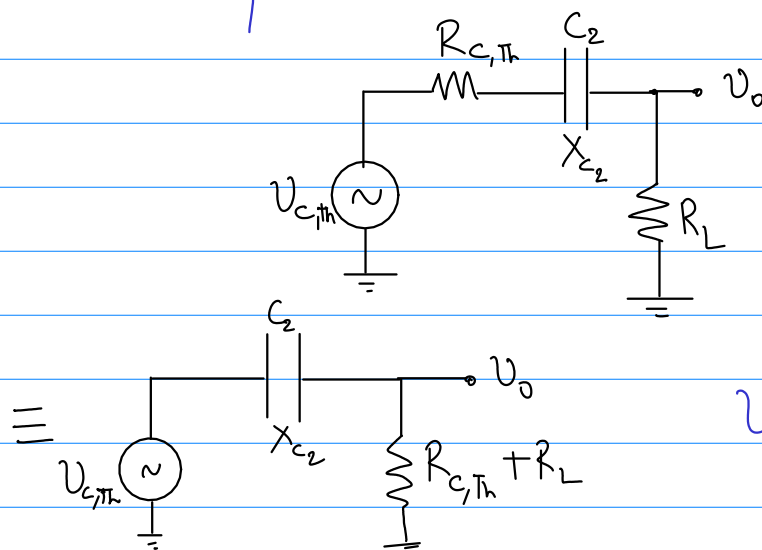
Recall 1st chapter

Norton's Eq. Ckt.



where, $R_{Th} = R_N$; $V_{Th} = i_N \cdot R_N$

If we apply to the collector circuit, the equivalent ckt. will be as follows:



$$V_o = \frac{(R_{C, Th} + R_L)}{\sqrt{X_{C_2}^2 + (R_{C, Th} + R_L)^2}} V_{C, Th}$$

If $X_{C_2} = R_{C_{th}} + R_L$ then we have cut-off freq. due to C_2 .

$$\frac{1}{2\pi f \cdot C_2} = R_{C_{th}} + R_L$$

$$f_{L, C_2} = \frac{1}{2\pi (R_{C_{th}} + R_L) C_2}$$

Example:

$$R_1 = 10\text{ k}\Omega ; R_2 = 1.5\text{ k}\Omega$$

$$R_c = 1.3\text{ k}\Omega ; R_E = 200\Omega ; R_L = 10\text{ k}\Omega$$

$$R_s = 50\Omega$$

$$C_1 = 10\mu\text{F} = C_2 ; \beta = 100 ; V_{CC} = 12\text{V}$$

$$r_e' = \frac{25\text{mV}}{I_E} = 1.98\Omega \approx 2\Omega$$

Let calculate cut-off freq. due to C_1

$$f_{L, C_1} = \frac{1}{2\pi (R_s + \underbrace{R_1 \parallel R_2 \parallel \beta r_e'}_{}) C_1}$$

$$\beta r_e' = 100 \times 2\Omega = 200\Omega$$

$$\text{Now, } 200\Omega \parallel 1500\Omega = \frac{200\Omega \times 1500\Omega}{200\Omega + 1500\Omega}$$

$$\approx 173.4 \Omega$$

$$\text{Also, } 173.4 \Omega \parallel 10 \text{ k}\Omega = \frac{173.4 \Omega \times 10 \text{ k}\Omega}{173.4 \Omega + 10 \text{ k}\Omega}$$

$$\approx 170.4 \Omega$$

$$\Rightarrow R_1 \parallel R_2 \parallel \beta r_{e'} \approx 170.4 \Omega$$

$$\Rightarrow f_{L, C_1} = \frac{1}{2 \times (3.14) \times (50 \Omega + 170.4 \Omega) \times 10^6 \text{ Hz}}$$

$$\Rightarrow f_{L, C_1} = \frac{10^6}{6.28 \times 220.4 \times 10} = 72.5 \text{ Hz}$$

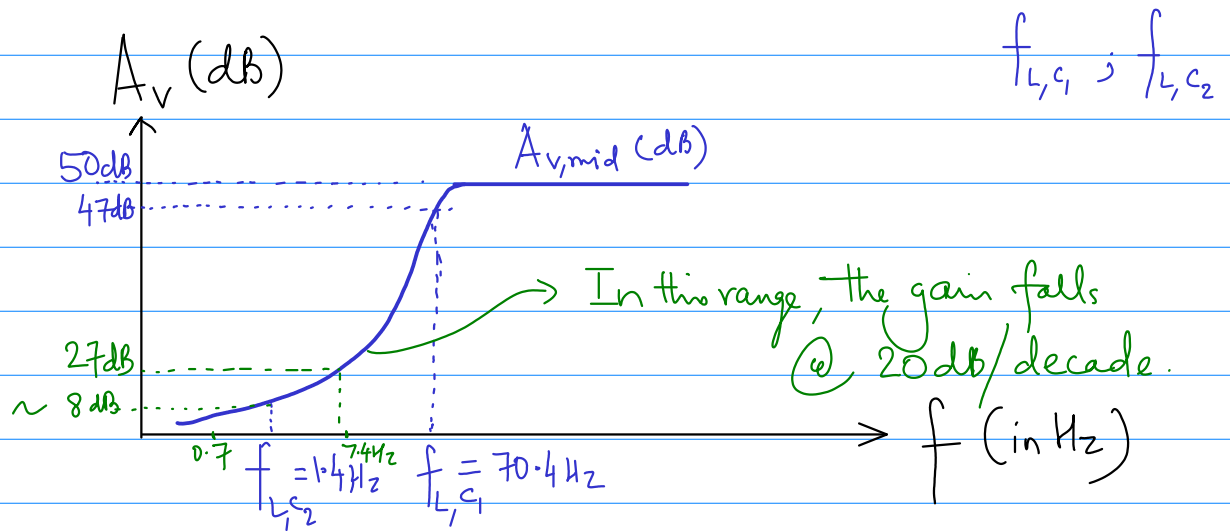
$$f_{L, C_1} = 72.5 \text{ Hz}$$

Let's calculate lower cut-off freq. due to C_2

$$f_{L, C_2} = \frac{1}{2\pi (R_c + R_L) \cdot C_2}$$

$$= \frac{1}{2 \times (3.14) \times (1.3 \text{ k}\Omega + 10 \text{ k}\Omega) \times 10^6 \text{ Hz}}$$

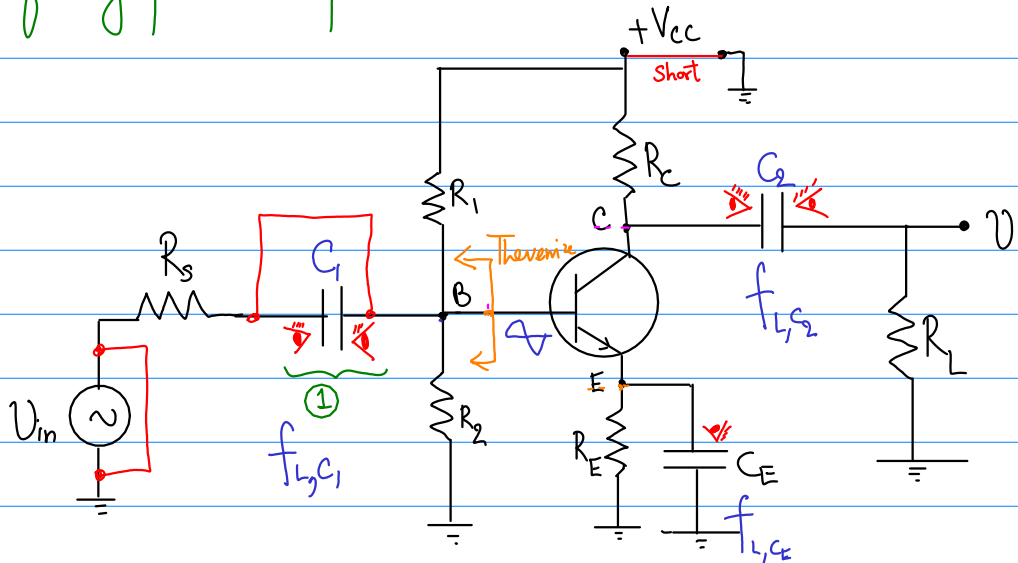
$$f_{L, C_2} = 1.4 \text{ Hz}$$



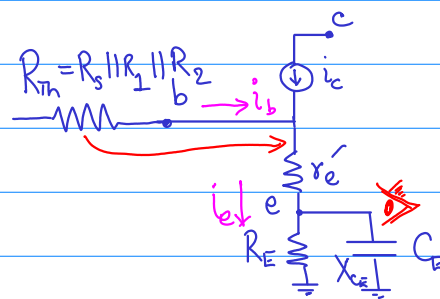
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③ Effect of By-pass Capacitor :



ac-equivalent ckt:



$$i_c = \beta i_b$$

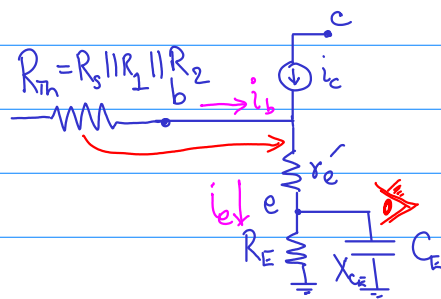
$$i_c \approx i_e \Rightarrow i_c = \beta i_b$$

$$\Rightarrow R_{eq. (emitter)} = \frac{R_E}{\beta}$$

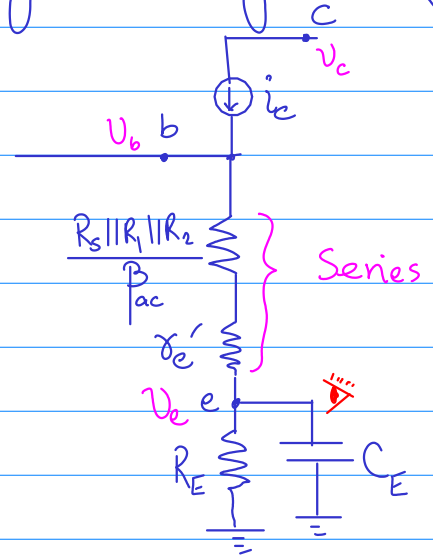
Step 1 : Thevenize from B towards input of the Base terminal.

$$R_{Th} = R_s \parallel R_1 \parallel R_2$$

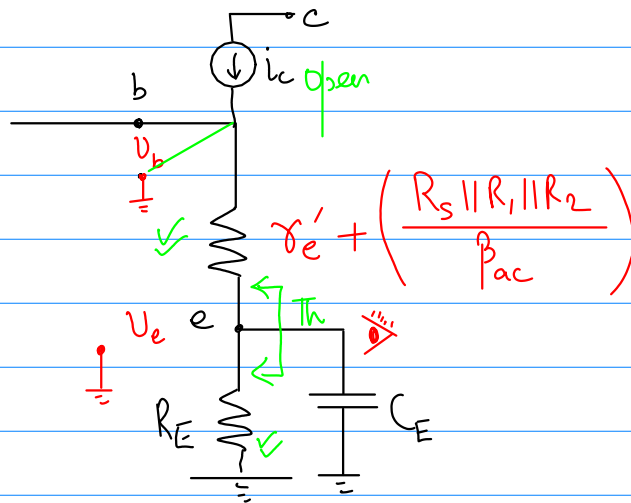
Step 2 : Bring the equivalent resistance (R_{Th}) to the emitter terminal by dividing it by β_{ac}



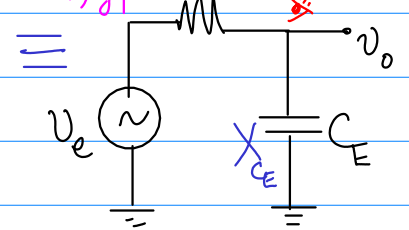
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$$R_{eq, \text{By-pass}} = R_E \parallel \left[r_e' + \left(\frac{R_s \parallel R_1 \parallel R_2}{\beta_{ac}} \right) \right]$$



From this equivalent ckt.

$$V_o = \frac{X_{C_E}}{\sqrt{X_{C_E}^2 + R_{eq, \text{By-pass}}^2}} V_e$$

if $X_{C_E} = R_{eq, \text{By-pass}}$

\Rightarrow cut-off frequency due to bypass Capacitor.

$$\frac{1}{2\pi f_{L, \text{By-pass}} C_E} = R_{eq, \text{By-pass}}$$

$$\Rightarrow f_{L, \text{By-pass}} = \frac{1}{2\pi R_{eq, \text{By-pass}} C_E}$$

Overall low frequency response:

We have three cut-offs

$$f_{L, C_1} ; f_{L, C_2} ; f_{L, C_E}$$

Among the three the highest one is the lower cut-off frequency!

