

# BJT Amplifier : DC and AC Analysis

## Common Emitter (CE) Amplifier Circuit :

Capacitors :

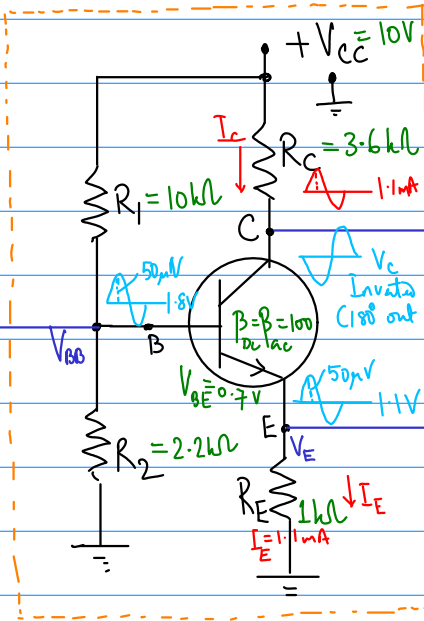
- OPEN for DC
- SHORT for AC

$$V_{BB} = \frac{R_2}{R_1 + R_2} \cdot V_{CC} =$$

$$V_{CE} = 6.04 - 1.1V$$

$$V_{CE} = 4.96V$$

$$V_{in} = V_1 \sin \omega t$$



$$V_C = V_{CC} - I_C R_C = 10V - (1.1mA)(3.6k\Omega) = 6.04V$$

$$V_O = V_2 \sin \omega t$$

$$V_1 = 100\mu V (pp)$$

$$V_2 = 100mV (pp)$$

$$A_v = \frac{V_2}{V_1} = \frac{100mV}{100\mu V}$$

$$A_v = \frac{V_2}{V_1} = 1000$$

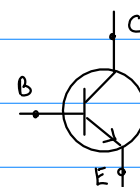
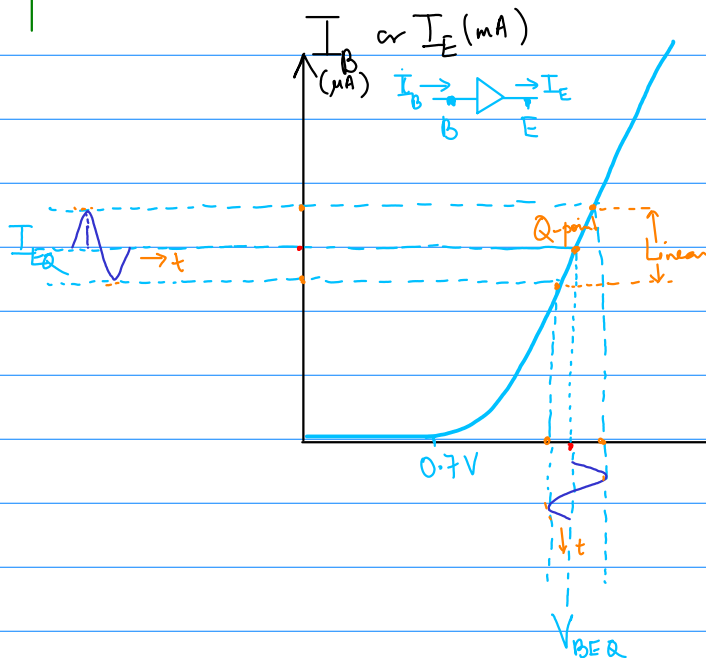
DC Biasing ckt.

- Voltage gain = 1000

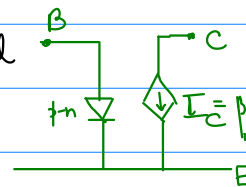
## Input Characteristics:

$$I_E = I_B + I_C ; I_E \approx I_C$$

$$I_E \propto I_B$$



dc-model



at any instant of time 't';

$$\underbrace{i_E}_{\text{Total-current}} = \underbrace{I_{EQ}}_{\text{DC-current}} + \underbrace{i_e}_{\text{ac-current}}$$

$$\underbrace{V_{BE}}_{\text{Total Base-Emitter voltage}} = \underbrace{V_{BEQ}}_{\text{DC}} + \underbrace{v_{be}}_{\text{ac}}$$

Here, we define ac-resistance of the base-emitter diode:

AC-Emitter Resistance  $r_e' = \frac{v_{be} \text{ (ac-voltage)}}{i_e \text{ (ac-current)}}$  → prime indicates that the resistance is within the transistor ie, virtual.

example at instant of time  $\left. \begin{array}{l} v_{be} = 5\text{mV} \\ i_e = 200\mu\text{A} \end{array} \right\} r_e' = \frac{5\text{mV}}{200\mu\text{A}} = 25\Omega$

Note: There is a standard formula to determine the value of ac-emitter resistance ( $r_e'$ , resistance of the base-emitter diode)

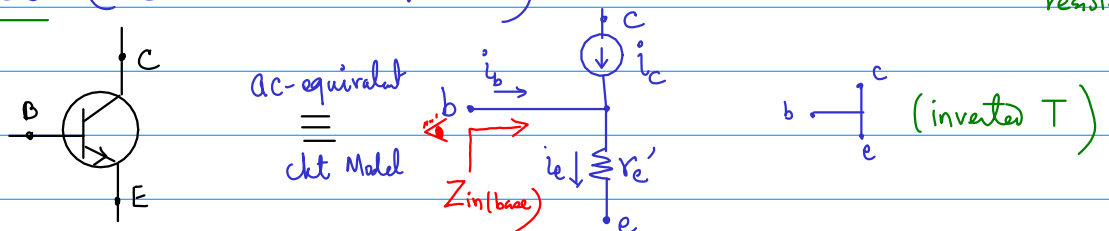
$$r_e' = \frac{25\text{mV}}{I_E}$$

where,  $I_E$  is the dc emitter-current.

## Transistor ac circuit Model:

①

T-model (Eber-Moll Model): Base-Emitter Diode  $\rightarrow$  ac-emitter resistor



$$\underline{i_e \approx i_b + i_c}$$

$$i_e \approx i_c = \beta i_b$$

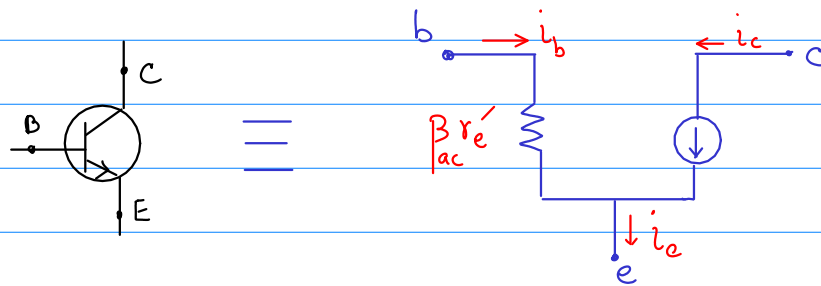
Input impedance as seen from the base terminal

$$Z_{in(base)} = \frac{V_{be}}{i_b} = \frac{i_e r'_e}{i_b} = \frac{i_e r'_e}{i_b} = \beta r'_e$$

$$\text{where, } \beta = \frac{i_c}{i_b}$$

$$Z_{in(base)} = \beta r'_e$$

② TT-Model : (Visual representation of  $Z_{in(base)}$ )



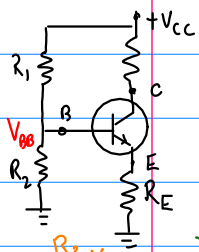
Note: Both the T- and  $\Pi$ -model are equivalent <sup>ac-</sup>circuit model of a BJT.

Analysis of a given CE Amplifier (Both DC & AC).

We apply Superposition theorem:

That is, effect of each sources acting alone is added to get the total effect of all the sources acting simultaneously.

## ① DC-Analysis : DC-equivalent ckt.



$$\begin{aligned} V_{BB} &= \frac{R_2 V_{CC}}{R_1 + R_2} \\ V_E &= V_{BB} - V_{BE} \\ I_E &= \frac{V_E}{R_E} \\ I_C &\approx I_E \\ V_C &= V_{CC} - I_C R_C \\ V_{CE} &= V_C - V_E \end{aligned}$$

Q-point  
( $V_{CEQ}, I_{CQ}$ )

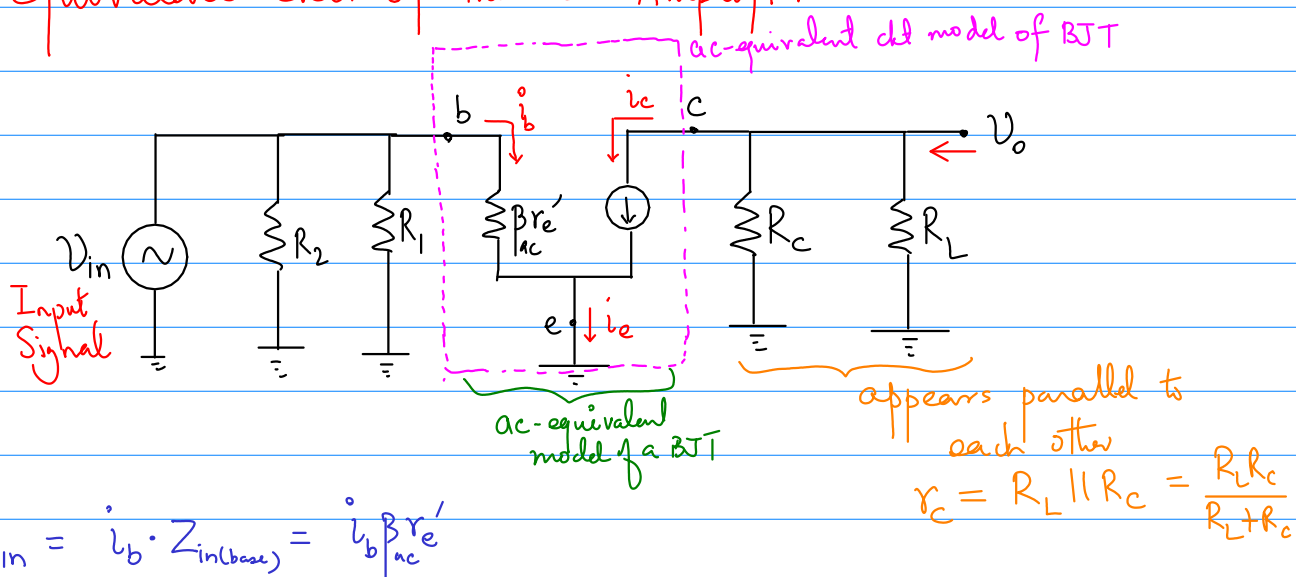
- Mentally "OPEN" all the capacitors.
- Determine the operating point (Q-point).  
-  $V_{CEQ}, I_{CQ}$ .

- Determine  $r_e' = \frac{25mV}{I_E}$   
ac-emitter resistance

## ② AC-Analysis :

- for all ac current, dc source acts as a 'SHORT'
- 'SHORT' all the capacitors
- Replace the BJT with equivalent ac-ckt model (ie, either T or  $\pi$ )!

ac-equivalent ckt. of the CE Amplifier:



$$V_o = i_c r_c$$

We define voltage gain  $A_v = \frac{V_o}{V_{in}} = \frac{i_c r_c}{i_b \beta_{ac} r_e'}$

Voltage Gain

$$A_v = \frac{r_c}{r_e'}$$

$$r_c = R_c \parallel R_L$$

$$r_e' = \frac{25 \text{ mV}}{I_E}$$

From the given CE amplifier ckt.

$$r_c = \frac{R_L \cdot R_c}{R_L + R_c} = \frac{10 \text{ k}\Omega \times 3.6 \text{ k}\Omega}{10 \text{ k}\Omega + 3.6 \text{ k}\Omega}$$

$$r_c = \frac{36 (\text{k}\Omega)^2}{13.6 \text{ k}\Omega} = 2.64 \text{ k}\Omega$$

$$\text{Now, } r_e' = \frac{25 \text{ mV}}{I_E} = \frac{25 \text{ mV}}{1.1 \text{ mA}} = 22.7 \Omega$$

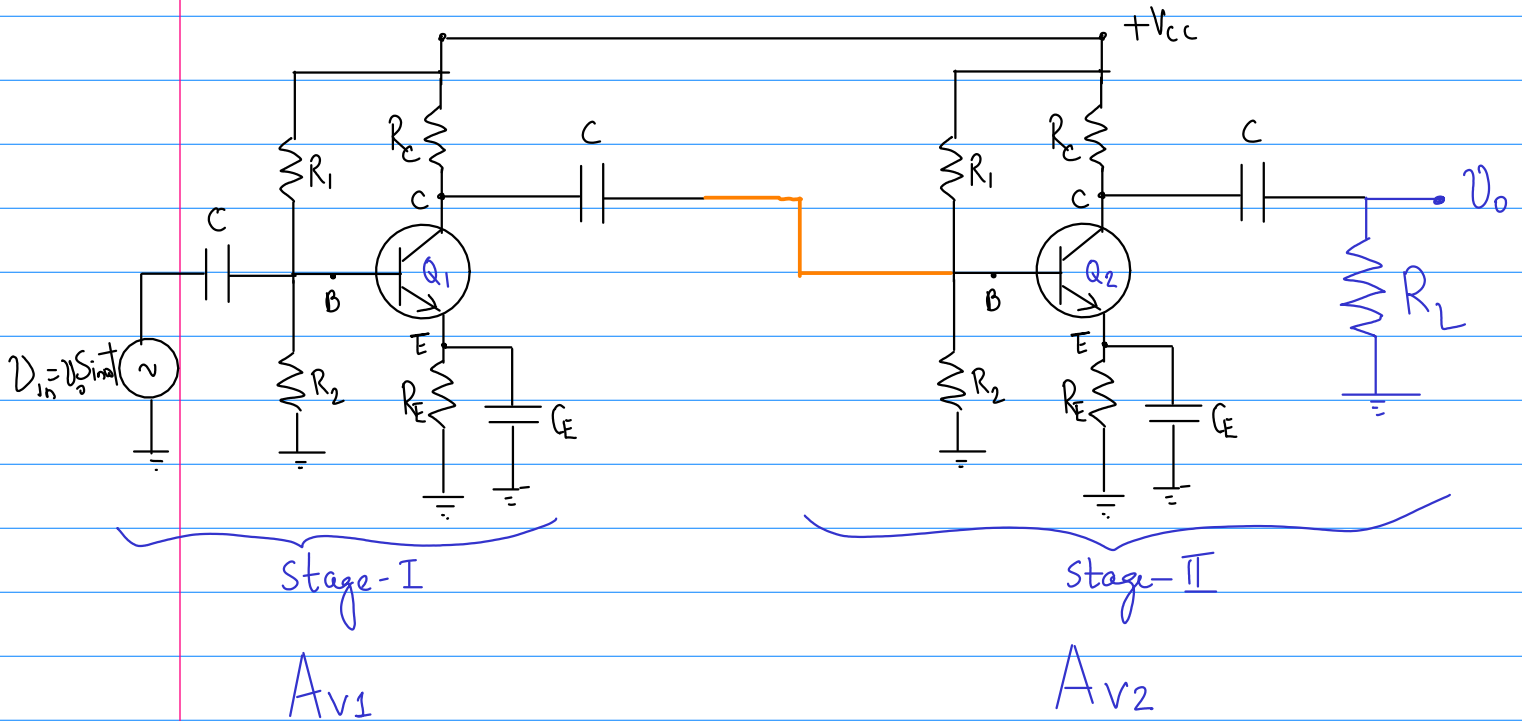
$$\text{Therefore, Voltage gain } A_v = \frac{r_c}{r_e'} = \frac{2.64 \text{ k}\Omega}{22.7 \Omega}$$

$$A_v = \frac{2640}{22.7} \approx 110$$

$$A_v = 110$$

$\Rightarrow$  If the amplitude of the input signal is  $1 \mu\text{V}$  then the amp. of output signal is  $110 \mu\text{V}$  ( $0.11 \text{ mV}$ )

Multi-stage (eg. two-stage) direct coupled CE Amplifier:



Overall voltage gain  $A_v = A_{v1} \cdot A_{v2}$

Similarly, if there 'n' no. of stages, we have

$$A_v = A_{v1} \cdot A_{v2} \cdot A_{v3} \dots A_{vn}$$

Let  $A_{v1} = 100$  &  $A_{v2} = 200$

$$A_v = 100 \times 200 = 20000 = 2 \times 10^4$$

Also, voltage gain is defined in unit of decibel (dB)

$$A_v(\text{dB}) = 20 \log_{10} A_v$$

If  $A_v = 100$  then

$$A_v(\text{dB}) = 20 \log_{10} 100 = 40 \text{ dB}$$

Similarly, if  $A_v = 1000$

$$A_v(\text{dB}) = 20 \log_{10} 1000 = 60 \text{ dB}$$

For  $A_v = 120 \text{ dB}$ ,  $A_v = 10^6$

$$\Rightarrow \frac{V_o}{V_{in}} = 10^6$$

Determine  $A_v$  if the  $A_v(\text{indB}) = 3 \text{ dB}$

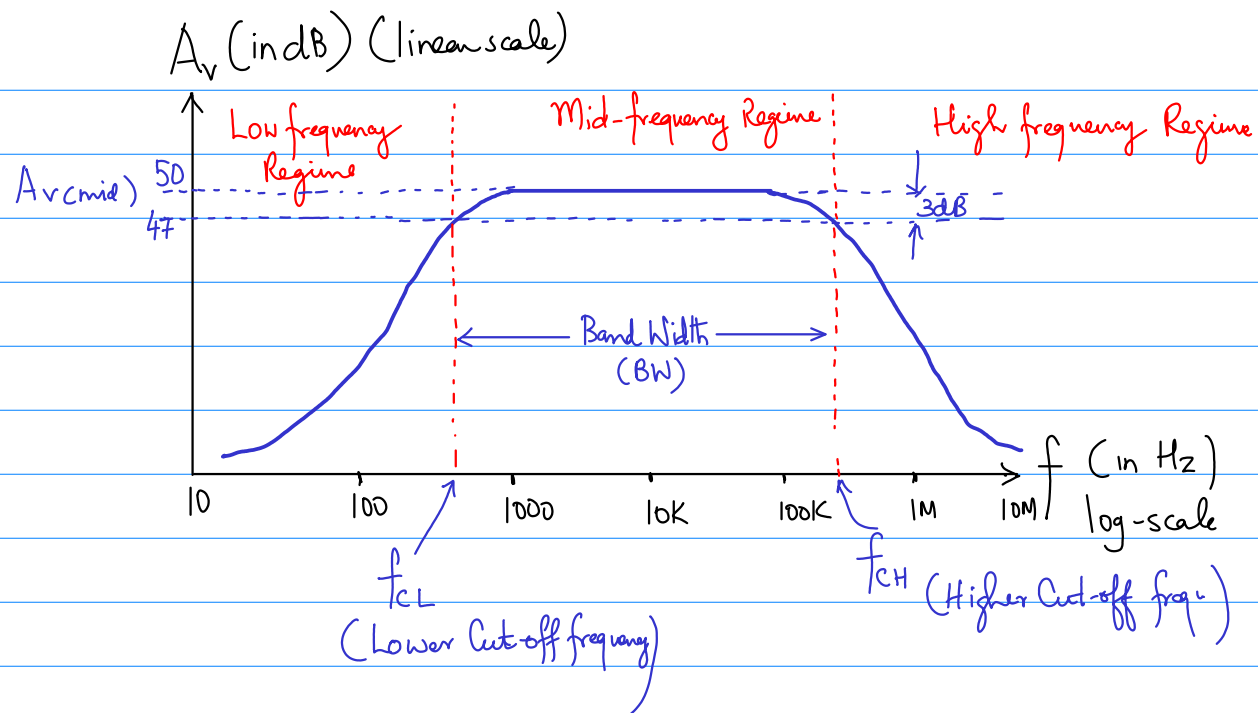
$$A_v = 1.414 = \sqrt{2}$$

$$\frac{V_o}{V_{in}} = \sqrt{2} \Rightarrow V_o = \sqrt{2} V_{in}$$

Determine  $A_v$  if the  $A_v(\text{indB}) = -3 \text{ dB}$

$$A_v = 0.707 = \frac{1}{\sqrt{2}}$$

$$\frac{V_o}{V_{in}} = \frac{1}{\sqrt{2}} \Rightarrow V_o = \frac{1}{\sqrt{2}} V_{in}$$



$$B.W = f_{CH} - f_{CL} \quad \text{--- (1)}$$

$$(Gain) \times (B.W) = \text{Constant} \quad \text{--- (2)}$$