## Assignment 02

- 1. Suppose a voltage source has an ideal voltage of 5V and an internal resistance of  $1\Omega$ . For what values of the lead resistance will the voltage source appear stiff?
- 2. Suppose a current source has an ideal current of 100 mA and an internal resistance of 50 MΩ. For what values of load resistance will the current source appear stiff?
- 3. The internal resistance of a voltage source equals  $0.1\Omega$ . How much voltage is dropped across this internal resistance when the current through it equals 5 A?
- 4. If the ideal current of a current source is 5 mA and the internal resistance is 250 k $\Omega$ , determine the load current flowing through the load resistor 10 k $\Omega$ . Is this a stiff current source?
- 5. Design a hypothetical current source using a battery and a resistor. The current source must meet the following specifications: It must supply a stiff 1 mA of current to any load resistance between 0 and 10 k $\Omega$ .
- 6. Somebody hands you a black box with a 2  $k\Omega$  resistor connected across the exposed load terminal. How can you measure the Thevenin voltage? Now suppose the black box has a knob on it that allows you to reduce all internal voltage and current sources to zero. How can you measure the Thevenin resistance?
- 7. Determine the Thevenin voltage and Thevenin resistance of the given circuit in Fig. 1. What would happen when the voltage source is decreased to 12 V? Calculate the load current for the load resistance of 1  $k\Omega$ .

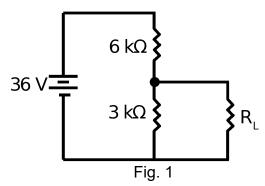
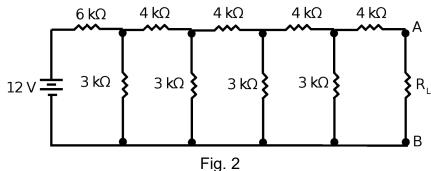
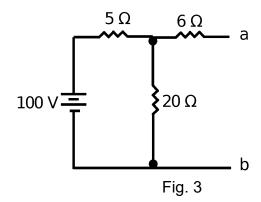


Fig. 2

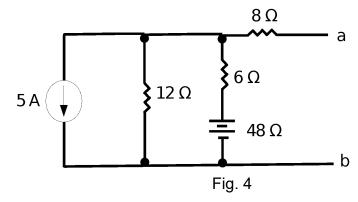
- 8. How do you analyze the circuit of Fig. 1 using Norton's theorem?
- 9. Calculate the load current in Fig. 2 for each of the load resistance: 1 k $\Omega$ , 3 k $\Omega$ , 4 k $\Omega$ , 5 k $\Omega$ , 6 k $\Omega$ .



10. What resistor draws a current of 5 A when connected across the terminals *a* and *b* of the circuit shown in Fig. 3.



11. Find the Thevenin equivalent of the circuit shown in Fig. 4.



12. Find the Thevenin equivalent of the circuit shown in Fig.5.

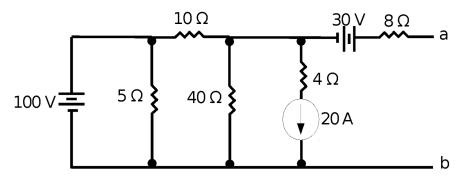


Fig. 5