

# Bassel Alesh

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<b>EDUCATION</b>	<b>Bachelor of Science in Electrical Engineering</b> University of Illinois at Urbana-Champaign	<b>GPA: 3.86/4.00</b> August 2014 - December 2017
	<b>Relevant Coursework</b> Wireless Communication Systems Microwave Circuits and Devices Digital Systems Lab Electronic Circuits (+ Lab) Data Structures	Analog IC Design Automated Microwave Measurements Fields and Waves (I & II) Digital Signal Processing Active Microwave Circuit Design
<b>SKILLS</b>	<i>Languages:</i> Python, C, C++, SystemVerilog, Bash, MATLAB. <i>Software:</i> ADS, Cadence Virtuoso, EAGLE, HFSS, PSPICE. <i>Lab:</i> VNAs, Spectrum Analyzers, Signal Generators, Oscilloscopes, Multimeters.	
<b>EXPERIENCE</b>	<b>RF Systems, Algorithms, and Calibration Intern</b> <i>Apple Inc.</i>	January 2018 - August 2018 Cupertino, CA
	<b>Digital Hardware Engineering Intern</b> <i>Qualcomm Inc.</i>	May 2017 - August 2017 San Diego, CA
	<ul style="list-style-type: none"><li>• Worked on the PLL of a transceiver chip for the RFIC Digital Design team.</li><li>• Extracted the RLCK parasitics of the digital modules' nets using RaptorX.</li><li>• Created a testbench using Cadence Virtuoso that simulated signal delays along the extracted model and reported the results in the design review.</li><li>• Wrote a Bash script that maintains version control for different tools' runs.</li></ul>	
	<b>Undergraduate Research Assistant</b> <i>University of Illinois at Urbana-Champaign</i>	September 2016 - December 2017 Champaign, IL
	<ul style="list-style-type: none"><li>• Working under Professor Jose Schutt-Aine and Professor Andreas Cangellaris on projects in computational electromagnetics using Python.</li></ul>	
	<b>Undergraduate Grader for Fields &amp; Waves I</b> <i>University of Illinois at Urbana-Champaign</i>	September 2016 - May 2017 Champaign, IL
	<ul style="list-style-type: none"><li>• Homework grader and review session organizer for ECE 329. Topics include Maxwell's equations, transmission line theory, and Smith Chart applications.</li></ul>	
	<b>Product Development Intern</b> <i>AT&amp;T Inc.</i>	May 2016 - August 2016 Atlanta, GA
	<ul style="list-style-type: none"><li>• Worked with an LTE modem board purposed for AT&amp;T's IoT platform.</li><li>• Tested the board's UART, GPIO pins and more using AT Commands.</li><li>• Designed a testing shield that for an LTE modem board using EAGLE.</li></ul>	
<b>EXTRA-CURRICULAR ACTIVITIES</b>	Electromagnetics Playground, <i>Lab Instructor</i> ECE Student Advancement Committee, <i>Representative</i> PULSE, <i>Media &amp; Design Director</i> Eta Kappa Nu, <i>ECE 329 Review Session Instructor</i> Illini Formula Electric, <i>Low-Voltage Team Member</i>	August 2017 - December 2017 August 2016 - December 2017 May 2015 - May 2017 February 2017 - May 2017 August 2015 - May 2016
<b>HONORS &amp; AWARDS</b>	ECE 483 (Analog IC Design) Low Dropout Regulator Design - 2 <sup>nd</sup> Place Floyd E. Lundgren Scholarship Ellery B. Paine Outstanding Junior Award ECE Visionary Award James Scholar, Dean's List	2017 2017 2017 2017 2015-2017