

Bassel Alesh

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OBJECTIVE	To obtain a hands-on internship or co-op in RF Hardware Design.		
EDUCATION	University of Illinois at Urbana-Champaign B.S. in Electrical Engineering, expected graduation of December 2017 GPA: 3.84/4.00		
	Relevant Coursework [F17 = Fall 2017 Coursework] Wireless Communication Systems Analog IC Design Microwave Circuits and Devices Automated Microwave Measurements Digital Systems Lab Fields and Waves (I & II) Electronic Circuits (+ Lab) Computer Systems & Programming Digital Signal Processing Semiconductor Electronics [F17] Active Microwave Circuit Design [F17] Data Structures [F17]		
SKILLS	<i>Languages:</i> Python, SystemVerilog, C, MATLAB. <i>Software:</i> ADS, Cadence Virtuoso, EAGLE, HFSS, PSPICE. <i>Lab:</i> VNAs, Signal Generators, Oscilloscopes, Multimeters, Soldering.		
EXPERIENCE	Digital Hardware Engineering Intern <i>Qualcomm Inc.</i>	May 2017 - August 2017 San Diego, CA	<ul style="list-style-type: none">• Working on the PLL of a wireless transceiver for the RFIC Digital Design team.• Analyzing the delays along its nets using Cadence Virtuoso and RaptorX.
	Undergraduate Research Assistant <i>University of Illinois at Urbana-Champaign</i>	September 2016 - Present Champaign, IL	<ul style="list-style-type: none">• Currently creating models for transmission lines and their designated coupling behavior using Python for quicker simulations of large-scale systems.
	Undergraduate Grader for Fields & Waves I <i>University of Illinois at Urbana-Champaign</i>	September 2016 - Present Champaign, IL	<ul style="list-style-type: none">• Homework grader for ECE 329: Fields & Waves I. Topics include Maxwell's equations, transmission line theory, and Smith Chart applications.
	Product Development Intern <i>AT&T Inc.</i>	May 2016 - August 2016 Atlanta, GA	<ul style="list-style-type: none">• Worked with an LTE modem board purposed for IoT applications and recorded any bugs, along with tests that found them, in preparation AT&T's Shape Expo.• Tested the board's UART, GPIO pins and more using AT Commands.• Designed a testing shield for an LTE modem board using EAGLE.
EXTRA-CURRICULAR ACTIVITIES	ECE Student Advancement Committee, <i>Junior Rep</i> PULSE, <i>Media & Design Director</i> Illini Formula Electric, <i>Low-Voltage Team Member</i>	September 2016 - Present May 2015 - May 2017 September 2016 - May 2016	
HONORS & AWARDS	ECE 483 (Analog IC Design) Low Dropout Regulator Design - 2 nd Place Ellery B. Paine Outstanding Junior Award ECE Visionary Award James Scholar Dean's List	2017 2017 2017 2015-2017 2015-2017	