

# Bassel Alesh

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501 South Sixth St., Apt. 209  
Champaign, IL  
(217) 819-7610  
alesh2@illinois.edu

<b>OBJECTIVE</b>	To obtain an internship or co-op in RF/Analog Hardware Design.		
<b>EDUCATION</b>	University of Illinois at Urbana-Champaign Bachelor of Electrical Engineering, expected graduation December 2017 GPA: 3.87/4.0  <b>Relevant Coursework</b> [F17 = Fall 2017 Coursework] Wireless Communication Systems      Analog IC Design Microwave Circuits and Devices      Automated Microwave Measurements Digital Systems Lab      Fields and Waves (I & II) Electronic Circuits (+ Lab)      Computer Systems & Programming Digital Signal Processing      Analog Signal Processing Active Microwave Circuit Design [F17]      Data Structures [F17]		
<b>SKILLS</b>	<i>Languages:</i> Python, SystemVerilog, C, MATLAB. <i>Software:</i> ADS, Cadence Virtuoso, EAGLE, HFSS, PSPICE. <i>Lab:</i> VNAs, Signal Generators, Oscilloscopes, Multimeters, Soldering.		
<b>EXPERIENCE</b>	<b>Incoming Digital Hardware Engineering Intern</b> <i>Qualcomm Inc.</i> <ul style="list-style-type: none"><li>Digital RF/Analog Team.</li></ul>	May 2017 - August 2017 San Diego, CA	
	<b>Undergraduate Research Assistant</b> <i>University of Illinois at Urbana-Champaign</i> <ul style="list-style-type: none"><li>Currently creating models for transmission lines and their designated behavior under coupling using Python for quicker simulations of large-scale systems.</li></ul>	September 2016 - Present Champaign, IL	
	<b>Undergraduate Grader for Fields &amp; Waves I</b> <i>University of Illinois at Urbana-Champaign</i> <ul style="list-style-type: none"><li>Homework grader for ECE 329: Fields &amp; Waves I. Topics include Maxwell's equations, transmission line theory, and Smith Chart applications.</li></ul>	September 2016 - Present Champaign, IL	
	<b>Product Development Intern</b> <i>AT&amp;T Inc.</i> <ul style="list-style-type: none"><li>Worked with an LTE modem board purposed for IoT applications and recorded any bugs, along with tests that found them, in preparation AT&amp;T's Shape Expo.</li><li>Tested the board's UART, GPIO pins and more using AT Commands.</li><li>Designed a testing shield for an LTE modem board using EAGLE.</li></ul>	May 2016 - August 2016 Atlanta, GA	
<b>EXTRA-CURRICULAR ACTIVITIES</b>	ECE Student Advancement Committee, <i>Junior Rep</i> PULSE, <i>Media &amp; Design Director</i> Illini Formula Electric, <i>Low-Voltage Team Member</i>	September 2016 - Present May 2015 - May 2017 September 2016 - May 2016	
<b>HONORS &amp; AWARDS</b>	ECE 483 (Analog IC Design) Low Dropout Regulator Design - 2 <sup>nd</sup> Place Ellery B. Paine Outstanding Junior Award ECE Visionary Award James Scholar Dean's List	2017 2017 2017 2015-2017 2015-2017	