

# Bassel Alesh

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<b>EDUCATION</b>	<b>Bachelor of Science in Electrical Engineering</b>	<b>GPA: 3.86/4.00</b>
	University of Illinois at Urbana-Champaign	August 2014 - December 2017
<b>Relevant Coursework</b>		
Wireless Communication Systems      Analog IC Design		
Microwave Circuits and Devices      Automated Microwave Measurements		
Digital Systems Lab      Fields and Waves (I & II)		
Electronic Circuits (+ Lab)      Digital Signal Processing		
Data Structures      Active Microwave Circuit Design		
<b>SKILLS</b>	<i>Languages:</i> Python, C, C++, SystemVerilog, Bash, MATLAB.	
	<i>Software:</i> ADS, Cadence Virtuoso, EAGLE, HFSS, PSPICE.	
	<i>Lab:</i> VNAs, Spectrum Analyzers, Signal Generators, Oscilloscopes, Multimeters.	
<b>EXPERIENCE</b>	<b>Wireless OTA/RF Desense &amp; Cal Intern</b>	January 2018 - August 2018 Cupertino, CA
	<b>Digital Hardware Engineering Intern</b>	May 2017 - August 2017 San Diego, CA
	<i>Qualcomm Inc.</i>	
	<ul style="list-style-type: none"><li>Working on the PLL of a transceiver chip for the RFIC Digital Design team.</li><li>Extracted the RLCK parasitics of the digital modules' nets using RaptorX.</li><li>Created a testbench using Cadence Virtuoso that simulated signal delays along the extracted model and reported the results in the design review.</li><li>Wrote a Bash script that maintains version control for different tools' runs.</li></ul>	
	<b>Undergraduate Research Assistant</b>	September 2016 - Present Champaign, IL
	<i>University of Illinois at Urbana-Champaign</i>	
	<ul style="list-style-type: none"><li>Working under Professor Jose Schutt-Aine and Professor Andreas Cangelaris on projects in computational electromagnetics using Python.</li></ul>	
	<b>Undergraduate Grader for Fields &amp; Waves I</b>	September 2016 - May 2017 Champaign, IL
<b>EXTRA-CURRICULAR ACTIVITIES</b>	<i>University of Illinois at Urbana-Champaign</i>	
	<ul style="list-style-type: none"><li>Homework grader and review session organizer for ECE 329. Topics include Maxwell's equations, transmission line theory, and Smith Chart applications.</li></ul>	
	<b>Product Development Intern</b>	May 2016 - August 2016 Atlanta, GA
	<i>AT&amp;T Inc.</i>	
	<ul style="list-style-type: none"><li>Worked with an LTE modem board purposed for AT&amp;T's IoT platform.</li><li>Tested the board's UART, GPIO pins and more using AT Commands.</li><li>Designed a testing shield that for an LTE modem board using EAGLE.</li></ul>	
	Electromagnetics Playground, <i>Lab Instructor</i>	August 2017 - Present
	ECE Student Advancement Committee, <i>Junior Rep</i>	September 2016 - Present
	PULSE, <i>Media &amp; Design Director</i>	May 2015 - May 2017
<b>HONORS &amp; AWARDS</b>	Eta Kappa Nu, <i>ECE 329 Review Session Instructor</i>	February 2017 - May 2017
	Illini Formula Electric, <i>Low-Voltage Team Member</i>	September 2015 - May 2016
	ECE 483 (Analog IC Design) Low Dropout Regulator Design - 2 <sup>nd</sup> Place	2017
	Floyd E. Lundgren Scholarship	2017
	Ellery B. Paine Outstanding Junior Award	2017
	ECE Visionary Award	2017
	James Scholar, Dean's List	2015-2017