

A

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C

D

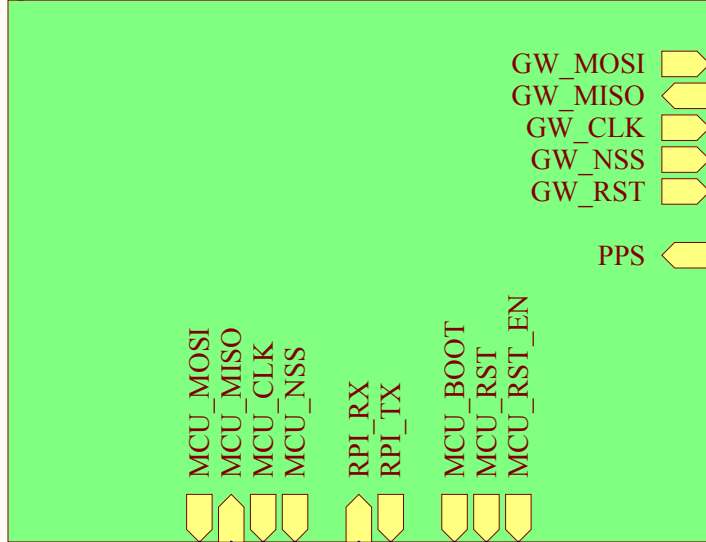
A

B

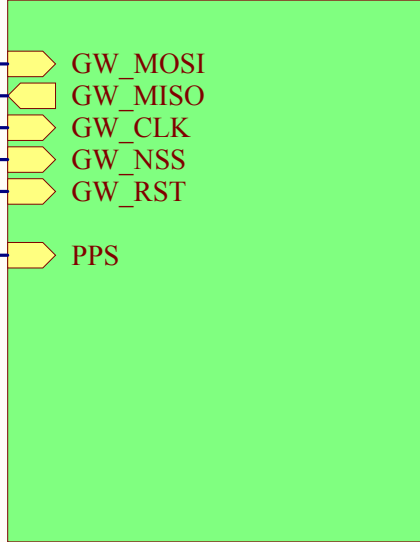
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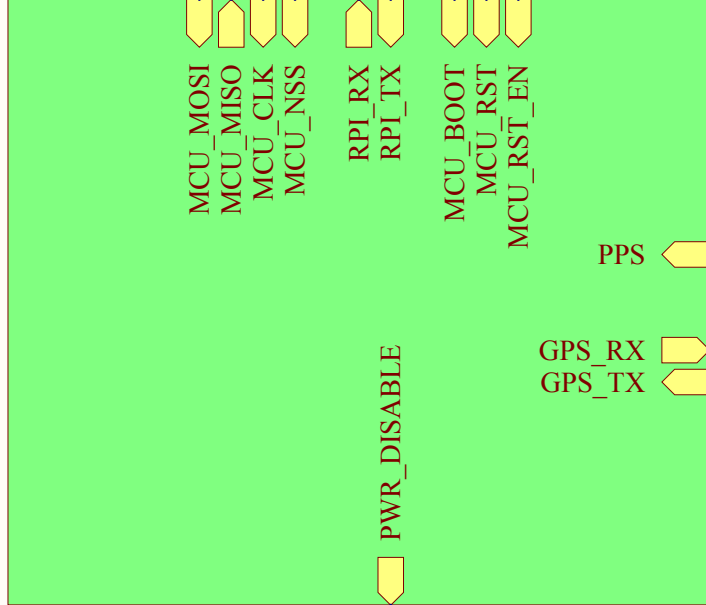
RPi3
rpi.SchDoc



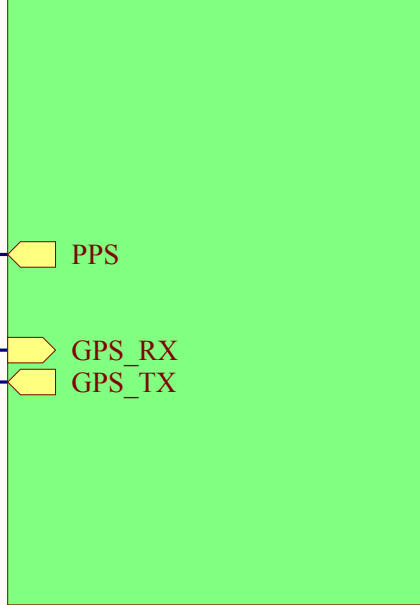
iC880A
ic880a.SchDoc



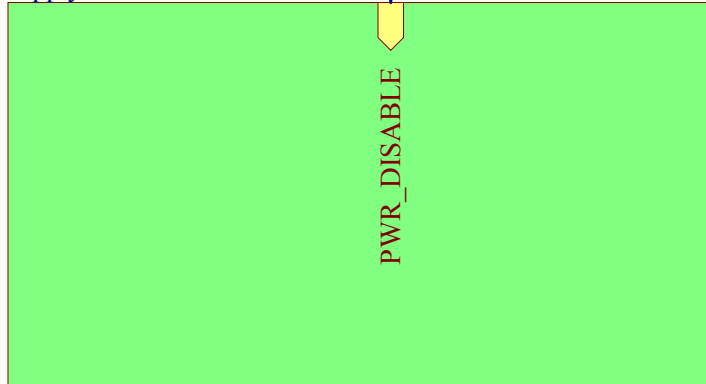
MCU
mcu.SchDoc



Designator
gps2.SchDoc



Supply
supply.SchDoc



MH1

MountHole

MH2

MountHole

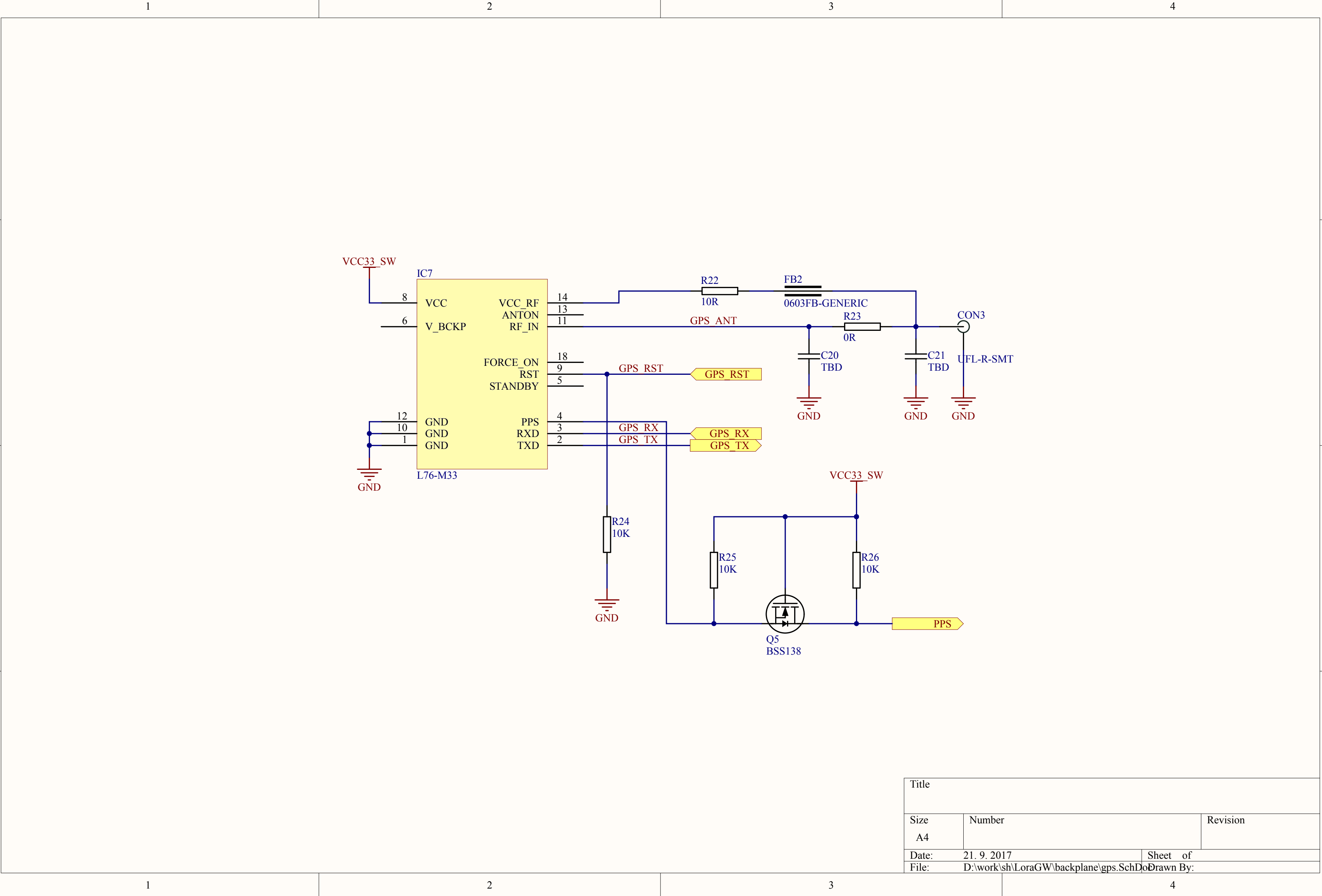
MH3

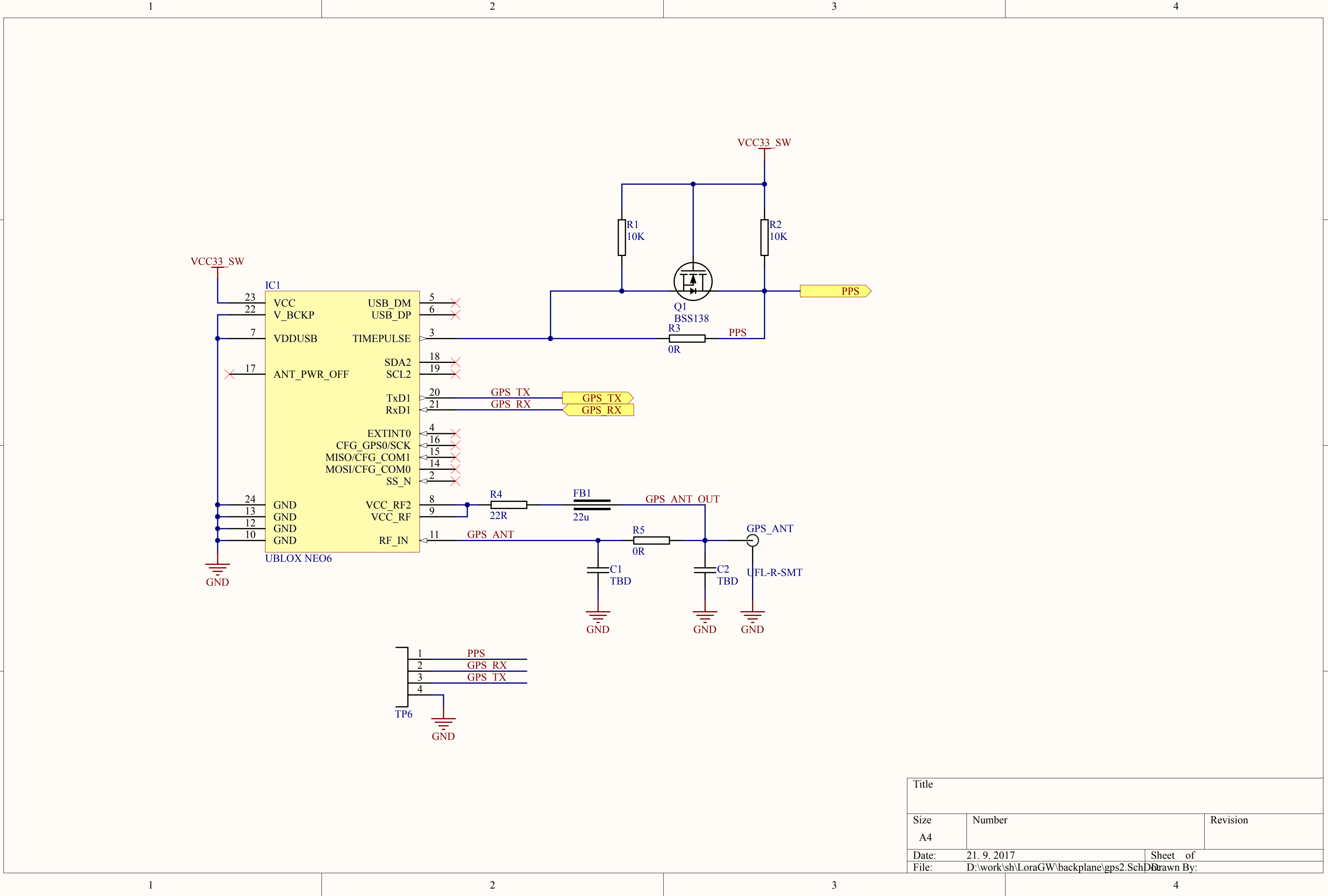
MountHole

MH4

MountHole

Title		
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Date:	21. 9. 2017	Sheet of
File:	D:\work\...\backplane.SchDoc	Drawn By:





A

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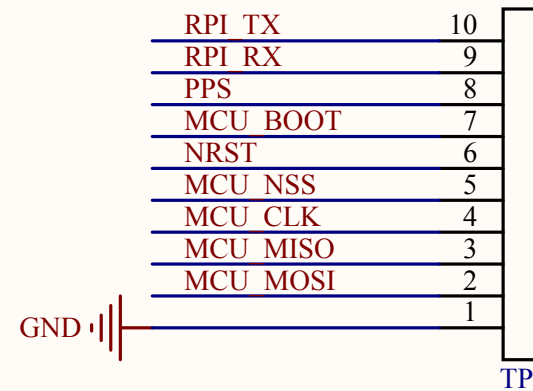
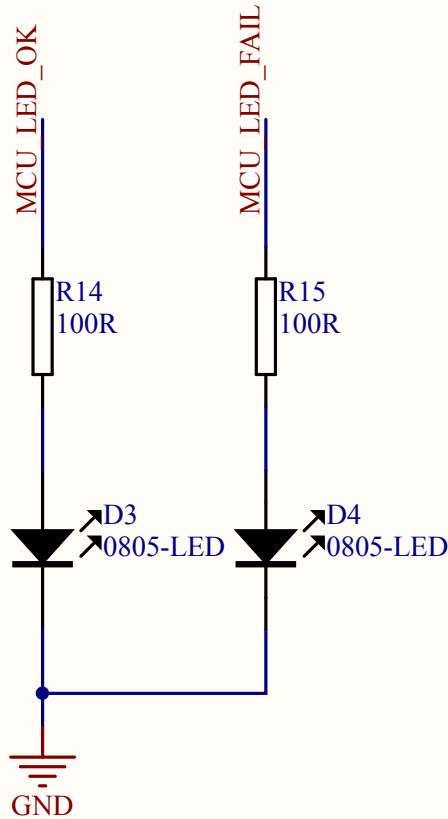
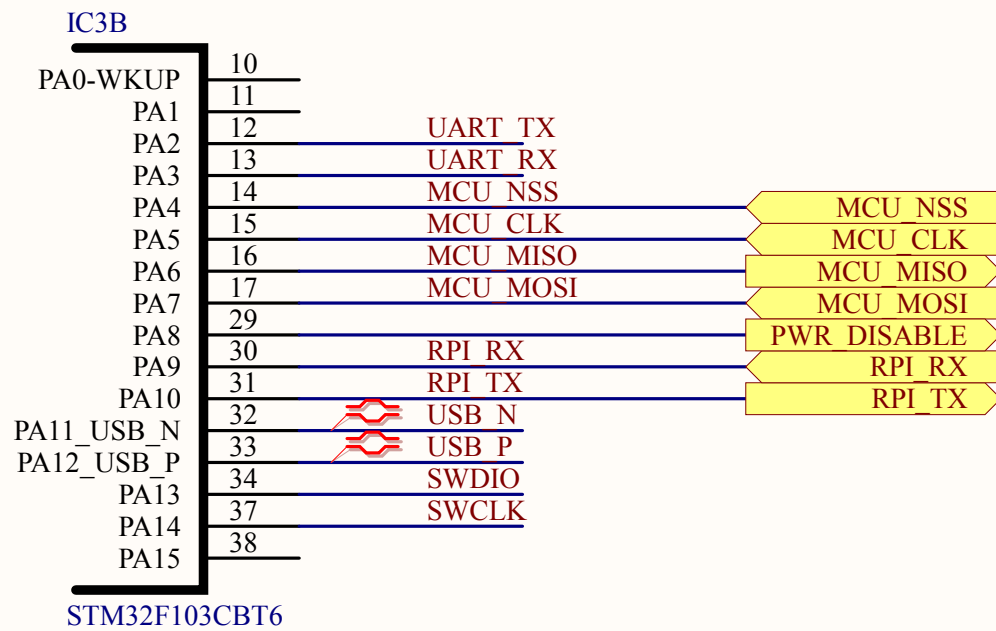
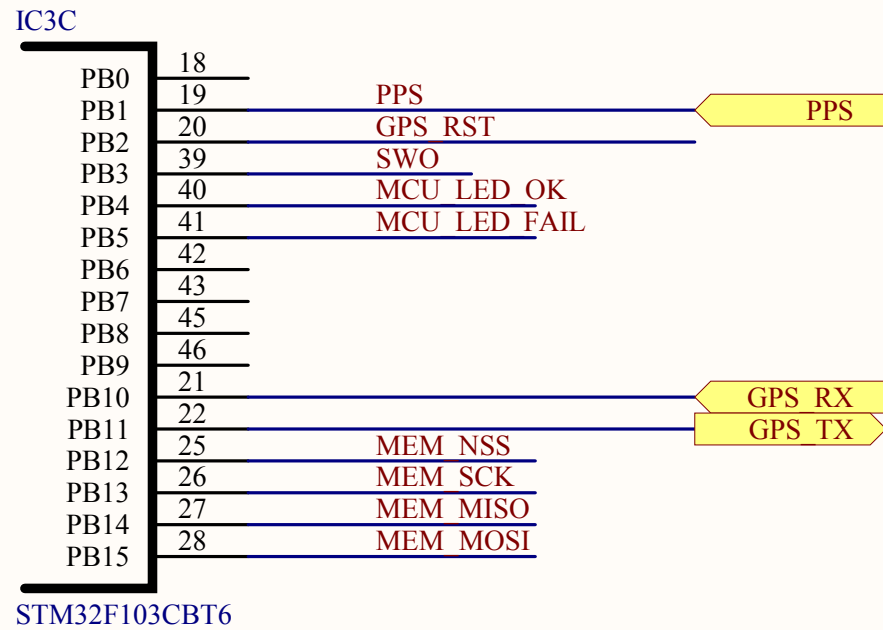
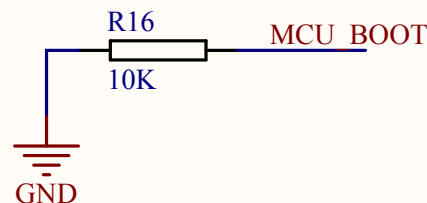
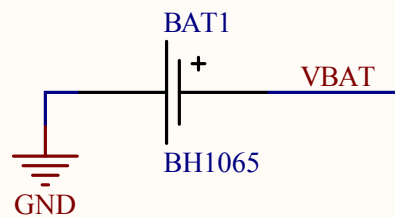
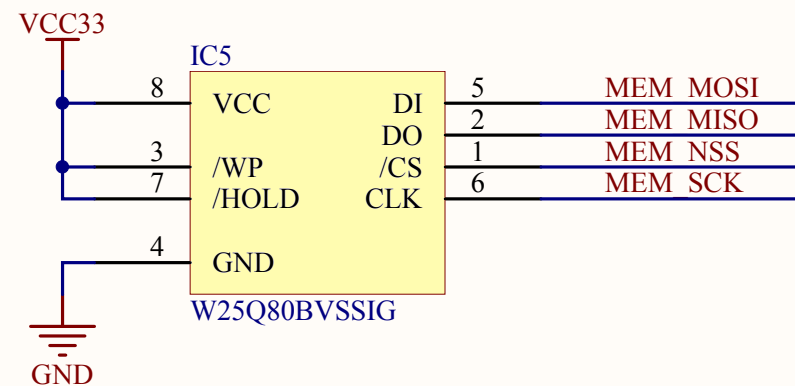
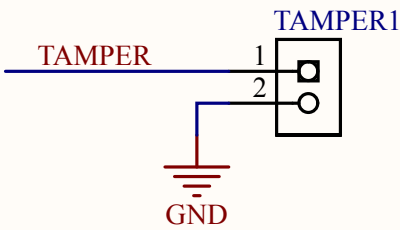
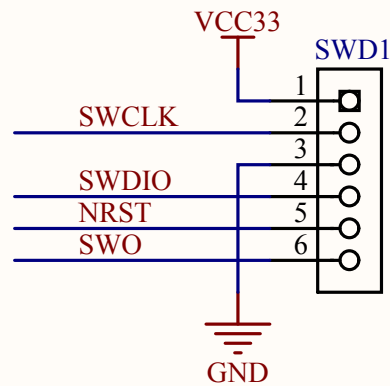
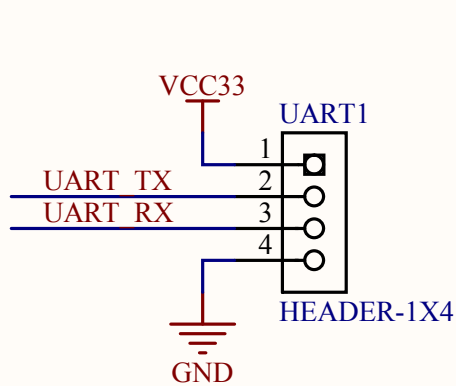
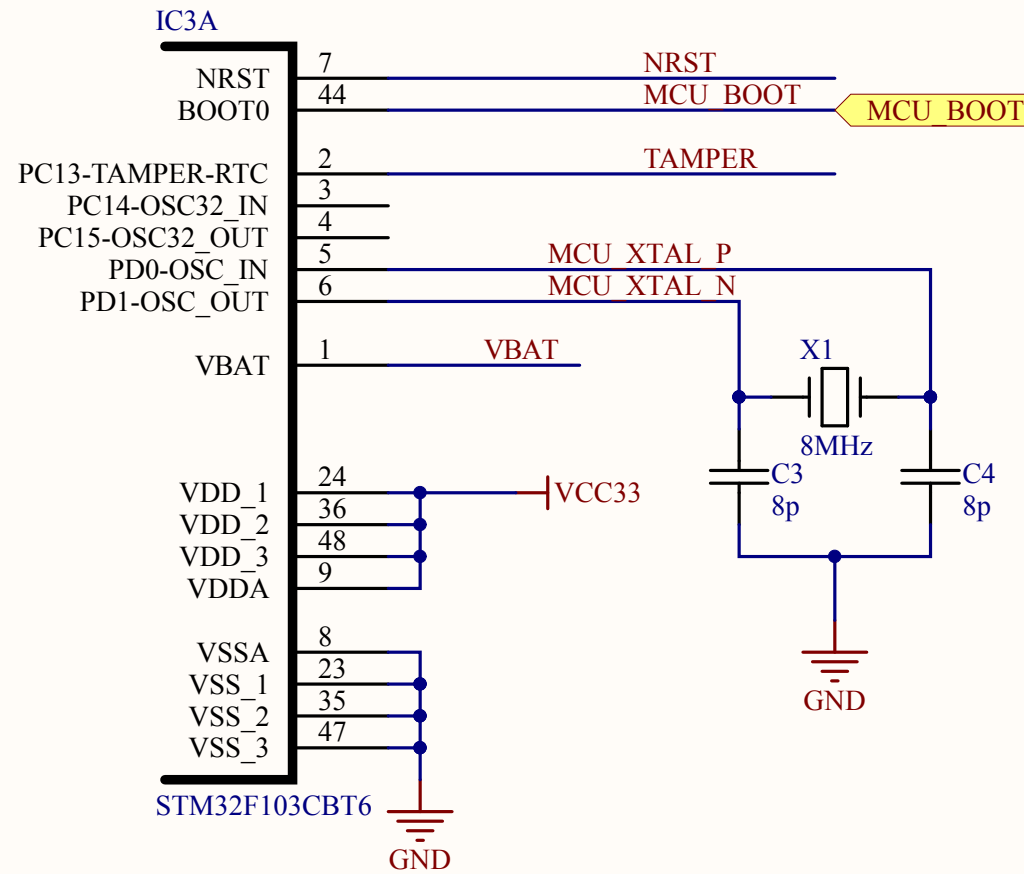
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A

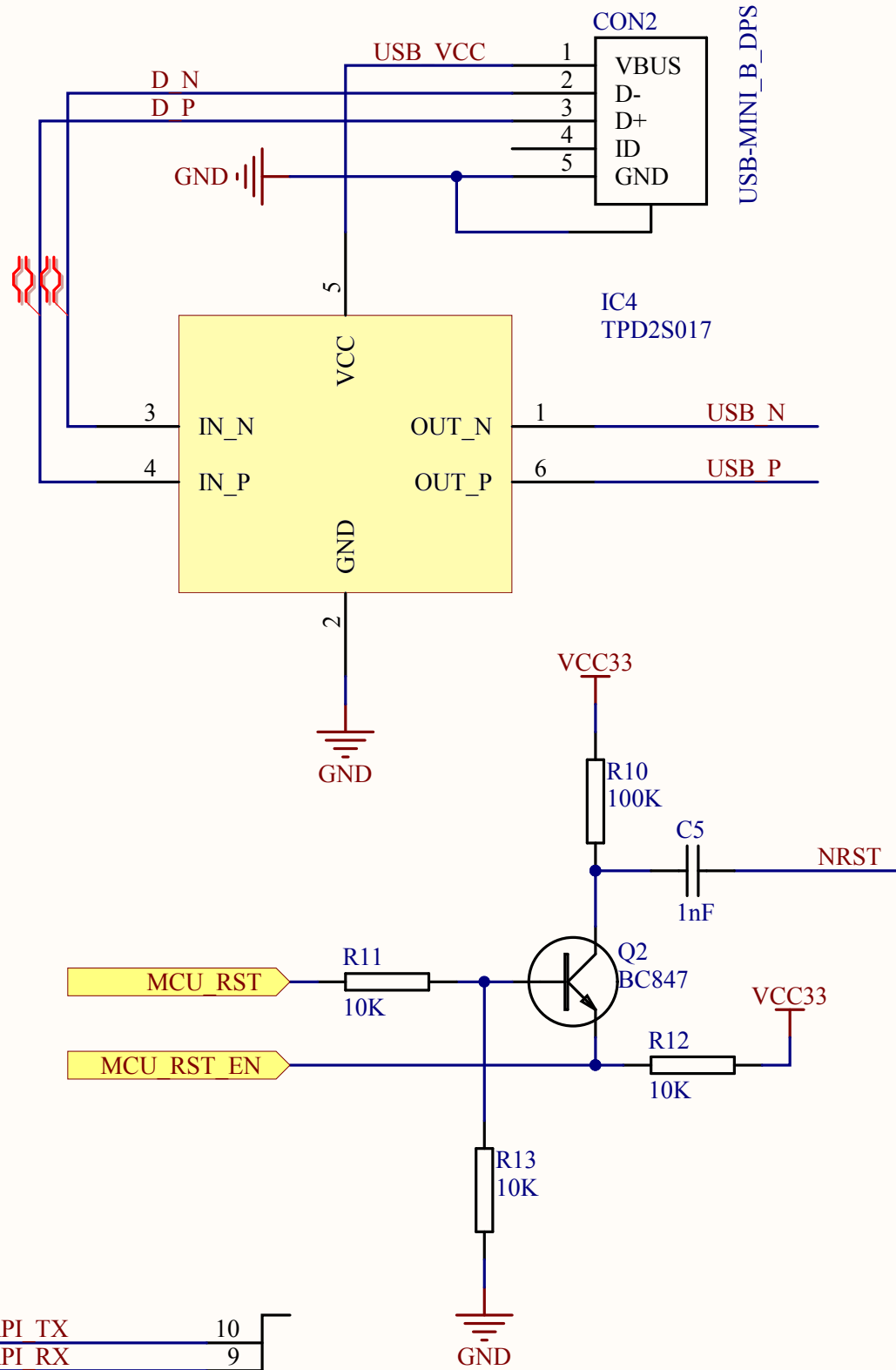
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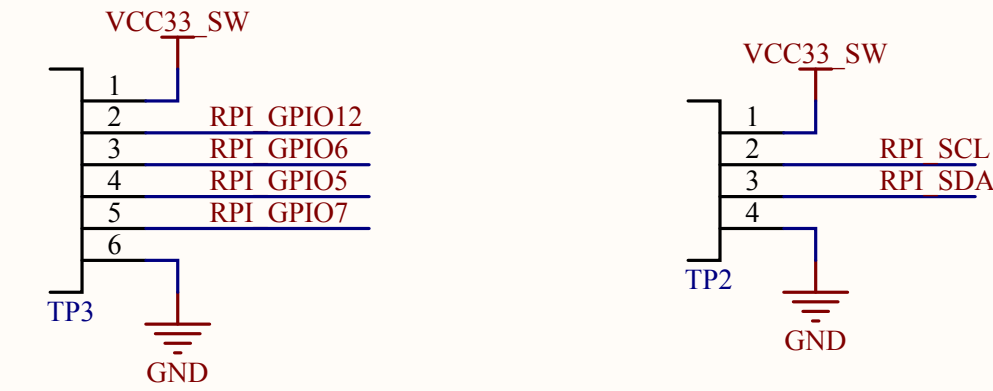
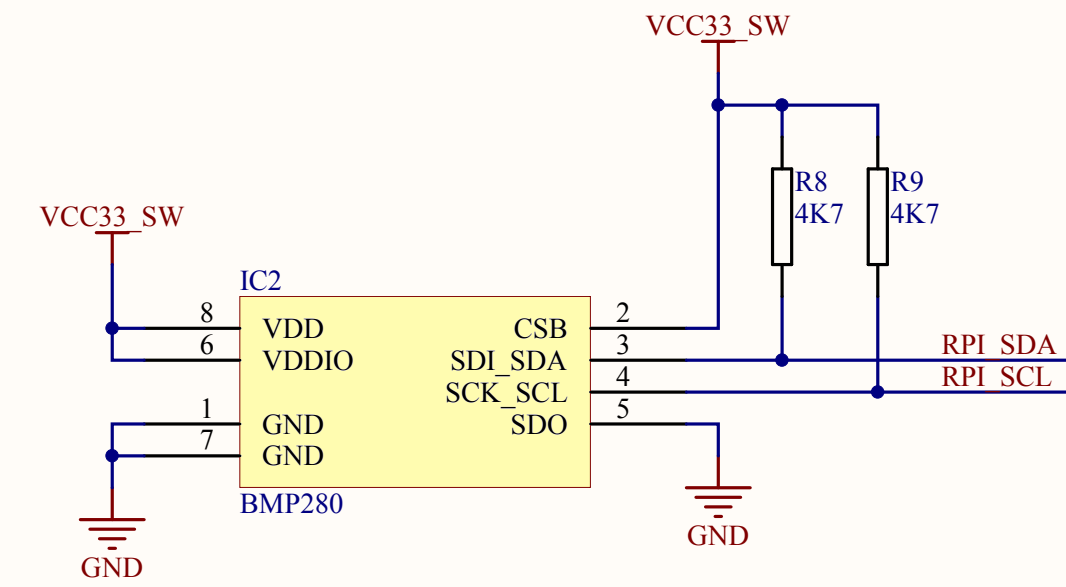
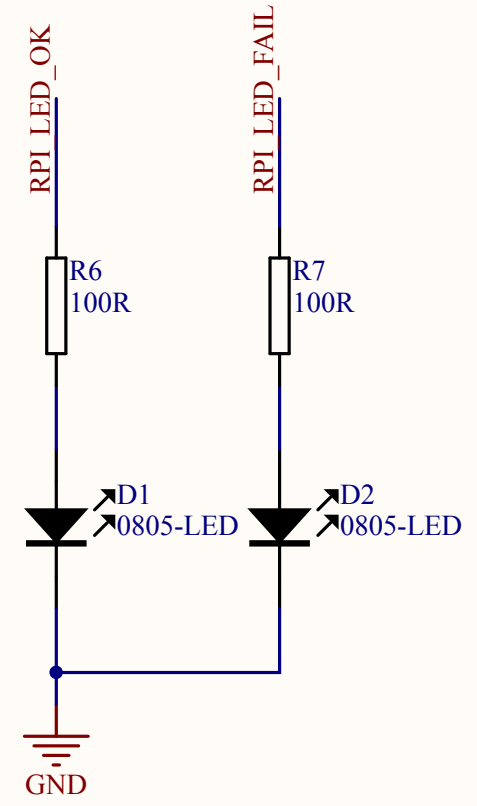
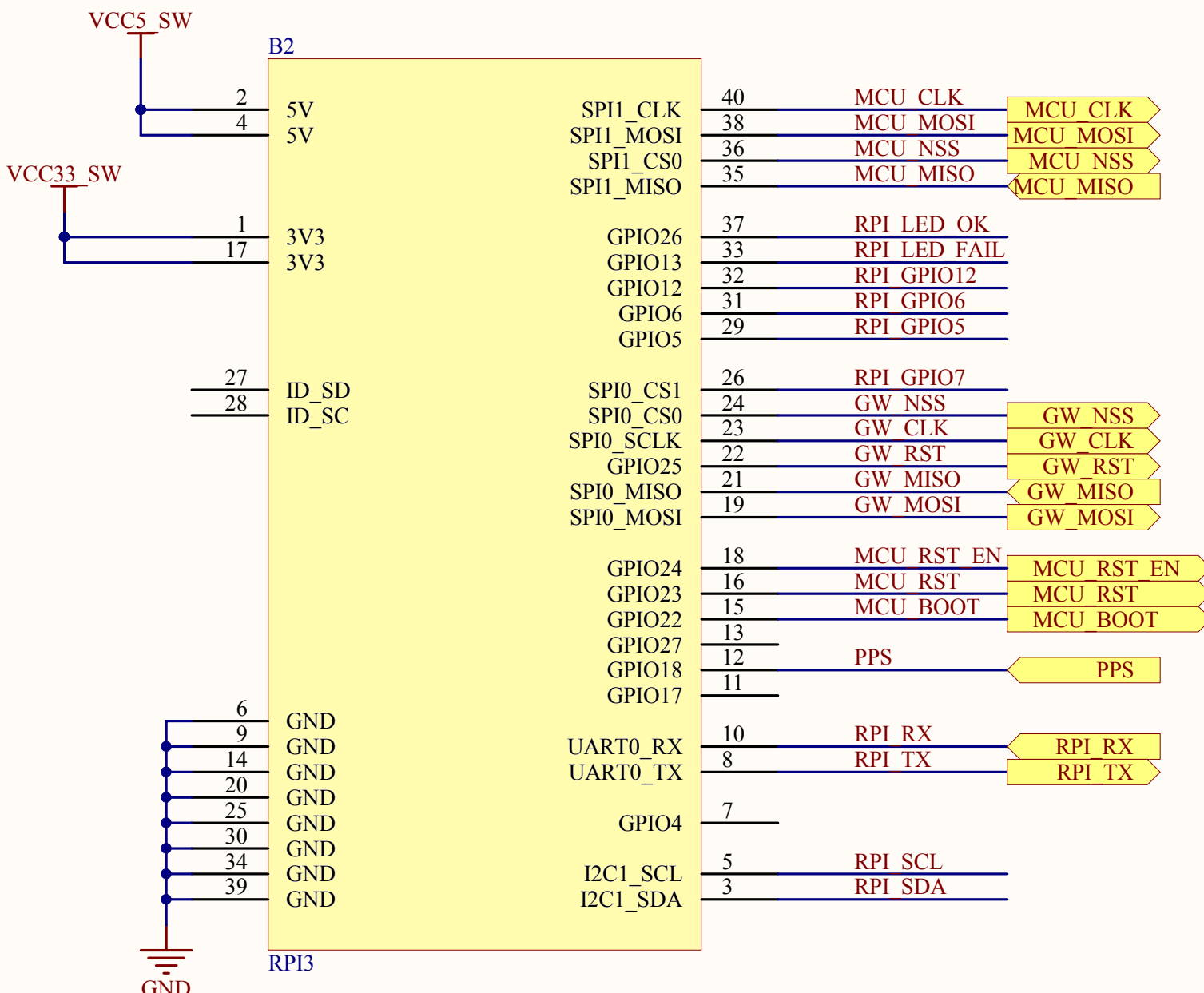
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D

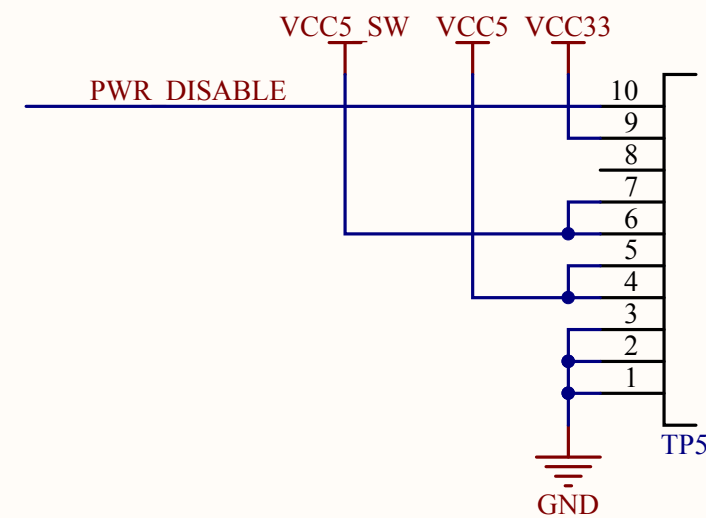
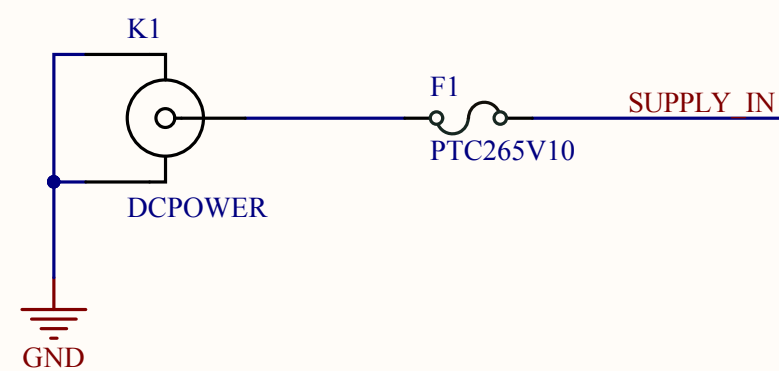
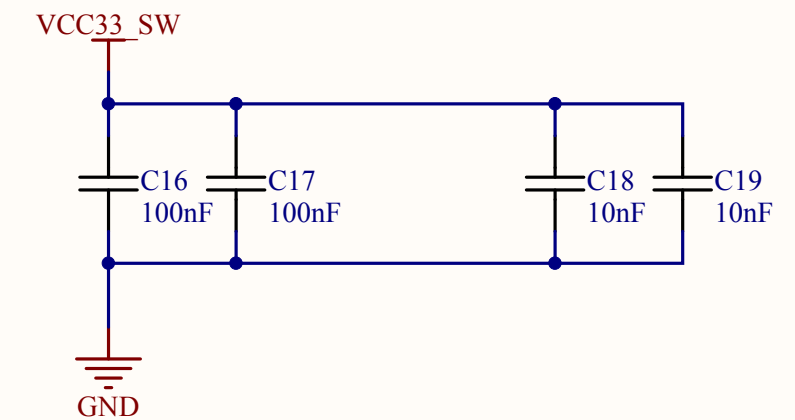
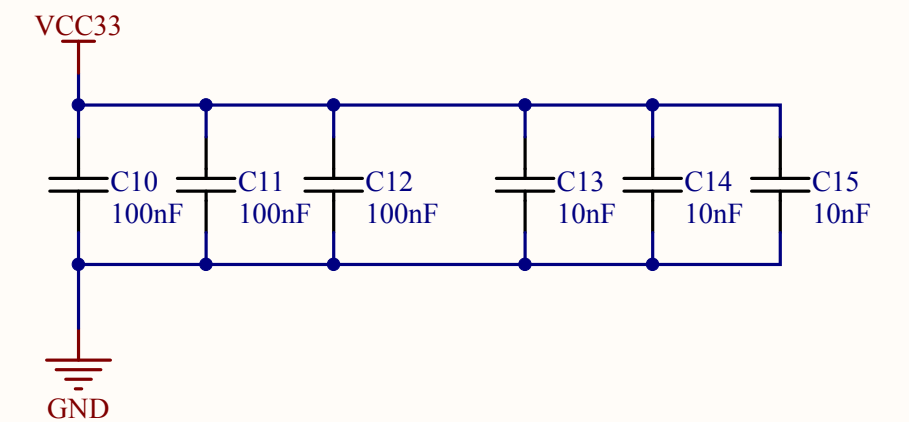
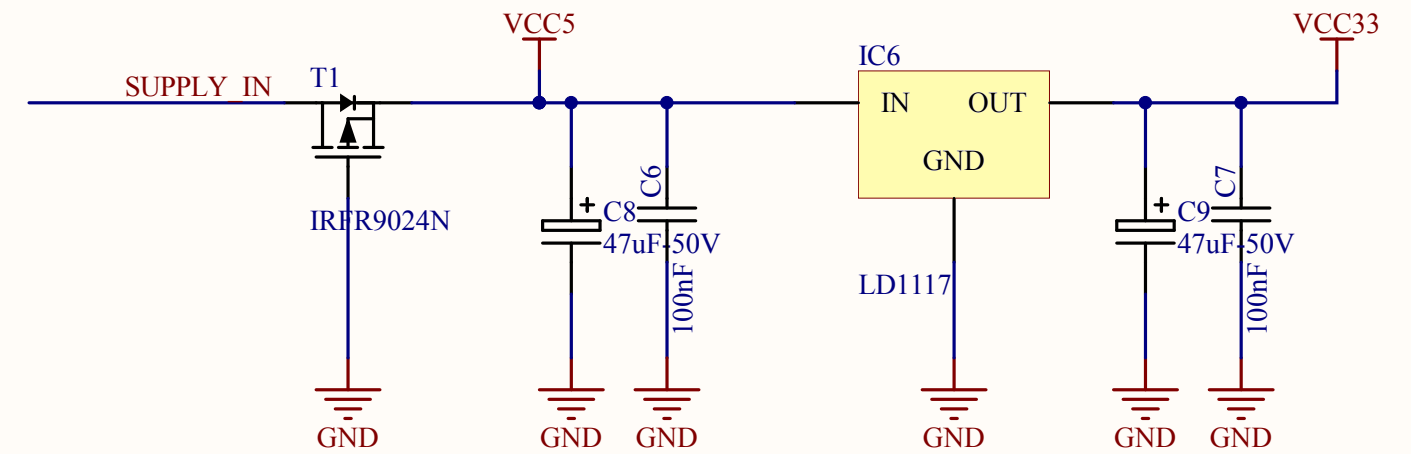
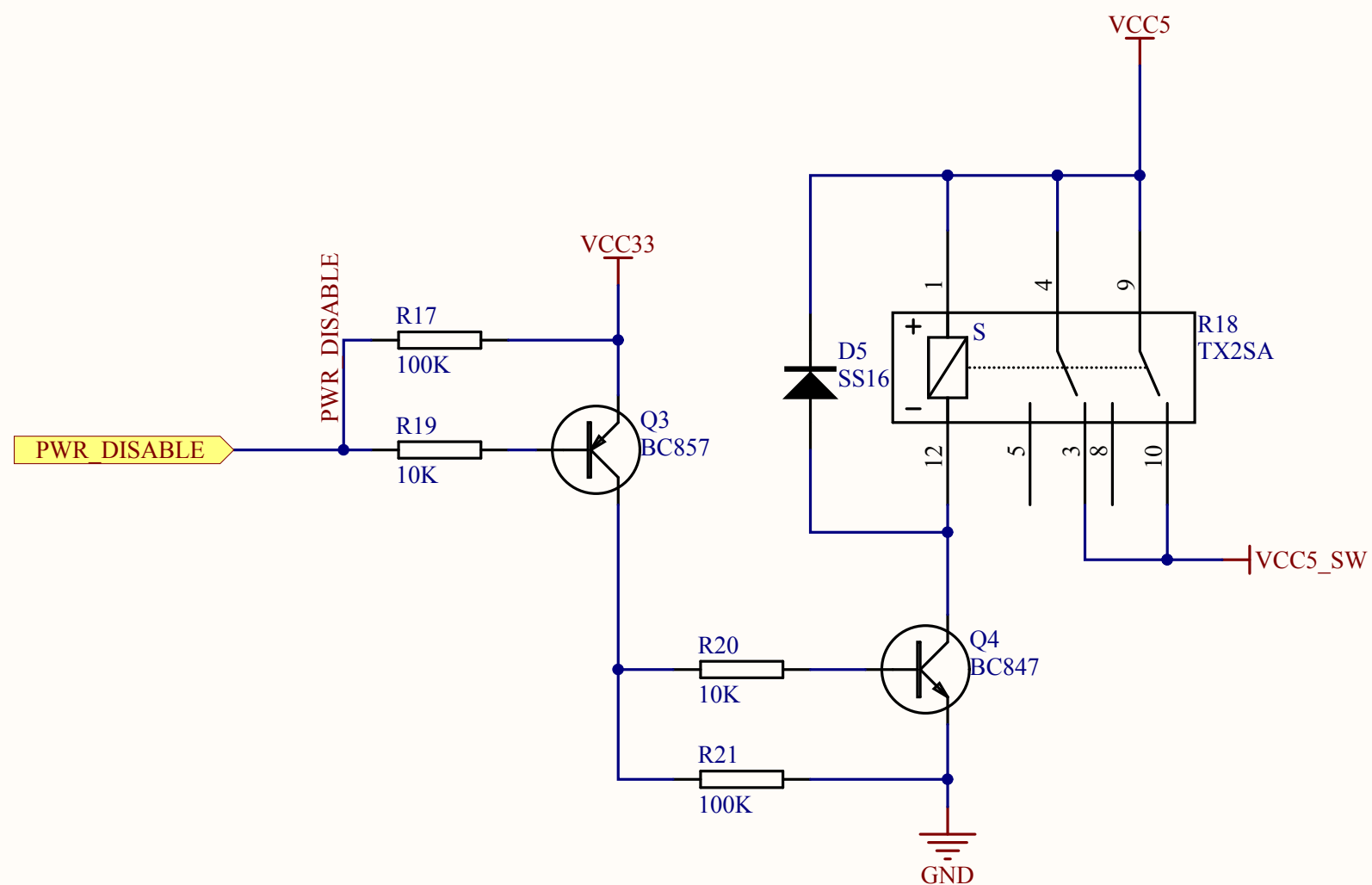


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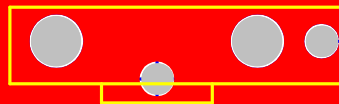


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LoRaWAN Gateway

<http://iot.siliconhill.cz>

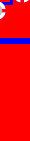
BAT1



CON2



GPS_ANT



Designed by
Vojtech Suk

T09

Ver: 1.0

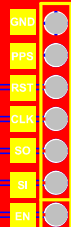
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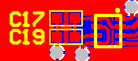
maxi



TP1



IC2



D2

D1



G7

G5

G6

G12

3V3

GND

GND

SDA

SC1

3V3

GND

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