AMERICAN INTERNATIONAL UNIVERSITY-BANGLADESH (AIUB)

FACULTY OF SCIENCE & TECHNOLOGY DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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Digital Logic & Circuits Laboratory
Section: D Group: 03

LAB REPORT NAME:
Studying different digital logic gates and designing of basic logic gates using Universal gates

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Title:

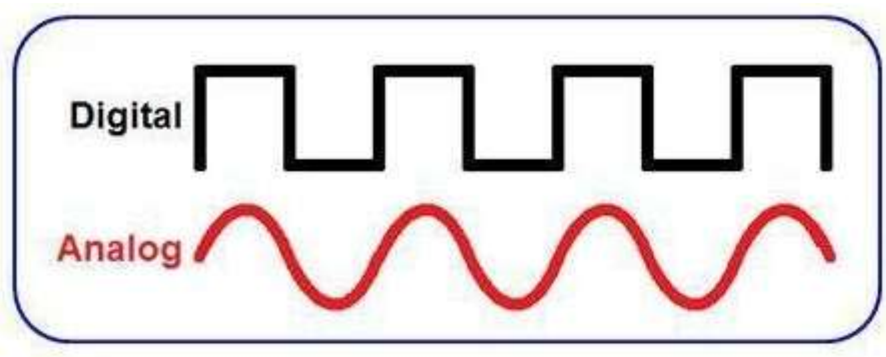
Studying different digital logic gates and designing of basic logic gates using Universal gates

Abstract:

To learn the characteristics of several logic gates and to get familiar with the digital trainer board and digital ICs

Theory and Methodology:

In analog signals, information is translated into electric pulses of varying amplitude but in case of digital, translation of information is in binary format (zero or one) where each bit is representative of two distinct amplitudes.



The main advantage of digital signals over analog signals is that the precise signal level of the digital signal is not vital. This means that digital signals are fairly immune to the imperfections of real electronic systems which tend to spoil analog signals. Codes are often used in the transmission of information. These codes can be used either as a means of keeping the information secret or as a means of breaking the information into pieces that are manageable by the technology used to transmit the code. It can convey information with greater noise immunity, because each information component (byte etc) is determined by the presence or absence of a data bit (0 or one). Analog signals vary continuously and their value is affected by all levels of noise. Digital signals can be processed by digital circuit components, which are cheap and easily produced in many components on a single chip. It uses typically less bandwidth with less electromagnetic interference. Moreover, Information storage can be easier in digital systems than in analog ones. The noise-immunity of digital systems permits data to be stored and retrieved without degradation.

There are two sorts of circuits which are known as integrated circuit and discrete circuit. The two main advantages of ICs over discrete circuits are cost and performance. Cost is low because the chips, with all their components, are printed as a unit by photolithography rather than being constructed one transistor at a time. Furthermore, much less material is used to construct a packaged IC die than to construct a discrete circuit. Performance is high because the components switch quickly and consume little power (compared to their discrete counterparts) as a result of the small size and close proximity of the components.

A logic gate is an elementary building block of a digital circuit. Most logic gates have two inputs and one output. At any given moment, every terminal is in one of the two binary conditions low (0V) and high (5V), represented by different voltage levels. The logic state of a terminal can, and generally does, change often, as the circuit processes data. In most logic gates, the low state is approximately zero volts (0 V), while the high state is approximately five volts positive (+5 V).

There are seven basic logic gates: AND, OR, NOT, NOR, NAND,XOR and XNOR. Different logic operations of different IC's will be introduced which perform the following characteristics:

Operation	Expression
AND	Y=AB
OR	Y=A+B
NOT	$Y=\bar{A}$
NOR	$Y=\overline{A+B}=\bar{A}\,\bar{B}$
NAND	$Y=\overline{AB}=\bar{A}+\bar{B}$
XOR	$Y=A\oplus B=\bar{A}B+A\bar{B}$
XNOR	$Y=AB+\bar{A}\,\bar{B}\ \overline{AB}$

AND operation:
The AND operation produces a high if and only if all the inputs are high. An AND gate can have two or more inputs and performs AND operation or logical multiplication.

Fig1.1: Symbol of AND gate

Truth Table:

Input, A	Input, B	Output, F
0	0	0
0	1	0
1	0	0
1	1	1

Pin configuration for IC-74HC08N :
For a quadrature 2input AND gate HC08 davice code is used. 74HC series devices are designed to work with a 5 V power supply, voltages from 2 V to 5 V are allowed and most circuits work well using 5 V.

OR operation:
The OR operation produces a high output when any of the inputs are high. It has two or more inputs and one output which performs OR operation or logical addition.

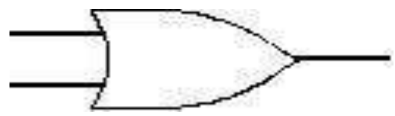


Fig 1.2: Symbol of OR gate

Truth Table:

Input, A	Input, B	Output, F
0	0	0
0	1	1
1	0	1
1	1	1

Pin configuration for IC-74HC32N:

HC32 is the device code. 74HC32 is a Quad 2-input OR gate (High Speed CMOS version) which has lower current consumption/wider Voltage range from 2 to 5V. It requires low input current of 1µA with high noise immunity characteristics of CMOS devices.

NOT operation:
The NOT operation changes one logic level to the opposite logic level. It is implemented by a logic circuit known as an inverter.

Fig1.3: Symbol of NOT gate

Truth Table:

Input, A	Output, F
0	1
1	0

Pin configuration for IC-74HC04N :

The 74HC04 is a hex inverter which consists of six inverters which perform logical invert action. The inputs include clamp diodes that enable the use of current limiting resistors to interface inputs to voltages in excess of V_{CC}. The Input level for 74HC04 is CMOS level .

NAND operation:

The NAND gate operates as an AND gate followed by a NOT gate. It acts in the manner of the logical operation "AND" followed by negation. The output will be low if both inputs are high. Otherwise, the output is high .

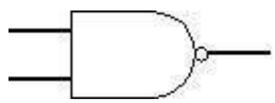


Fig 1.4: Symbol of NAND gate

Truth Table:

Input, A	Input, B	Output, F
0	0	1
0	1	1
1	0	1
1	1	0

Pin configuration for IC-74HC00N :
HC00 is the device code. The device inputs are compatible with Standard CMOS outputs; with pullup resistors. The operating voltage range is 2.0 to 5.0 V and low input current is 1.0 μA.

NOR operation:
The NOR gate is a combination OR gate followed by an inverter. Its output is high if both inputs are low. Otherwise, the output is low.

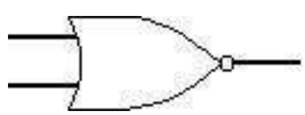


Fig 1.5: Symbol of NOR gate

Truth Table:

Input, A	Input, B	Output, F
0	0	1
0	1	0
1	0	0
1	1	0

Pin configuration for IC-74HC02N :
The 74HC02 is a high speed Si-gate CMOS device that provides a quadrature 2 –input NOR function. CMOS level is the input level for this sort of IC's. The operating Voltage Range is 2.0 to 5.0 V and low input current is 1.0 µA.

XOR operation:

The XOR (exclusive OR) gate acts in the same way as the logical "either/or" .The output is high if either, but not both, of the inputs are high. The output is low if both inputs are low or if both inputs are high. Another way of looking at this circuit is to observe that the output is 1 if the inputs are different, but 0 if the inputs are the same.

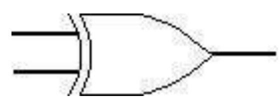


Fig 1.6: Symbol of XOR gate

Truth Table:

Input, A	Input, B	Output, F
0	0	0
0	1	1
1	0	1
1	1	0

Pin configuration for IC-74HC86N :
HC86 is the device code for a quad 2-input xor gate which utilizes advanced silicon gate CMOS technology . It maintains low power consumption and high noise immunity characteristic of standard CMOS integrated circuits. The 74HC logic family has a voltage range of 2V to 5V and the operating temperature is -40°C to 125°C with input current of 1µA.

XNOR operation:

The XNOR *(exclusive-NOR) gate* is a combination XOR gate followed by an inverter.
Its output is high if the inputs are the same, and low if the inputs are different.

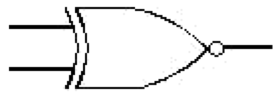


Fig 1.7: Symbol of XNOR gate

Truth Table:

Input, A	Input, B	Output, F
0	0	1
0	1	0
1	0	0
1	1	1

Using combinations of logic gates, complex operations can be performed. Arrays of logic gates are found in digital integrated circuits (ICs). As IC technology advances, the required physical volume for each individual logic gate decreases and digital devices of the same or smaller size become capable of performing ever more complicated operations at ever- increasing speeds.

Part II: **Study of Universal Gates**

A Logic Gate which can infer any of the gate among Logic Gates or a gate which can be used to create any Logic gate is called Universal Gate. **NAND** and **NOR** Gates are called Universal Gates because all the other gates such as NOT, AND, OR, XOR, XNOR etc can be created by using these gates.

The Objective of this lab is to implement different logic functions using universal gates.

Theory and Methodology:

NAND gate:

The graphic symbol for the NAND gate consists of an AND symbol with a bubble on the output, denoting that a complement operation is performed on the output of the AND gate

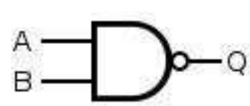


Fig 2.1: Symbol of NAND gate

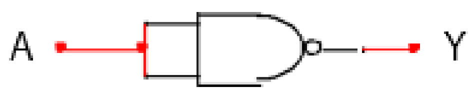
Output, Q= = +

Truth Table		
Input A	Input B	Output Q
0	0	1
0	1	1
1	0	1
1	1	0

It is possible to construct other gates using NAND gates which are shown in Experimental procedure part

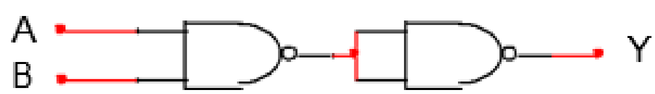
Implementing various logic functions using NAND Gates:

1) Implementing NOT gate using NAND gate:



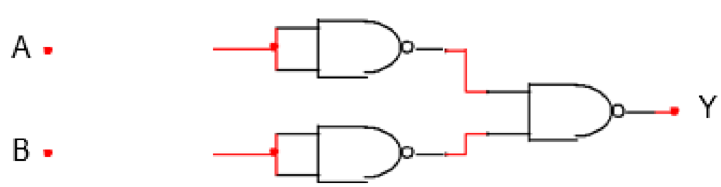
NOT gate using NAND gate

2) Implementing AND gate using NAND gate:



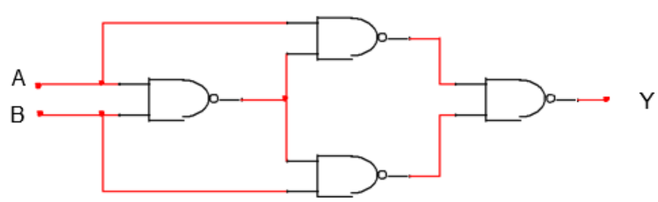
AND gate using NAND gates

3) Implementing OR gate using NAND gate:



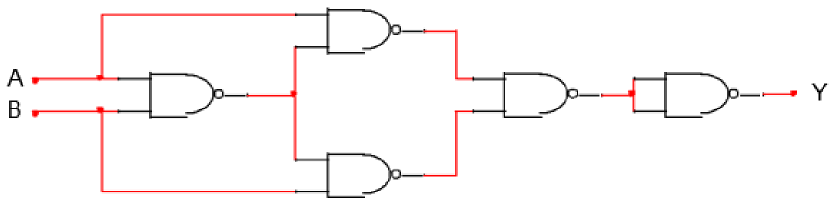
OR gate using NAND gates

4) Implementing XOR gate using NAND gate:



XOR gate using NAND gates

5) Implementing XNOR gate using NAND gate:



XNOR gate using NAND gates

NOR gate:

The **NOR** gate represents the complement of the OR operation. It's name is an abbreviation of NOT **OR**. The graphic symbol for the NOR gate consists of an OR symbol with a bubble on the output, denoting that a complement operation is performed on the output of the OR gate. The truth table and the graphic symbol of NOR gate is shown in the figure.

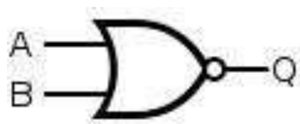


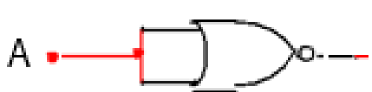
Fig 2.2: Symbol of NOR

gate Output,Q= =

Truth Table		
Input A	**Input B**	**Output Q**
0	0	1
0	1	0
1	0	0
1	1	0

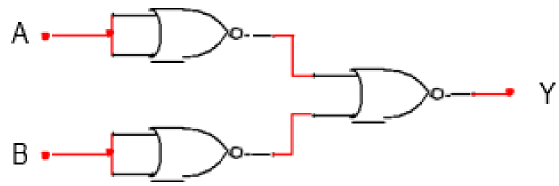
Implementing various logic functions using NOR Gates:

1) Implementing NOT gate using NOR gate:



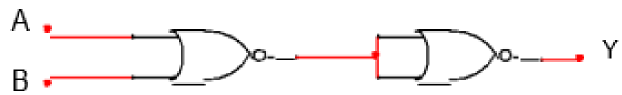
NOT gate using NOR gate

2) Implementing AND gate using NOR gate:



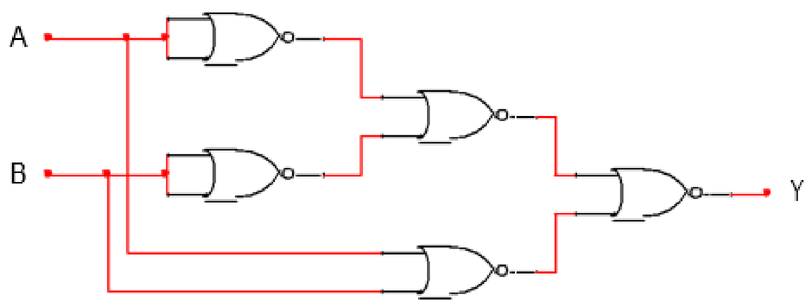
AND gate using NOR gates

3) Implementing OR gate using NOR gate:



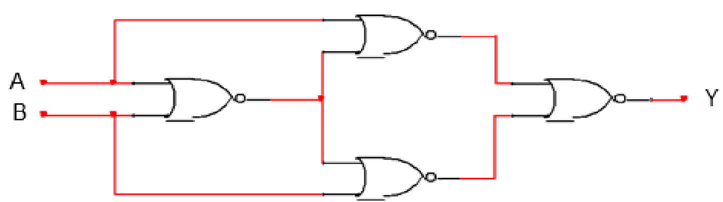
OR gate using NOR gates

4) plementing XOR gate using NOR gate:



XOR gate using NOR gates

5) Implementing XNOR gate using NOR gate:



XNOR gate using NOR gates

Apparatus:

1. Digital trainer board.
2. Integrated Circuits (ICs).
3. Power supply.
4. Connecting wires.

Integrated Circuits (ICs):

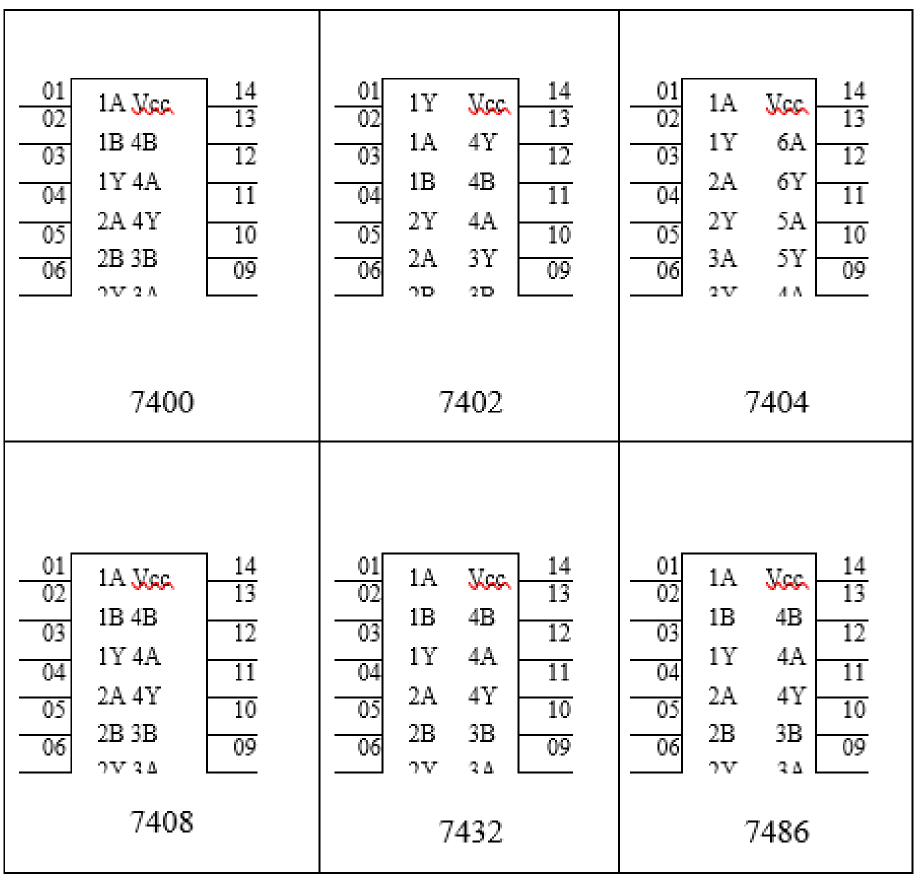
7400 : 1 pcs

7402 : 1 pcs

Precautions:

After the circuit has been set up, all connections should be checked by the instructor. Only enough voltage should be applied to turn on the chip; otherwise, it may be damaged.

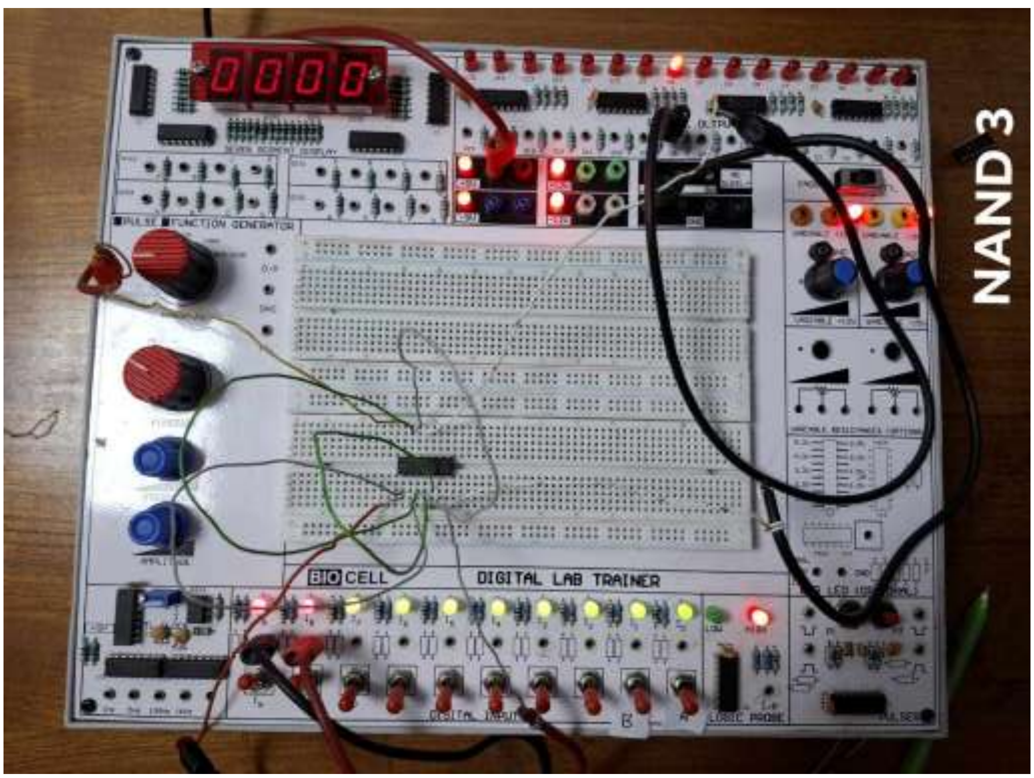
IC configurations:



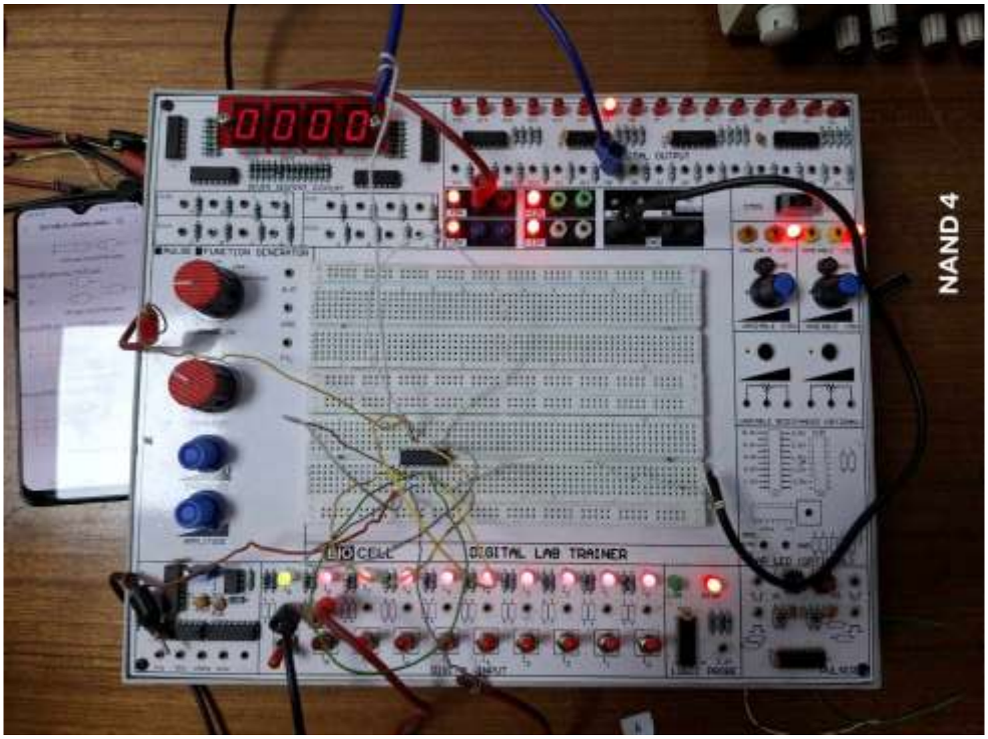
Experimental Procedure:

1. An X-OR and X-NOR gate should be constructed on the trainer board using only NAND gates. The required IC should be used to complete the circuit.
2. The equivalent NOT, OR, and AND gates should be derived using only NOR gates. Then, an X-OR and X-NOR gate should be constructed on the trainer board using only NOR gates. The required IC should be used to complete the circuit.
3. The following expressions should be converted using universal gates and implemented on the trainer board. The results should be compared with the truth table of the equations:
   i) A (+) B
   ii) (A(+)B) + C
   iii) (AB + CD)’

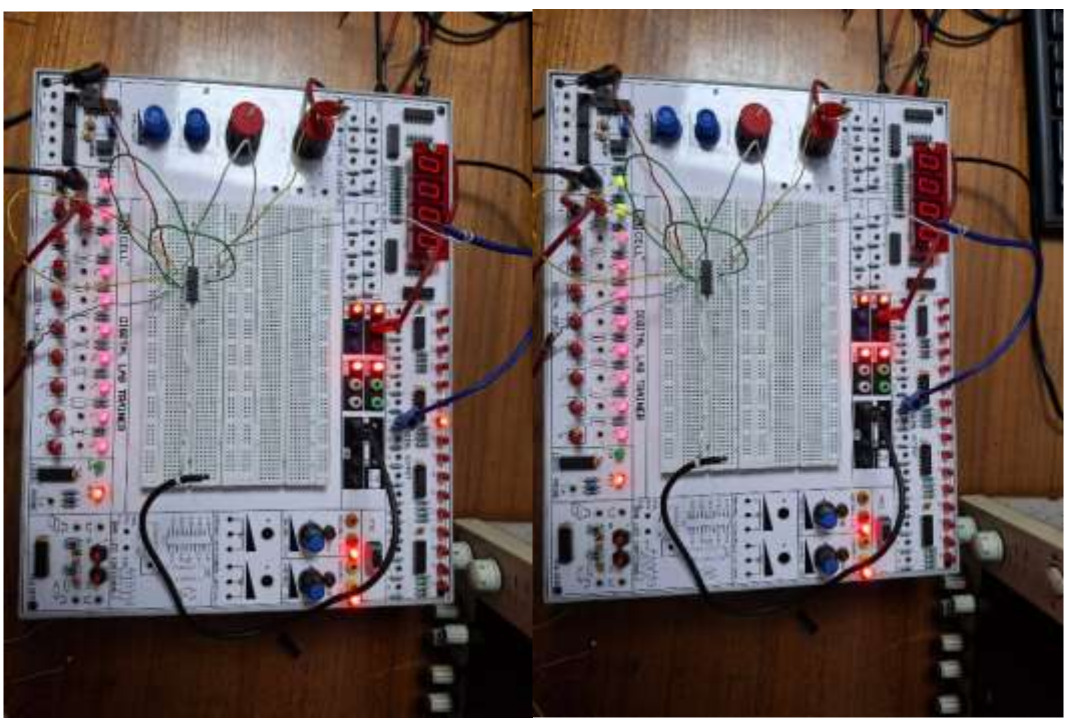
Measurement :



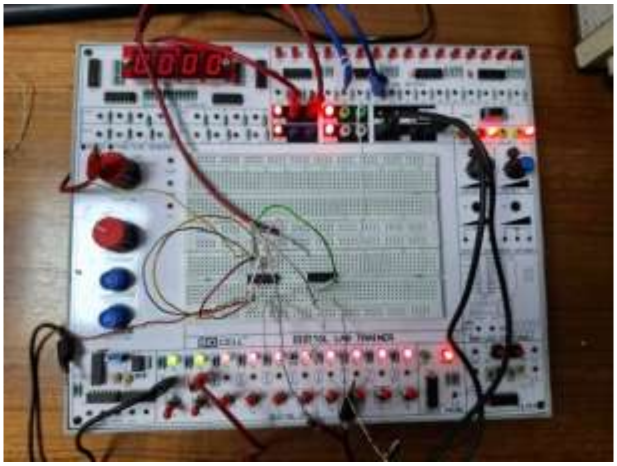
Implementing OR gate using NAND gate:

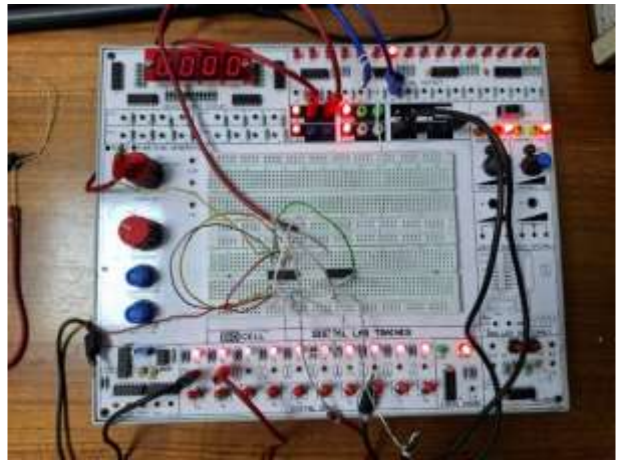


Implementing XOR gate using NAND gate:

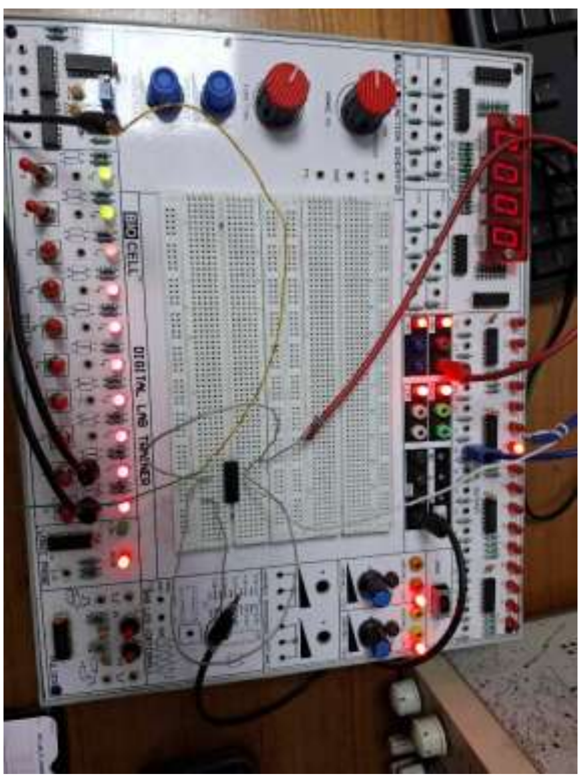


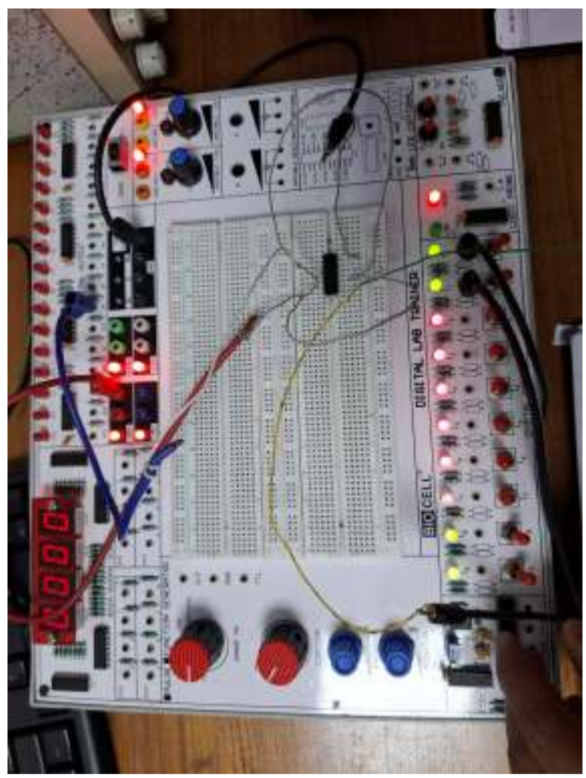
Implementing AND gate using NOR gate:





Implementing XOR gate using NOR gate:

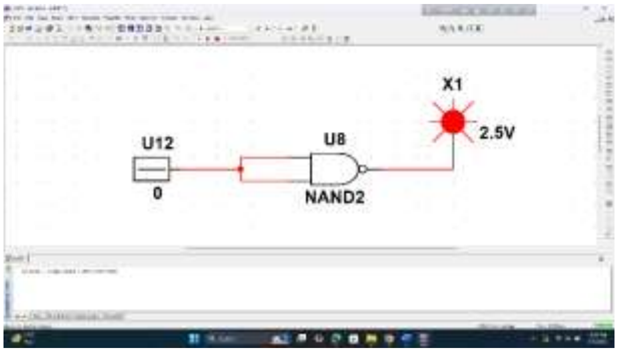


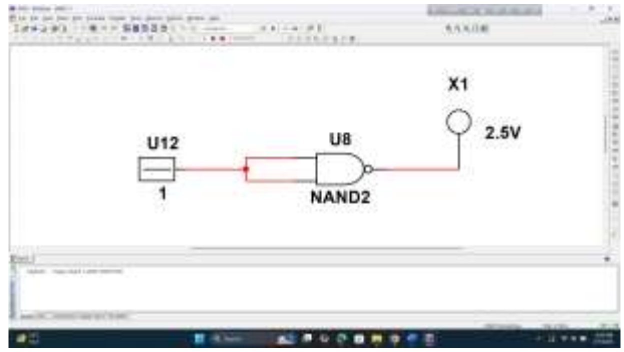


Implementing AND gate using NAND gate:

Simulation :

NAND gate:

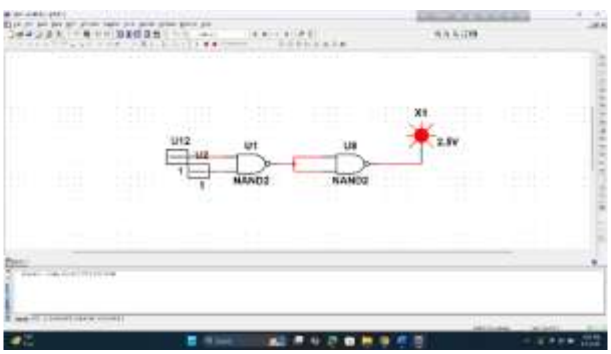


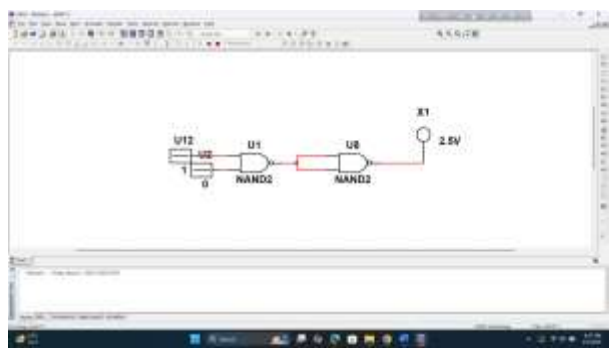


Implementing NOT gate using NAND gate:

Truth Table

Input, A	Input, B	Output, Y
0	1	1
1	0	0

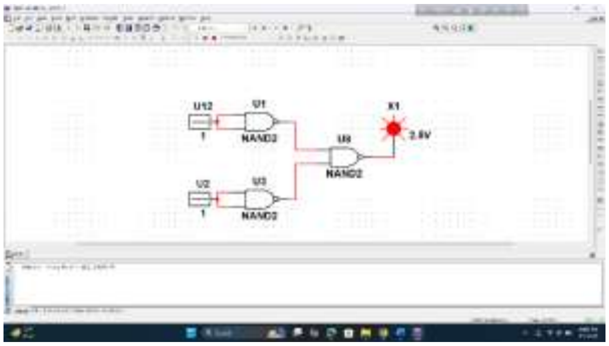
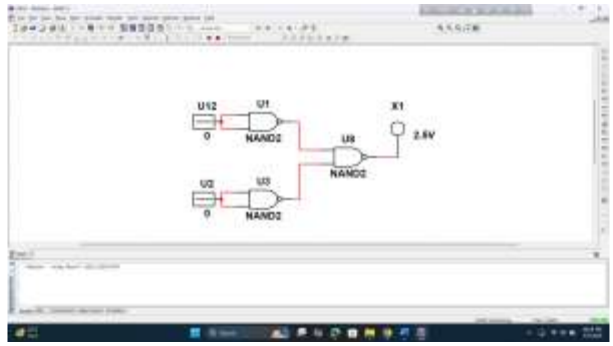




Implementing AND gate using NAND gate

Truth Table

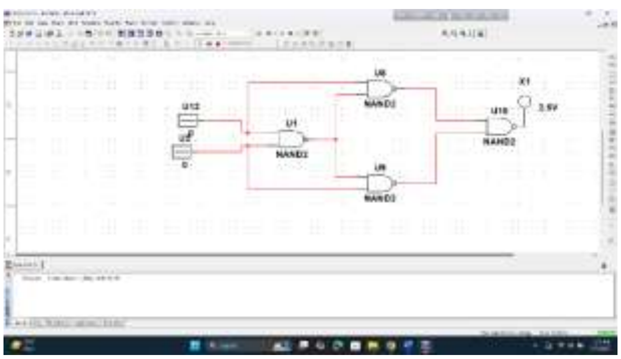
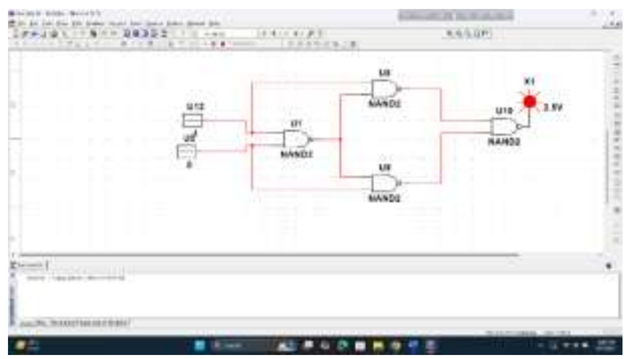
Input, A	Input, B	(A.B)’	Output
0	0	1	0
0	1	1	0
1	0	1	0
1	1	0	1

Implementing OR gate using NAND gate:

Truth Table

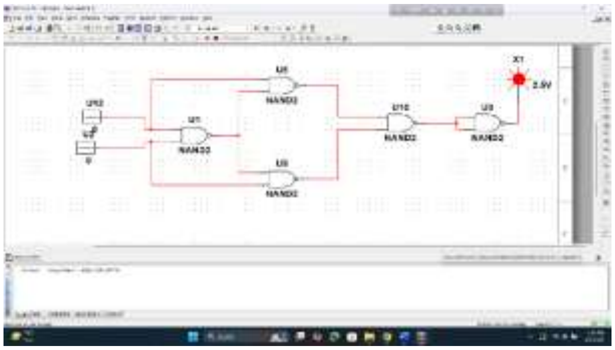
Input, A	Input, B	Output Y
0	0	0
0	1	1
1	0	1
1	1	1

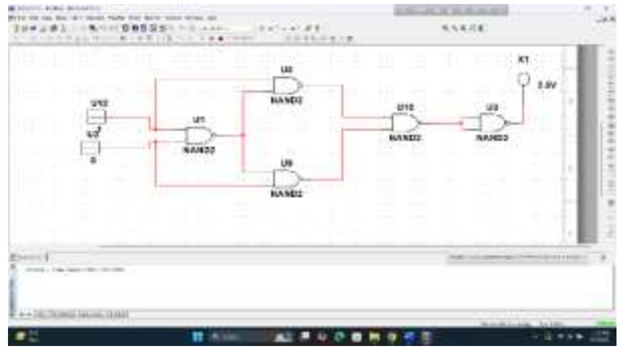
Implementing XOR gate using NAND gate:

Truth Table: XOR

Input, A	Input, B	Output, F
0	0	0
0	1	1
1	0	1
1	1	0



1)

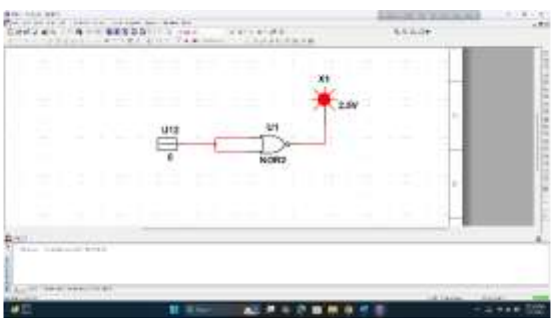


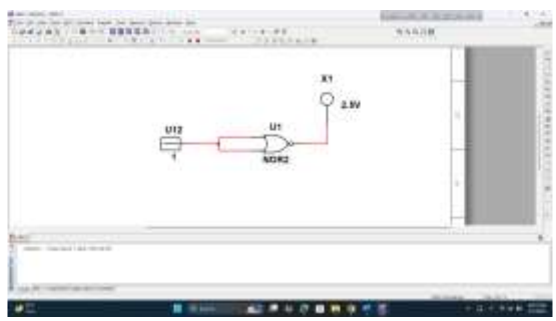
Implementing XNOR gate using NAND gate:

Truth Table: X-XOR

Input, A	Input, B	Output, F
0	0	1
0	1	0
1	0	0
1	1	1

NOR gate:

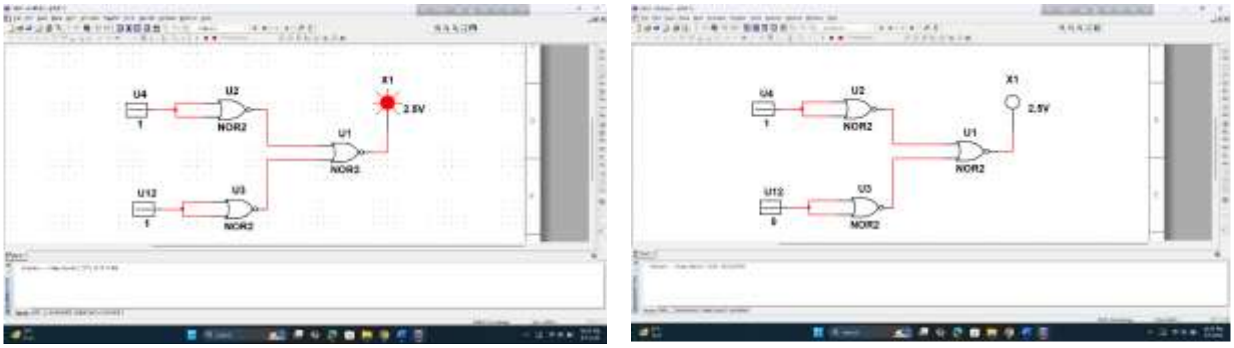




Implementing NOT gate using NOR

Truth Table: NOT

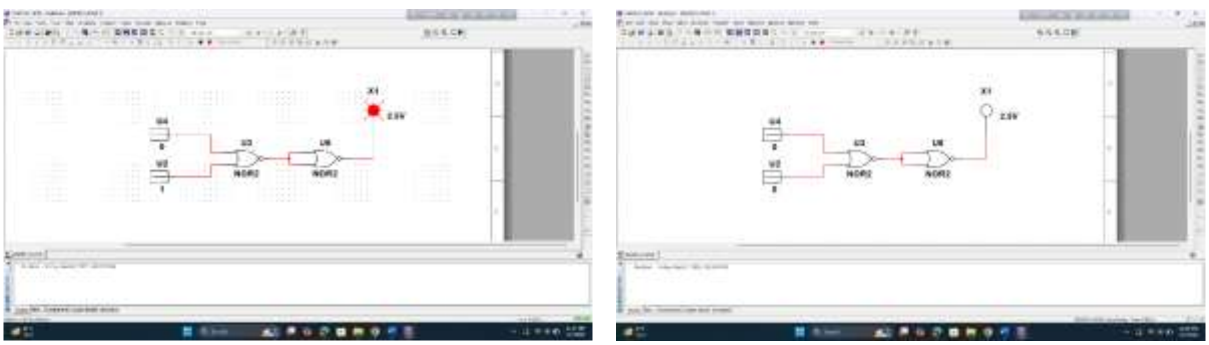
Input, A	Output, B
0	1
1	0



Implementing AND gate using NOR gate

Truth Table: AND

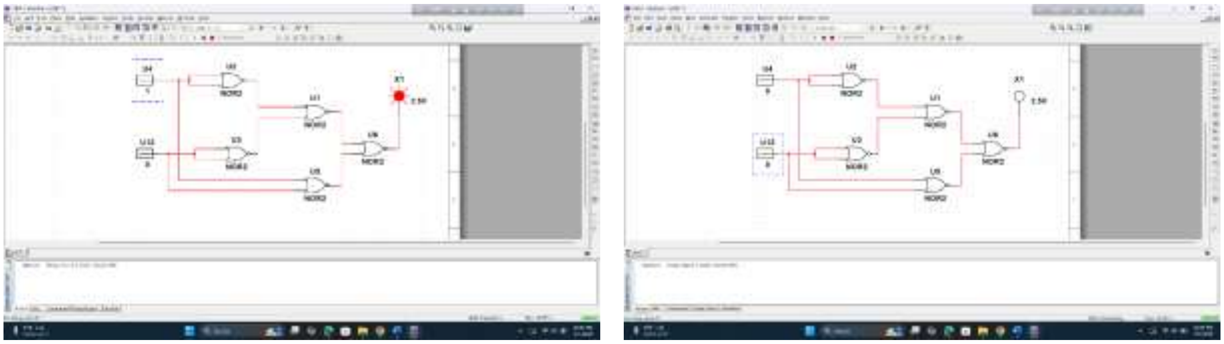
Input, A	Input, B	Output, F
0	0	0
0	1	0
1	0	0
1	1	1



Implementing OR gate using NOR gate

Truth Table: OR

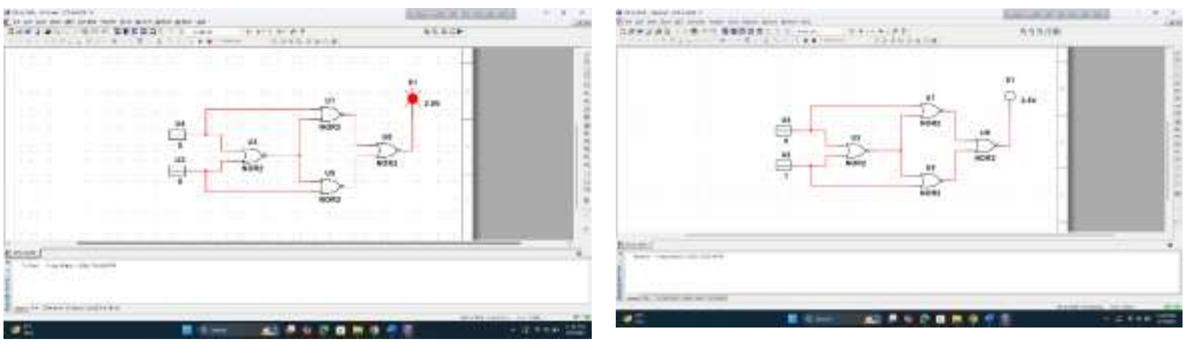
Input, A	Input, B	Output, F
0	0	0
0	1	1
1	0	1
1	1	1



Implementing XOR gate using NOR

Truth Table: XOR

Input, A	Input, B	Output, F
0	0	0
0	1	1
1	0	1
1	1	1



Implementing XNOR gate using NOR

Truth Table: XNOR

Input, A	Input, B	Output, F
0	0	1
0	1	0
1	0	0
1	1	1

Questions with answers for report writing:

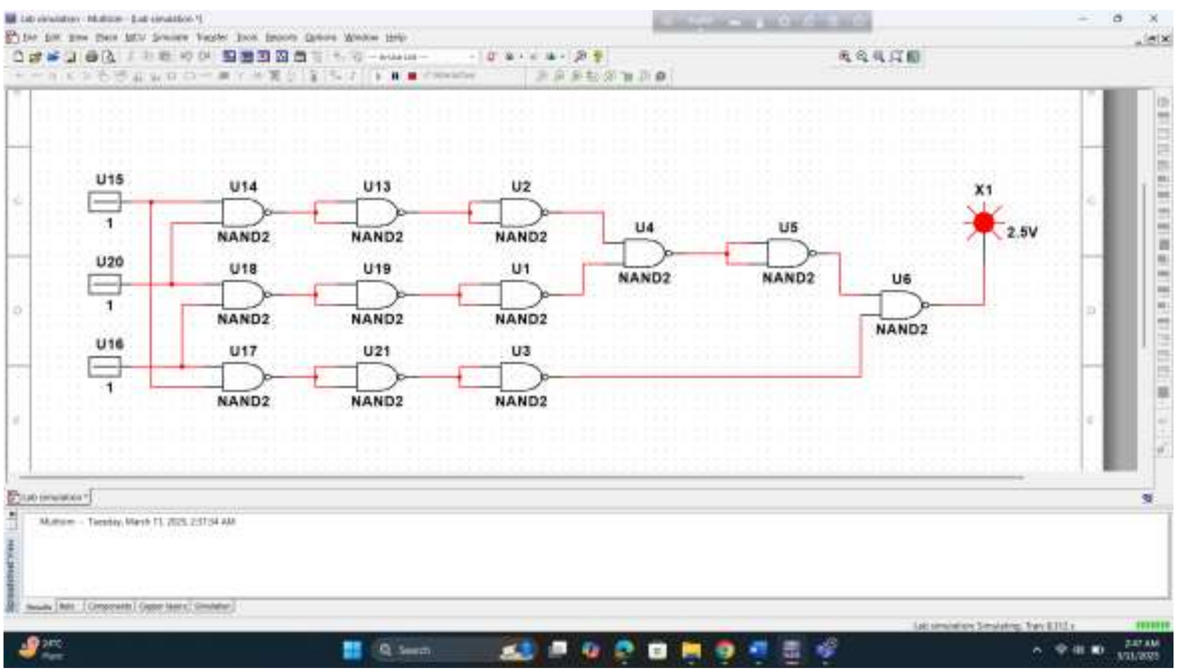
1) What do you mean by universal gate?

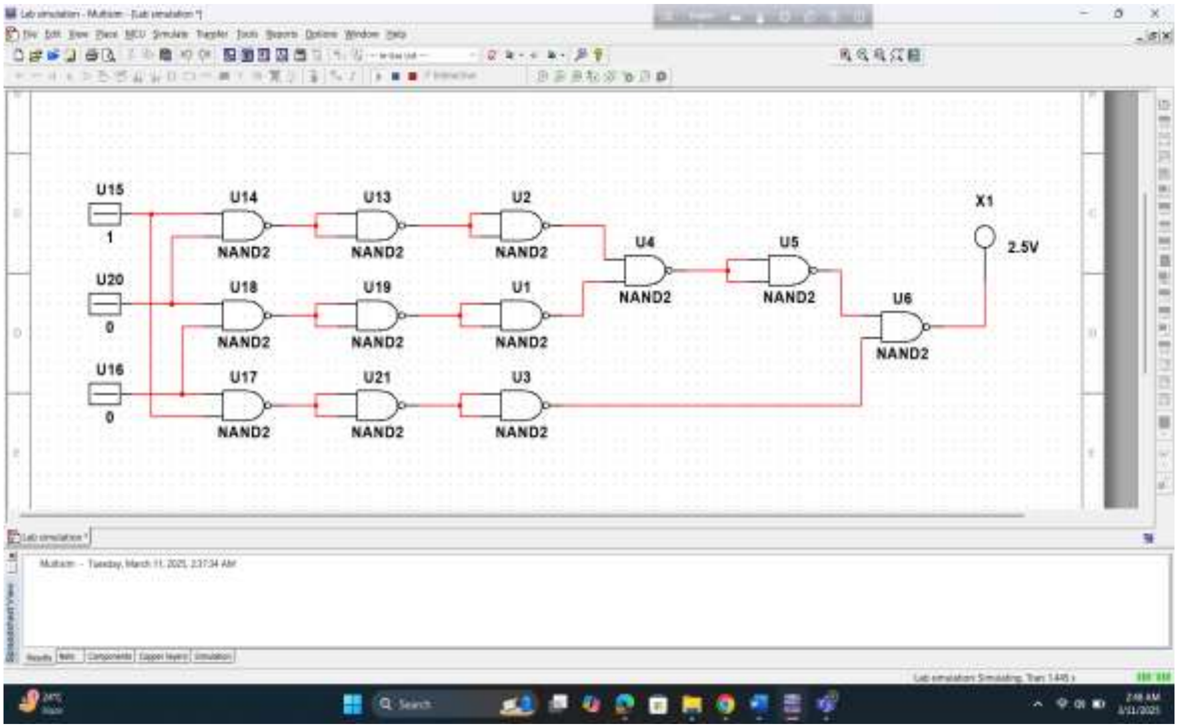
Ans: A universal gate is a logic gate that can be used to create any other basic logic gate (AND, OR, NOT). NAND and NOR are universal gates because they can perform all logical operations, making them essential in digital circuit design.

2) What are the ICs required in this experiment?

Ans: The ICs required in this experiment are:
NAND Gate IC (7400) – Used to design AND, OR, and NOT gates.
NOR Gate IC (7402) – Another universal gate used to create basic gates.
AND Gate IC (7408) – For direct implementation of AND logic.
OR Gate IC (7432) – Used for OR logic verification.
NOT Gate IC (7404) – Inverter IC for logical negation.

3) Construct a circuit of output F, where F=AB + BC + CA, by using NAND gates only in the PSIM Software and show the output states for each of the available conditions.





Truth Table for F=AB + BC + CA, by using NAND

A	B	C	AB	BC	CA	Y=AB+BC+CA
0	0	0	0	0	0	0
0	0	1	0	0	0	0
0	1	0	0	0	0	0
0	1	1	0	1	0	1
1	0	0	0	0	0	0
1	0	1	0	0	1	1
1	1	0	1	0	0	1
1	1	1	1	1	1	1

Ans: A universal gate is a logic gate that can create any other gate. NAND and NOR are universal gates because they can implement AND, OR, and NOT functions.
The required ICs are 7400 (NAND), 7402 (NOR), for logic gate implementation.Convert the equation using NAND-only logic, design the circuit with NAND gates in PSIM, simulate it, and verify outputs for all input conditions.

Discussion:

In this study, we explored different digital logic gates and learned how to design basic logic gates using universal gates like NAND and NOR. Universal gates are powerful because they can create any other gate, simplifying circuit design. Understanding these concepts helps in building more efficient digital circuits.

Conclusion:

By studying logic gates and using universal gates for designing circuits, we gained valuable knowledge of digital electronics. This understanding is essential for developing complex digital systems and improving problem-solving skills in circuit design.

Reference(s):

1) www.tutorialspoint.com
2) www.electronics-tutorials.ws
3) http://www.tutorialspoint.com/computer_logical_organization/combinational_circuits.ht m