

SUMMARY OF QUALIFICATION

- Engineer Post Graduate with 18+ years of professional experiences in Embedded Systems and Platform Software research and development. Involved in Architecting, Designing, Developing, Supporting complex software products and innovative software solutions.
- Have deep understanding and experiences in diverse technology domains including Telcom, Networking, Mobile Devices, Enterprise Datacenters, Payment Solutions, Printing, Cloud Computing and Automation Test Suites.
- 2+ years of Product Leadership and Program Management Experience.

COMPUTER SKILLS

Programming Languages:

- Primary Programming Languages: C, C++, Python.
- Other programming languages: Java (basics), VC++, Perl, Shell Scripting.
- Web Technologies/Standards: REST Architecture Pattern, AWS, HTTP, XMPP, XML, JSON, YAML.
- Specialized In: Linux System Programming, Device Drivers, Debugging, Unit Test Suite Automation.
- Peripheral and Buses: SPI, I2C, PCIe, UART, USB, Ethernet and GPIO. JTag and Lauterbach ICE.

Software & Tools:

- OS Platforms: Linux, Docker, UNIX, Microsoft Windows, VxWorks RTOS, Career Grade Linux, MacOS.
- SCM Tools: GIT/GitHub, ClearCase, SVN, Perforce, CVS.
- Other SW Tools: Rally (Agile), Rational Rose, MS Project, MS Office Suites.

Certifications:

- Green Belt Certification in Lean Sigma (Hewlett-Packard, 2010)
- Certified Scrum Master from Scrum Alliance (Hewlett-Packard, 2012)

EMPLOYMENT

Cisco Systems

Technical Lead

Jun 2015 – Till Date

Developing Platform Dependent Infrastructure SW and Drivers that manages Fabric Switching ASICs and Data Plane for Cisco Core and Access Routers. Responsibilities includes new board bring-up, feature additions, triages & defect fixing, Third-party SDK Integration and collaboration with Platform Independent, HW & Diagnostics teams. Also responsible for development of White-Box Automation Unit Test Suites and Training & mentoring new hires.

Accomplishments:

- Porting and Development of First ever low-cost Cisco Access Router which runs on ARM 64-bit platform soc.
- Development of Automated White-Box UT Suite based on CMocka Framework.

Hewlett-Packard

Senior Software Engineer

Sep 2010 – Jun 2015

Designing and development of Firmware Connectivity Modules for the Next Generation Inkjet Printers to interface with external clients (Software Drivers, Mixed Cloud Infrastructure, Web Services, Telemetry). Re-architecting and coding of platforms and software components for enhanced scalability, improved security, and better performance. Prototype development for proof of concepts before productization. Conducting workshops, trainings, and seminars.

Accomplishments:

- Re-architecture of cloud connectivity module replacing XMPP with TLV for reduced latency and high throughput.

Hewlett-Packard

Program Manager

Jun 2008 – Aug 2010

Developing and Re-Engineering Firmware Deployment Solutions for entire fleet of HP Business Critical Servers supporting multiple OS. Program Management of Firmware Delivery for entire range of HP Business Critical Integrity Servers. Participating in Global Program and Tactical Forums to drive next product pipelines, budgeting, roadmap planning and de-risking program for faster time to market. Co-ordinating with multiple functional teams across geographies to meet the product delivery schedule.

Accomplishments:

- Green Belt in Lean Sigma initiative that resulted in YoY cost savings of US\$400,000.
- Development of EFI based Firmware Installer and automated release note generation.

Motorola

Senior Software Engineer

Aug 2006 – Jun 2008

Developing AJAR Multimedia Framework for various Motorola Mobile Devices using TI ASIC SDKs, joint development with peer teams in Cambridge and Taipei following agile methodology. Traveling between Cambridge, Taipei and ODM partners for technical liaison and product certification.

Accomplishments:

- Received Bravo Awards on several occasions for fixing multiple critical defects and enabling early release.

Motorola**Senior Software Engineer****May 2005 – Jul 2006**

Developing and maintaining of High Availability Platforms used in Motorola CDMA & iDEN Mobile Network and RAN Infrastructure aiming to provide 99.999% availability of underlying services to applications. Developed new features within various software modules (Statistics Manager, High-Precision Timer and Platform Abstraction Layer) across all hardware platforms. Analysis of logs and issue reported by customers related to HAL and Platform OS.

Accomplishments:

- Received Bravo awards for providing L3 Technical Support to Motorola Customers by handling critical issues and performing quick analysis and providing fix, workaround, and permanent solutions.

Hewlett-Packard**Senior Software Engineer****Sep 2003 – Apr 2005**

Development of communication library module for VeriFone Point-of-Sales Payment Devices and optimize firmware driver to support new GPRS modem and integrated end to end application solution.

Wrote from scratch, Windows Device Driver and Tool for configuration and monitoring Sensors HP Workstations.

Fixed a critical Linux Kernel Bug in USB Device Driver which was blocking shipment of HP iPAQ PDAs.

Accomplishments:

- Travelled to Istanbul for providing technical assistance during field testing on “Garanti Bank” premises.

iCMG India Pvt Limited**Trainee Software Engineer****Aug 2002 – Sep 2003**

Training and developing compiler tools for converting IDL to WSDL file and vice-versa as per CSDL specification.

Migrating standalone 3DFont Software in C++ language to distributed applications using CORBA 3.0.

EDUCATION

Master of Business Administration (MBA) in Information Systems (SMU, 2010-2012), 79.92%

Bachelor of Engineering (BE) in Computer Science & Engineering (VTU, 1998-2002), 77.78%

PROFESSIONAL REFERENCES

Available upon request.