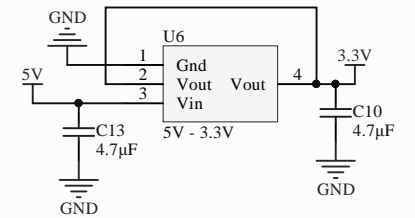
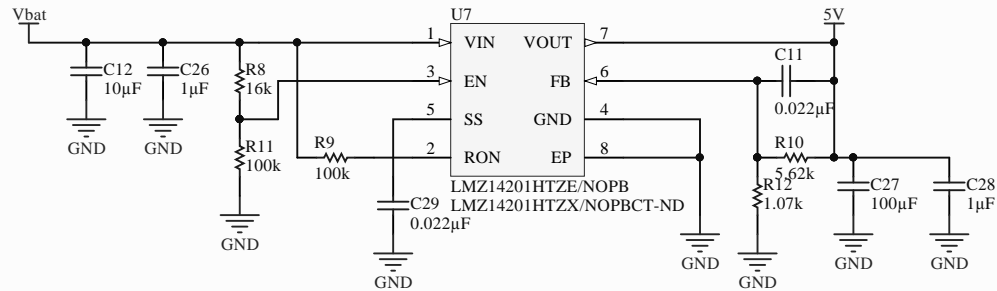
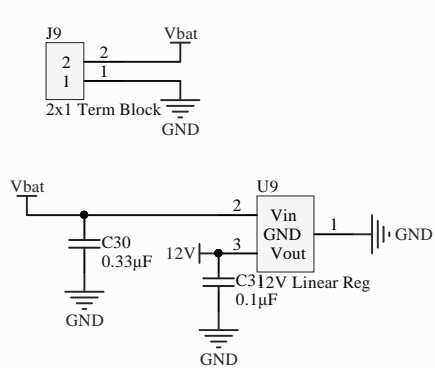




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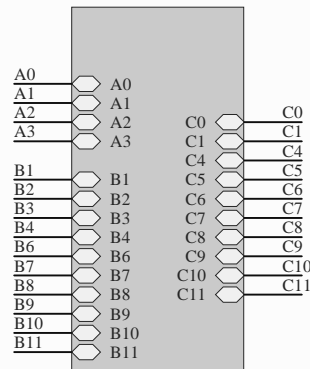


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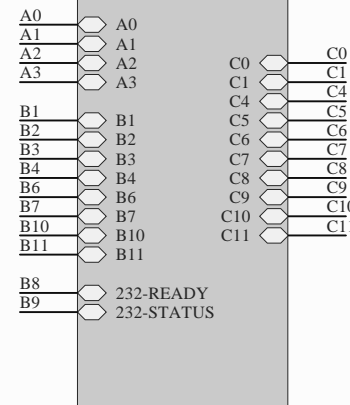
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MPU and Power  
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Interfaces  
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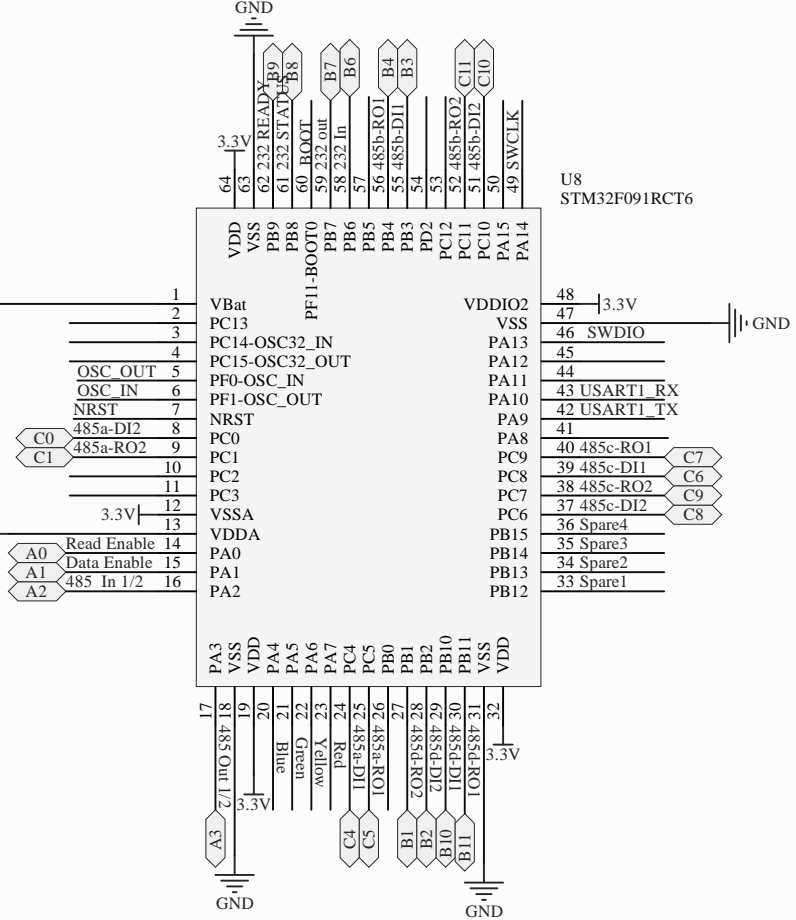
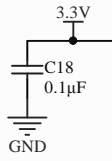
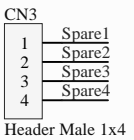
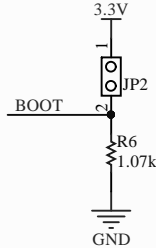
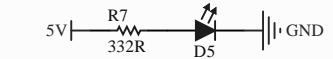
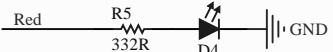
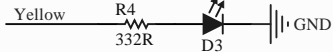
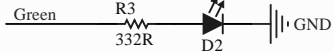
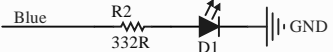
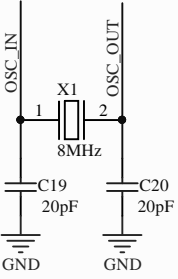
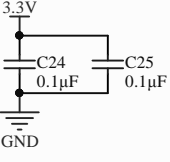
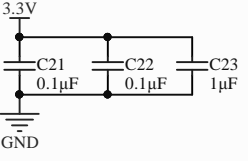
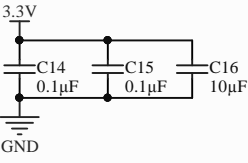
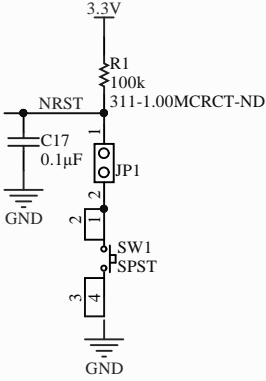
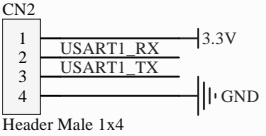
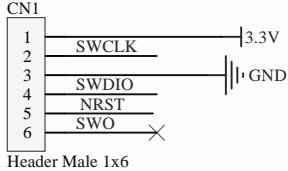
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